

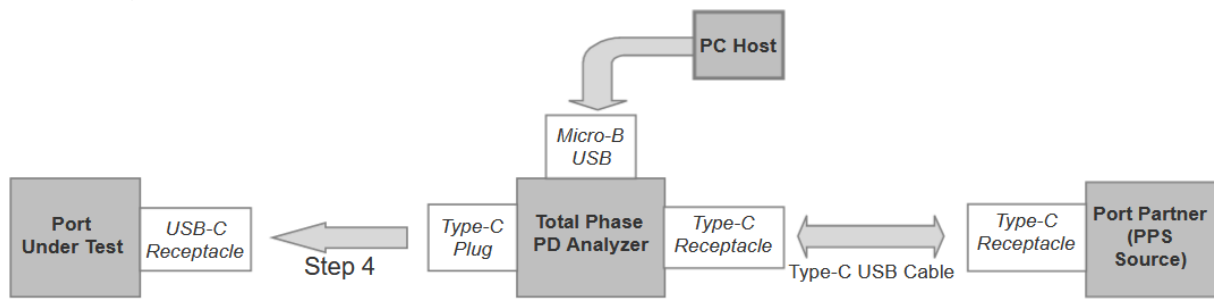
# SINK PPS Interoperability Charging Cycle Test Procedure

Rev 0.82

## Test Equipment:

- 1 USB Power Delivery Analyzer (Part No. TP350110) Sold by Total Phase
- 1 Micro-B USB cable
- 1 Host or Test Computer
- 1 Type-C Cable
- KGSrcPPS "Known Good Source PPS" (listed in the table 1)

## Test Setup:



## Procedure:

- 1) Verify PUT (Port Under Test) has empty battery (empty enough that product under test does not turn on or function)
- 2) Verify Port Partner (PPS Source) is connected to power
- 3) Open Total Phase PD Analyzer software and start capture
- 4) Plug in Total Phase PD Analyzer to Product Under Test
- 5) Verify Total Phase PD Analyzer SW is capturing data
- 6) Wait and check up on capture until PUT battery is fully charged. This can be seen on Total Phase PD Analyzer current measurement graph in a subtle change of current to a flat line around or below 150 mA. This will often occur around 1.5 hr or 2 hr after start of test.
- 7) Stop capture in Total Phase PD Analyzer SW

8) Save capture file. Label Trace Filename Clearly indicating which Source PPS was used in the capture.

9) Repeat Steps 1-8 for each condition in the table below. Once done, email the traces to PIL for analysis.

Table 1

Source Vendor	#	APDO	PDP	PL Bit
Renesas	1 Renesas Port 1 on Board	20V	60W	0
Renesas	2 Renesas Port 2 on Board	5V	15W	0
Via Labs	3 VLI VP302-15W	5V	15W	0
Via Labs	4 VLI VP302-18W	5V, 9V	18W	0
Via Labs	5 VLI VP302- 27W	9V	27W	0
Via Labs	6 VLI VP302- 27W	9V	27W	1
Samsung	7 Samsung 25W	5V, 9V	25W	1
Samsung	8 Samsung 45W	9V, 15V, 20V	45W	1

**Note:** Please contact [usbcompliance@usb.org](mailto:usbcompliance@usb.org) for KGSrcPPS Ordering Info.