



OMPL2001 Result

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Colfax International Supermicro X7QC3

SPECompLpeak2001 = --
SPECompLbase2001 = 82487

SPEC license #HPG0017 | Tested by: Advanced Micro Devices | Test site: Austin, TX | Test date: May-2008 | Hardware AvailNov-2007 | Software AvailNov-2007

Benchmark	Reference Time	Base Runtime	Base Ratio	Peak Runtime	Peak Ratio
311.wupwise_1	9200	1487	98971		
313.swim_1	12500	4789	41759		
315.mgrid_1	13500	4186	51599		
317.applu_1	13500	4454	48490		
321.quake_1	13000	3173	65563		
325.apsi_1	10500	1734	96881		
327.gafort_1	11000	1671	105328		
329.fma3d_1	23500	3478	108118		
331.art_1	25000	1692	236350		

Hardware

CPU: Intel(R) Xeon(R) X7350
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 16 cores, 4 chips, 4 cores/chip
 CPU(s) orderable: 1-4
 Primary Cache: 32 KB I + 32 KB D per core on chip
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: N/A
 Memory: 32GB (16 x 2GB PC2-5300 FBDIMM, CL5)
 Disk Subsystem: 120GB 7200 RPM SATA
 Other Hardware:

Software

OpenMP Threads: 16
 Parallel: OpenMP
 Operating System: SLES10 SP1 (64bit)
 Compiler: Intel FORTRAN Compiler 10.1.008 for Linux
 Intel C++ Compiler 10.1.008 for Linux
 GNU C Compiler (4.1.0 (SUSE Linux))
 File System: Linux ext3
 System State: Default

Notes/Tuning Information

Baseline optimization flags:

```
C : -mmodel=medium -ipo -i-dynamic -O3 -no-prec-div -fno-alias -xT -openmp -openmp-lib compat
Fortran : -mmodel=medium -ipo -i-dynamic -O3 -no-prec-div -fno-alias -xT -openmp -openmp-lib compat
ONESTEP=yes
```

Extra Flags:

```
331.art_1: -DINTS_PER_CACHELINE=32 -DDBLS_PER_CACHELINE=16
all: -gcc-name=/usr/bin/gcc
```

User Environment (bash):

```
export KMP_LIBRARY=turnaround
export KMP_STACKSIZE=31M
export OMP_DYNAMIC=FALSE
export KMP_BLOCKTIME=infinite
ulimit -s unlimited
export LD_LIBRARY_PATH=/opt/intel/fce/10.1.008/lib:/opt/intel/cce/10.1.008/lib
```

For flag descriptions, see file AMD-ic10.1-intel64-linux-flags-file-20080506.html
 BIOS version 8.00.15. Default BIOS settings used.