



SPEChpc™ 2021 Tiny Result

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xFusion

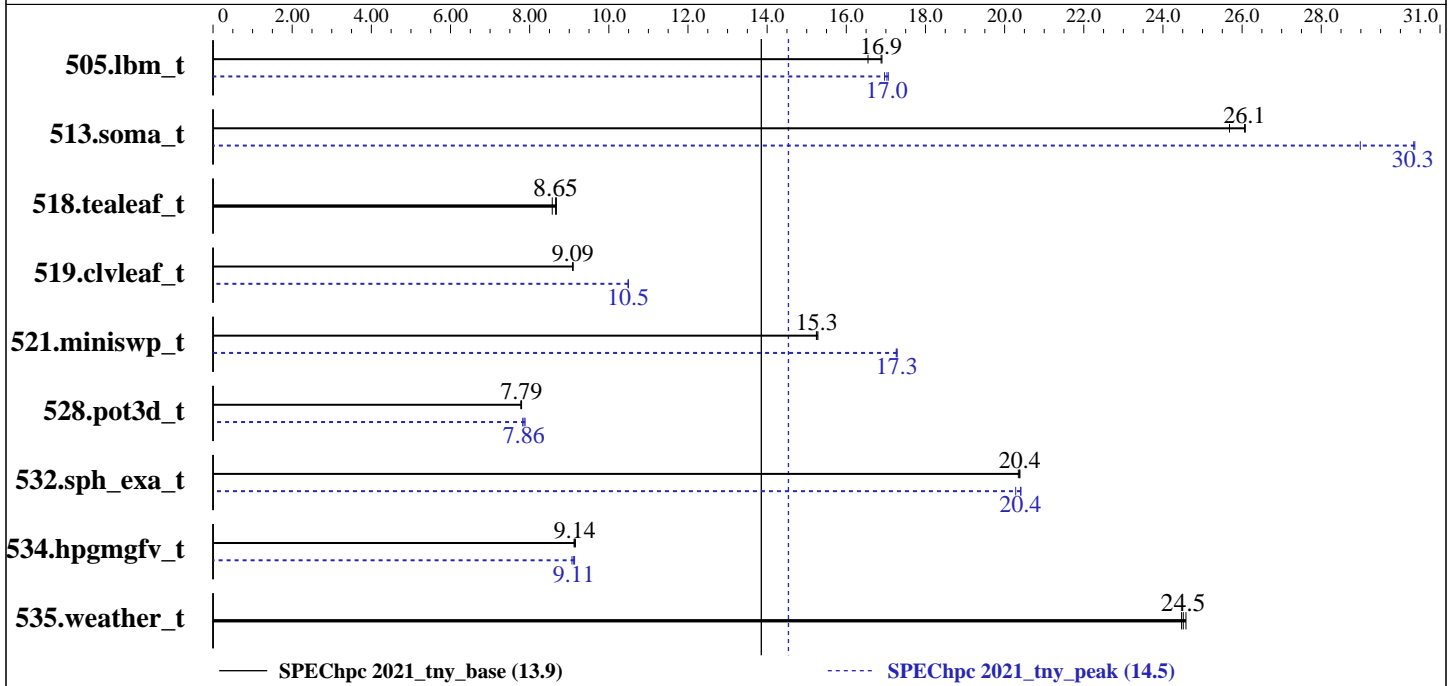
SPEChpc 2021_tny_base = 13.9

FusionServer 2288H V6 (Intel Xeon Platinum 8380)

SPEChpc 2021_tny_peak = 14.5

hpc2021 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jul-2022
Hardware Availability: Apr-2021
Software Availability: Oct-2021



Results Table

Benchmark	Base								Peak									
	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
505.lbm_t	OMP	48	10	136	16.5	133	16.9	133	16.9	OMP	24	20	133	17.0	132	17.0	132	17.1
513.soma_t	OMP	48	10	144	25.7	142	26.1	142	26.1	OMP	6	80	122	30.4	128	29.0	122	30.3
518.tealeaf_t	OMP	48	10	190	8.68	191	8.65	193	8.57	OMP	48	10	190	8.68	191	8.65	193	8.57
519.civleaf_t	OMP	48	10	182	9.08	182	9.09	181	9.10	OMP	24	20	157	10.5	157	10.5	157	10.5
521.miniswp_t	OMP	48	10	105	15.2	105	15.3	105	15.3	OMP	24	20	92.6	17.3	92.6	17.3	92.7	17.3
528.pot3d_t	OMP	48	10	273	7.79	273	7.80	273	7.78	OMP	6	80	272	7.82	270	7.86	270	7.88
532.sph_exa_t	OMP	48	10	95.8	20.3	95.7	20.4	95.7	20.4	OMP	48	10	95.5	20.4	96.2	20.3	95.6	20.4
534.hpgmgfv_t	OMP	48	10	129	9.12	129	9.14	128	9.16	OMP	48	10	129	9.14	130	9.07	129	9.11
535.weather_t	OMP	48	10	131	24.6	132	24.5	132	24.5	OMP	48	10	131	24.6	132	24.5	132	24.5

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.



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Hardware Summary

Type of System: Homogeneous Cluster
Compute Node: FusionServer 2288H V6
Interconnect: Mellanox HDR
Compute Nodes Used: 3
Total Chips: 6
Total Cores: 240
Total Threads: 480
Total Memory: 768 GB
Max. Peak Threads: 80

Software Summary

Compiler: Intel oneAPI Compiler 2021.4.0
MPI Library: Intel MPI Library for Linux* OS, Version 2021.4.0 Build 20210831
Other MPI Info: --
Other Software: --
Base Parallel Model: OMP
Base Ranks Run: 48
Base Threads Run: 10
Peak Parallel Models: OMP
Minimum Peak Ranks: 6
Maximum Peak Ranks: 48
Max. Peak Threads: 80
Min. Peak Threads: 10

Node Description: FusionServer 2288H V6

Hardware

Number of nodes: 3
Uses of the node: Compute
Vendor: xFusion
Model: FusionServer 2288H V6
CPU Name: Intel Xeon Platinum 8380
CPU(s) orderable: 1, 2 chips
Chips enabled: 2
Cores enabled: 80
Cores per chip: 40
Threads per core: 2
CPU Characteristics: Turbo Boost Technology up to 3.4 GHz
CPU MHz: 2300
Primary Cache: 32 KB I + 48 KB D on chip per core
Secondary Cache: 1.25 MB I+D on chip per core
L3 Cache: 60 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx8 PC4-3200R)
Disk Subsystem: 2 x 480 GB SATA 2.5" SSD (RAID 1)
Other Hardware: None
Accel Count: None
Accel Model: --
Accel Vendor: None
Accel Type: None
Accel Connection: None
Accel ECC enabled: None
Accel Description: None
Adapter: MCX653105A-EFAT
Number of Adapters: 1
Slot Type: PCI-Express 4.0 x16
Data Rate: 100 Gb/s
Ports Used: 1

Software

Accelerator Driver: --
Adapter: MCX653105A-EFAT
Adapter Driver: 5.4-3.1.0
Adapter Firmware: 20.32.1010
Operating System: CentOS Linux release 8.2.2004
4.18.0-193.el8.x86_644
Local File System: xfs
Shared File System: NFS
System State: Multi-user, run level 3
Other Software: None

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Node Description: FusionServer 2288H V6

Hardware (Continued)

Interconnect Type: Mellanox HDR

Interconnect Description: Mellanox HDR

Hardware

Vendor: Mellanox
Model: Mellanox HDR
Switch Model: Mellanox MQM8790-HS2F
InfiniBand Switch
Number of Switches: 1
Number of Ports: 40
Data Rate: 200 Gbit/s
Firmware: --
Topology: Mesh
Primary Use: MPI Traffic

Software

: --

Submit Notes

The config file option 'submit' was used.
export LD_PRELOAD="/usr/lib64/libhugetlbfs.so \$LD_PRELOAD"
export OMP_PROC_BIND=true
mpirun.hydra -bootstrap ssh -hostfile \${top}/3node --bind-to core -np \$ranks -ppn \$ppn -genv OMP_NUM_THREADS=\$threads \$command

Compiler Version Notes

=====
CC 505.lbm_t(base, peak) 513.soma_t(base, peak) 518.tealeaf_t(base, peak)
521.miniswp_t(base, peak) 534.hpgmgfv_t(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler 2021.4.0 (2021.4.0.20210924)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /home/opt/compiler/oneapi/2021.4.0/compiler/2021.4.0/linux/bin

=====
CXXC 532.sph_exa_t(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler 2021.4.0 (2021.4.0.20210924)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /home/opt/compiler/oneapi/2021.4.0/compiler/2021.4.0/linux/bin

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Compiler Version Notes (Continued)

=====

```
FC 519.clvleaf_t(base, peak) 528.pot3d_t(base, peak) 535.weather_t(base,
peak)
```

```
ifx (IFORT) 2021.4.0 Beta 20210924
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.
```

Base Compiler Invocation

C benchmarks:

```
mpiicc -cc=icx -lstdc++(*)
```

C++ benchmarks:

```
mpiicpc -cxx=icx -lstdc++(*)
```

Fortran benchmarks:

```
mpiifort -fc=ifx -lstdc++(*)
```

(*) Indicates a compiler flag that was found in a non-compiler variable.

Base Portability Flags

```
513.soma_t: -DSPEC_NO_VAR_ARRAY_REDUCE
```

Base Optimization Flags

C benchmarks:

```
-Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512 -fiopenmp
-ansi-alias
```

C++ benchmarks:

```
-Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512 -fiopenmp
-ansi-alias
```

Fortran benchmarks:

```
-Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512 -fiopenmp
-nostandard-realloc-lhs -align array64byte
```



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Peak Compiler Invocation

C benchmarks:

```
mpiicc -cc=icx -lstdc++(*)
```

C++ benchmarks:

```
mpiicpc -cxx=icx -lstdc++(*)
```

Fortran benchmarks:

```
mpiifort -fc=ifx -lstdc++(*)
```

(*) Indicates a compiler flag that was found in a non-compiler variable.

Peak Portability Flags

```
513.soma_t: -DSPEC_NO_VAR_ARRAY_REDUCE
```

Peak Optimization Flags

C benchmarks:

```
505.lbm_t: -Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512  
-fiopenmp -ansi-alias
```

```
513.soma_t: Same as 505.lbm_t
```

```
518.tealeaf_t: basepeak = yes
```

```
521.miniswp_t: Same as 505.lbm_t
```

```
534.hpgmgfv_t: -Ofast -ipo -fiopenmp -ansi-alias
```

C++ benchmarks:

```
-Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512 -ffast-math  
-fiopenmp -ansi-alias
```

Fortran benchmarks:

```
519.clvleaf_t: -Ofast -ipo -xCORE-AVX512  
-mllvm -hir-nontemporal-cacheline-count=0 -fiopenmp  
-nostandard-realloc-lhs -align array64byte
```

```
528.pot3d_t: -Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512  
-fiopenmp -nostandard-realloc-lhs -align array64byte
```

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Peak Optimization Flags (Continued)

535.weather_t: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/hpc2021/flags/Intel-oneAPI-icx2021-official-linux64.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/hpc2021/flags/Intel-oneAPI-icx2021-official-linux64.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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