



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 439

H3C UniServer R4700 G5 (Intel Xeon Gold 6338)

SPECrate®2017_fp_peak = 443

CPU2017 License: 9066

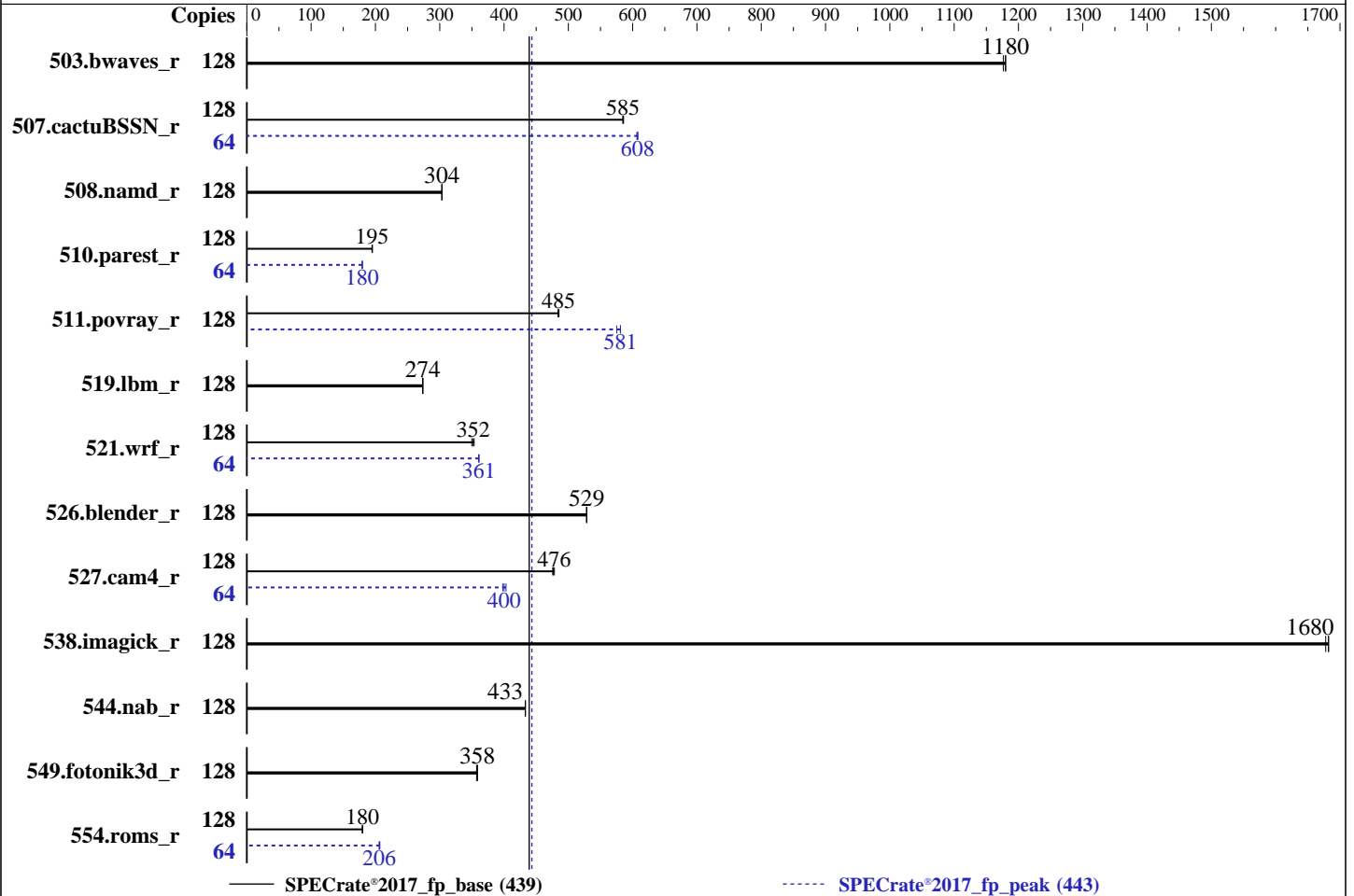
Test Date: Aug-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Jun-2021

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Mar-2024



Hardware

CPU Name: Intel Xeon Gold 6338
 Max MHz: 3200
 Nominal: 2000
 Enabled: 64 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1.25 MB I+D on chip per core
 L3: 48 MB I+D on chip per chip
 Other: None
 Memory: 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R)
 Storage: 1 x 3.2 TB NVME SSD
 Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise High Performance Computing 15 SP5 5.14.21-150500.53-default
 Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
 Parallel: No
 Firmware: Version 5.71 released Jan-2024 BIOS
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 439

H3C UniServer R4700 G5 (Intel Xeon Gold 6338)

SPECrate®2017_fp_peak = 443

CPU2017 License: 9066

Test Date: Aug-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Jun-2021

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Mar-2024

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	128	1088	1180	1091	1180	1088	1180	128	1088	1180	1091	1180	1088	1180
507.cactuBSSN_r	128	277	585	277	585	277	586	64	133	608	133	608	134	607
508.namd_r	128	401	303	401	304	400	304	128	401	303	401	304	400	304
510.parest_r	128	1720	195	1714	195	1718	195	64	933	179	933	180	932	180
511.povray_r	128	616	485	617	485	618	484	128	514	581	520	575	514	581
519.lbm_r	128	493	274	493	274	493	273	128	493	274	493	274	493	273
521.wrf_r	128	815	352	811	354	820	350	64	397	361	398	360	397	361
526.blender_r	128	369	528	369	529	369	529	128	369	528	369	529	369	529
527.cam4_r	128	471	476	468	478	470	476	64	278	403	281	398	280	400
538.imagick_r	128	189	1680	189	1680	190	1680	128	189	1680	189	1680	190	1680
544.nab_r	128	498	433	497	433	497	433	128	498	433	497	433	497	433
549.fotonik3d_r	128	1396	357	1392	358	1391	359	128	1396	357	1392	358	1391	359
554.roms_r	128	1130	180	1129	180	1133	180	64	493	206	494	206	492	207

SPECrate®2017_fp_base = **439**

SPECrate®2017_fp_peak = **443**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/speccpunew/lib/intel64:/home/speccpunew/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 439

H3C UniServer R4700 G5 (Intel Xeon Gold 6338)

SPECrate®2017_fp_peak = 443

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2024

Hardware Availability: Jun-2021

Software Availability: Mar-2024

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Settings:

SNC (Sub NUMA) = Enabled

Power Performance Tuning = BIOS Controls EPB

Energy Performance BIAS = Performance

Sysinfo program /home/specpunew/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Thu Aug 22 05:20:38 2024

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent_hugepage
19. /sys/kernel/mm/transparent_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS

1. uname -a
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043/lp)
x86_64 x86_64 x86_64 GNU/Linux

2. w
05:20:38 up 4:35, 1 user, load average: 90.39, 118.64, 123.56
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 439

H3C UniServer R4700 G5 (Intel Xeon Gold 6338)

SPECrate®2017_fp_peak = 443

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2024

Hardware Availability: Jun-2021

Software Availability: Mar-2024

Platform Notes (Continued)

```
root      tty1      -                00:53      4:08m  2.08s  0.01s  sh 66.sh
```

3. Username

From environment variable \$USER: root

4. ulimit -a

```

core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 2060268
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 2060268
virtual memory          (kbytes, -v) unlimited
file locks               (-x) unlimited

```

5. sysinfo process ancestry

```

/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
sh 66.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 -c
ic2024.1-lin-core-avx512-rate-20231213.cfg --define smt-on --define cores=64 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 --configfile
ic2024.1-lin-core-avx512-rate-20231213.cfg --define smt-on --define cores=64 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templots/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/specpunew

```

6. /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) Gold 6338 CPU @ 2.00GHz
vendor_id      : GenuineIntel
cpu family     : 6
model          : 106
stepping      : 6
microcode     : 0xd0003d1
bugs          : spectre_v1 spectre_v2 spec_store_bypass swapgs mmio_stale_data eibrs_pbrsb
cpu cores     : 32
siblings      : 64
2 physical ids (chips)
128 processors (hardware threads)
physical id 0: core ids 0-31
physical id 1: core ids 0-31
physical id 0: apicids 0-63
physical id 1: apicids 128-191

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 439

H3C UniServer R4700 G5 (Intel Xeon Gold 6338)

SPECrate®2017_fp_peak = 443

CPU2017 License: 9066

Test Date: Aug-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Jun-2021

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Mar-2024

Platform Notes (Continued)

virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                128
On-line CPU(s) list:   0-127
Vendor ID:             GenuineIntel
Model name:            Intel(R) Xeon(R) Gold 6338 CPU @ 2.00GHz
CPU family:            6
Model:                 106
Thread(s) per core:   2
Core(s) per socket:   32
Socket(s):             2
Stepping:              6
CPU max MHz:           3200.0000
CPU min MHz:           800.0000
BogoMIPS:              4000.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx
                        est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe
                        popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                        3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_ppin ssbd mba
                        ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad
                        fsgsbase tsc_adjust bmil hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a
                        avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt
                        avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc
                        cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect wbnoinvd
                        dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi
                        umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
                        tme avx512_vpopcntdq la57 rdpid fsrm md_clear pconfig flush_l1d
                        arch_capabilities

Virtualization:        VT-x
L1d cache:             3 MiB (64 instances)
L1i cache:             2 MiB (64 instances)
L2 cache:              80 MiB (64 instances)
L3 cache:              96 MiB (2 instances)
NUMA node(s):         4
NUMA node0 CPU(s):    0-15,64-79
NUMA node1 CPU(s):    16-31,80-95
NUMA node2 CPU(s):    32-47,96-111
NUMA node3 CPU(s):    48-63,112-127
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:    Not affected
Vulnerability Mds:     Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Mitigation; Clear CPU buffers; SMT vulnerable
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW
                        sequence
Vulnerability Srbds:   Not affected

```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 439

H3C UniServer R4700 G5 (Intel Xeon Gold 6338)

SPECrate®2017_fp_peak = 443

CPU2017 License: 9066

Test Date: Aug-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Jun-2021

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Mar-2024

Platform Notes (Continued)

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	3M	12	Data	1	64	1	64
L1i	32K	2M	8	Instruction	1	64	1	64
L2	1.3M	80M	20	Unified	2	1024	1	64
L3	48M	96M	12	Unified	3	65536	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0-15,64-79
node 0 size: 128526 MB
node 0 free: 119406 MB
node 1 cpus: 16-31,80-95
node 1 size: 129015 MB
node 1 free: 121824 MB
node 2 cpus: 32-47,96-111
node 2 size: 129015 MB
node 2 free: 121004 MB
node 3 cpus: 48-63,112-127
node 3 size: 128539 MB
node 3 free: 121583 MB
node distances:
node  0  1  2  3
0:  10  11  20  20
1:  11  10  20  20
2:  20  20  10  11
3:  20  20  11  10

```

9. /proc/meminfo

MemTotal: 527460384 kB

10. who -r

run-level 3 Aug 22 00:46

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

```

Default Target Status
multi-user      degraded

```

12. Failed units, from systemctl list-units --state=failed

```

UNIT          LOAD  ACTIVE SUB    DESCRIPTION
* sep5.service loaded failed failed systemd script to load sep5 driver at boot time

```

13. Services, from systemctl list-unit-files

```

STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance
issue-generator kbdsettings kdump kdump-early klog lvm2-monitor nscd
nvme-fc-boot-connections postfix purge-kernels rollback rsyslog sep5 smartd sshd
systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronyd console-getty cups cups-browsed debug-shell dmraid-activation ebttables

```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 439

H3C UniServer R4700 G5 (Intel Xeon Gold 6338)

SPECrate®2017_fp_peak = 443

CPU2017 License: 9066

Test Date: Aug-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Jun-2021

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Mar-2024

Platform Notes (Continued)

```
exchange-bmc-os-info firewallld gpm grub2-once haveged haveged-switch-root ipmi ipmiev
irqbindall issue-add-ssh-keys kexec-load ksm kvm_stat lunmask man-db-create multipathd
nfs blkmap nvme-autoconnect rpcbind rpmconfigcheck rsyncd serial-getty@ set_kthread_prio
smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures
systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned
udisks2 vncserver@
wickedd
```

indirect

```
-----
14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=4607dbel-932d-4bea-b43c-6701598385ba
splash=silent
resume=/dev/disk/by-uuid/2ccfd1c4-5b7d-4cdb-9370-aca95a4f79b6
mitigations=auto
quiet
security=apparmor
crashkernel=403M,high
crashkernel=72M,low
```

```
-----
15. cpupower frequency-info
analyzing CPU 0:
current policy: frequency should be within 800 MHz and 3.20 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes
```

```
-----
16. tuned-adm active
Current active profile: throughput-performance
```

```
-----
17. sysctl
kernel.numa_balancing          0
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                  20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   10
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0
```

```
-----
18. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled        [always] madvice never
```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 439

H3C UniServer R4700 G5 (Intel Xeon Gold 6338)

SPECrate®2017_fp_peak = 443

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2024

Hardware Availability: Jun-2021

Software Availability: Mar-2024

Platform Notes (Continued)

hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

19. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000

20. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise High Performance Computing 15 SP5

21. Disk information
SPEC is set to: /home/speccpunew
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p3 xfs 1.0T 396G 629G 39% /home

22. /sys/devices/virtual/dmi/id
Vendor: New H3C Technologies Co., Ltd.
Product: H3C UniServer R4700 G5
Product Family: Rack
Serial: 210235A2PM522CC000LQ

23. dmidecode
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
16x Micron 36ASF4G72PZ-3G2R1 32 GB 2 rank 3200

24. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 5.71
BIOS Date: 01/04/2024
BIOS Revision: 5.22

Compiler Version Notes

=====
C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 439

H3C UniServer R4700 G5 (Intel Xeon Gold 6338)

SPECrate®2017_fp_peak = 443

CPU2017 License: 9066

Test Date: Aug-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Jun-2021

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Mar-2024

Compiler Version Notes (Continued)

C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++, C, Fortran | 507.cactuBSSN_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 439

H3C UniServer R4700 G5 (Intel Xeon Gold 6338)

SPECrate®2017_fp_peak = 443

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2024

Hardware Availability: Jun-2021

Software Availability: Mar-2024

Base Compiler Invocation (Continued)

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 439

H3C UniServer R4700 G5 (Intel Xeon Gold 6338)

SPECrate®2017_fp_peak = 443

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2024

Hardware Availability: Jun-2021

Software Availability: Mar-2024

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 439

H3C UniServer R4700 G5 (Intel Xeon Gold 6338)

SPECrate®2017_fp_peak = 443

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2024

Hardware Availability: Jun-2021

Software Availability: Mar-2024

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1) -fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1) -flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

526.blender_r: basepeak = yes

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 439

H3C UniServer R4700 G5 (Intel Xeon Gold 6338)

SPECrate®2017_fp_peak = 443

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2024

Hardware Availability: Jun-2021

Software Availability: Mar-2024

Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -fltto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-CPX-RevD.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-CPX-RevD.xml

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2024-08-21 17:20:37-0400.

Report generated on 2024-09-11 09:37:24 by CPU2017 PDF formatter v6716.

Originally published on 2024-09-10.