SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11
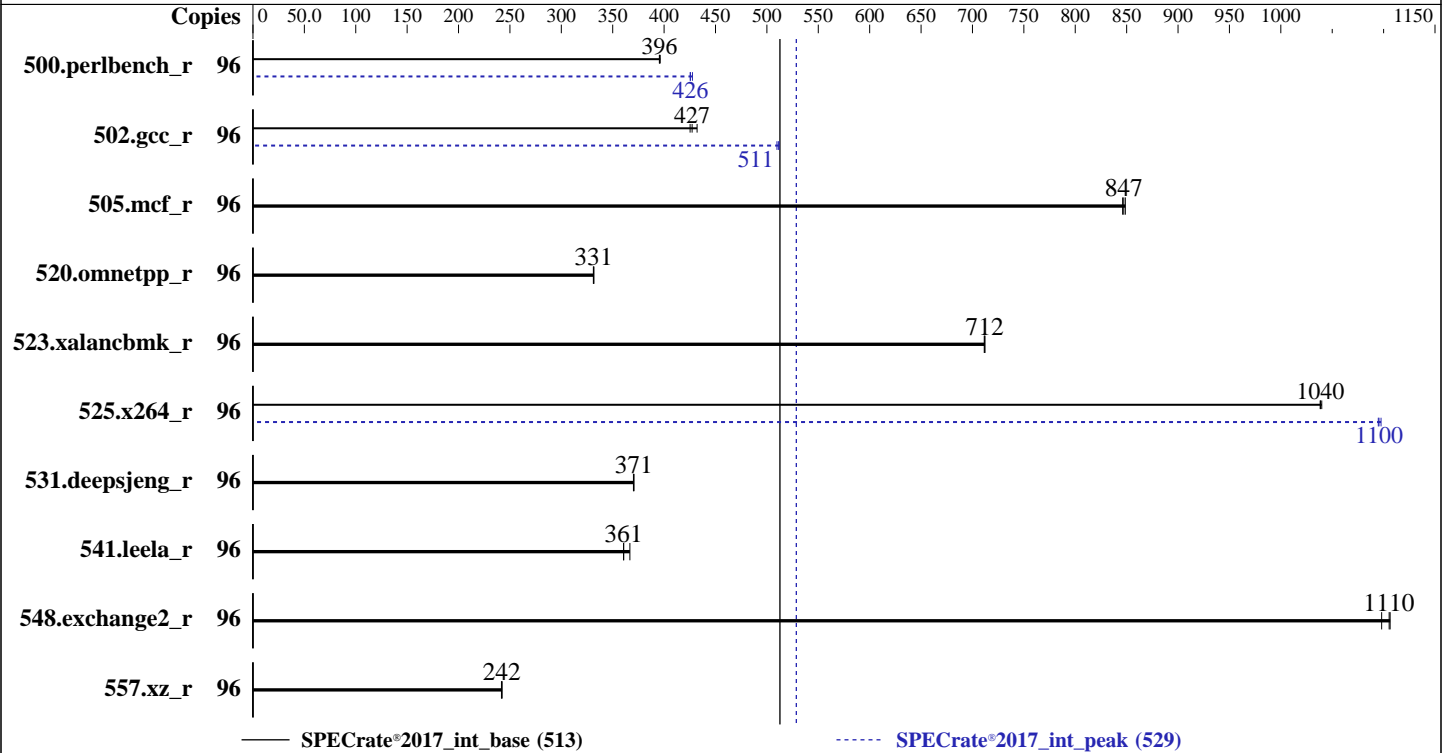(2.90 GHz, Intel Xeon Gold 6542Y)

$SPECrate®2017\_int\_base = 513$

$SPECrate®2017\_int\_peak = 529$

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

Copies	Benchmark	base	peak
500.perlbench_r	96	396	426
502.gcc_r	96	427	511
505.mcf_r	96	847	
520.omnetpp_r	96	331	
523.xalancbmk_r	96	712	
525.x264_r	96	1040	1100
531.deepsjeng_r	96	371	
541.leela_r	96	361	
548.exchange2_r	96	1110	
557.xz_r	96	242	

SPECrate®2017_int_base (513)   SPECrate®2017_int_peak (529)

Hardware

CPU Name: Intel Xeon Gold 6542Y
 Max MHz: 4100
 Nominal: 2900
Enabled: 64 cores, 2 chips, 2 threads/core
Orderable: 1, 2 chip(s)
Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 60 MB I+D on chip per chip
 Other: None
Memory: 512 GB (16 x 32 GB 2Rx8 PC5-5600B-R, running at 5200)
Storage: 1 x 480 GB SATA SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP5
 Kernel 5.14.21-150500.53-default
Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
Parallel: No
Firmware: HPE BIOS Version v2.12 12/13/2023 released Dec-2023
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage

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Results Table

Benchmark	Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
500.perlbench_r	96	**386**	**396**	387	395	386	396	96	359	425	**359**	**426**	357	428
502.gcc_r	96	315	432	320	425	**318**	**427**	96	266	512	267	510	**266**	**511**
505.mcf_r	96	183	849	**183**	**847**	183	846	96	183	849	**183**	**847**	183	846
520.omnetpp_r	96	380	332	**380**	**331**	380	331	96	380	332	**380**	**331**	380	331
523.xalancbmk_r	96	**142**	**712**	142	712	142	711	96	**142**	**712**	142	712	142	711
525.x264_r	96	162	1040	**162**	**1040**	162	1040	96	154	1090	153	1100	**153**	**1100**
531.deepsjeng_r	96	297	371	297	370	**297**	**371**	96	297	371	297	370	**297**	**371**
541.leela_r	96	**441**	**361**	433	367	441	361	96	**441**	**361**	433	367	441	361
548.exchange2_r	96	227	1110	229	1100	**228**	**1110**	96	227	1110	229	1100	**228**	**1110**
557.xz_r	96	429	242	**428**	**242**	428	242	96	429	242	**428**	**242**	428	242
SPECrate®2017_int_base =				**513**										
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>   /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
tuned-adm profile was set to Throughput-Performance using "tuned-adm profile throughput-performance"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

(Continued on next page)

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General Notes (Continued)

```
is mitigated in the system as tested and documented.
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
The system ROM used for this result contains Intel microcode version 0x21000200 for
the Intel Xeon Gold 6542Y processor.
BIOS Configuration:
 Workload Profile set to General Throughput Compute
 Thermal Configuration set to Maximum Cooling
 Memory Patrol Scrubbing set to Disabled
 Enhanced Processor Performance Profile set to Aggressive
 Dead Block Predictor set to Enabled
 Intel UPI Link Enablement set to Single Link
 Last Level Cache (LLC) Dead Line Allocation set to Disabled
 Sub-NUMA Clustering set to Enable SNC2(2-clusters)
 Workload Profile set to Custom
  Adjacent Sector Prefetch set to Disabled
  DCU Stream Prefetcher set to Disabled
  Intel UPI Link Power Management set to Enabled

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost Mon Jan 29 03:28:14 2024

 SUT (System Under Test) info as seen by some common utilities.

 -----------------------------------------------------------
 Table of contents
 -----------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. tuned-adm active
 16. sysctl
 17. /sys/kernel/mm/transparent_hugepage
 18. /sys/kernel/mm/transparent_hugepage/khugepaged
 19. OS release
 20. Disk information
 21. /sys/devices/virtual/dmi/id
 22. dmidecode
 23. BIOS
 -----------------------------------------------------------

 -----------------------------------------------------------
  1. uname -a
```

(Continued on next page)

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Platform Notes (Continued)

```
  Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
  x86_64 x86_64 x86_64 GNU/Linux


----------------------------------------------------------
2. w
   03:28:14 up 0 min,  0 users,  load average: 0.19, 0.06, 0.02
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT

----------------------------------------------------------
3. Username
  From environment variable $USER:  root


----------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 2062744
  max locked memory       (kbytes, -l) 64
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 2062744
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited


----------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize 29
 sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
 sshd: root@notty
 bash -c cd $SPEC/ && $SPEC/intrate_new.sh
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=96 -c
   ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=48 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=96 --configfile
   ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=48 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
   --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
 $SPEC = /home/cpu2017


----------------------------------------------------------
6. /proc/cpuinfo
    model name      : INTEL(R) XEON(R) GOLD 6542Y
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 207
    stepping        : 2
    microcode       : 0x21000200
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
    cpu cores       : 24
    siblings        : 48
    2 physical ids (chips)
```

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Platform Notes (Continued)

```
   96 processors (hardware threads)
   physical id 0: core ids 0-23
   physical id 1: core ids 0-23
   physical id 0: apicids 0-47
   physical id 1: apicids 128-175
 Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
 virtualized systems.  Use the above data carefully.


  -----------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.4:
  Architecture:                 x86_64
  CPU op-mode(s):               32-bit, 64-bit
  Address sizes:                46 bits physical, 57 bits virtual
  Byte Order:                   Little Endian
  CPU(s):                       96
  On-line CPU(s) list:          0-95
  Vendor ID:                    GenuineIntel
  Model name:                   INTEL(R) XEON(R) GOLD 6542Y
  CPU family:                   6
  Model:                        207
  Thread(s) per core:           2
  Core(s) per socket:           24
  Socket(s):                    2
  Stepping:                     2
  BogoMIPS:                     5800.00
  Flags:                        fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                                lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                                nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                                ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                                sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                                lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                                invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                                vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep
                                bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                                avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                                xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hfi
                                avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                                avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                                cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                                arch_lbr avx512_fp16 amx_tile flush_l1d arch_capabilities
  Virtualization:               VT-x
  L1d cache:                    2.3 MiB (48 instances)
  L1i cache:                    1.5 MiB (48 instances)
  L2 cache:                     96 MiB (48 instances)
  L3 cache:                     120 MiB (2 instances)
  NUMA node(s):                 4
  NUMA node0 CPU(s):            0-11,48-59
  NUMA node1 CPU(s):            12-23,60-71
  NUMA node2 CPU(s):            24-35,72-83
  NUMA node3 CPU(s):            36-47,84-95
  Vulnerability Itlb multihit:  Not affected
  Vulnerability L1tf:           Not affected
  Vulnerability Mds:            Not affected
  Vulnerability Meltdown:       Not affected
  Vulnerability Mmio stale data: Not affected
  Vulnerability Retbleed:       Not affected
```

(Continued on next page)

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Platform Notes (Continued)

```
   Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
   Vulnerability Spectre v1:        Mitigation; usercopy/swapgs barriers and __user pointer sanitization
   Vulnerability Spectre v2:        Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW
                                    sequence
   Vulnerability Srbds:             Not affected
   Vulnerability Tsx async abort:   Not affected


 From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE         LEVEL  SETS PHY-LINE COHERENCY-SIZE
     L1d      48K     2.3M   12 Data             1    64       1             64
     L1i      32K     1.5M    8 Instruction      1    64       1             64
     L2        2M      96M   16 Unified          2  2048       1             64
     L3       60M     120M   15 Unified          3 65536       1             64


  ------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0-3)
  node 0 cpus: 0-11,48-59
  node 0 size: 128711 MB
  node 0 free: 128106 MB
  node 1 cpus: 12-23,60-71
  node 1 size: 128983 MB
  node 1 free: 128548 MB
  node 2 cpus: 24-35,72-83
  node 2 size: 129017 MB
  node 2 free: 128548 MB
  node 3 cpus: 36-47,84-95
  node 3 size: 128997 MB
  node 3 free: 128525 MB
  node distances:
  node   0   1   2   3
    0:  10  20  30  30
    1:  20  10  30  30
    2:  30  30  10  20
    3:  30  30  20  10


  ------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       528087052 kB


  ------------------------------------------------------------
10. who -r
   run-level 3 Jan 29 03:27


  ------------------------------------------------------------
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
   Default Target  Status
   multi-user      running


  ------------------------------------------------------------
12. Services, from systemctl list-unit-files
   STATE            UNIT FILES
   enabled          apparmor auditd cron getty@ irqbalance issue-generator kbdsettings lvm2-monitor postfix
                    purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4
                    wickedd-dhcp6 wickedd-nanny
   enabled-runtime  systemd-remount-fs
   disabled         blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell
                    grub2-once haveged haveged-switch-root hwloc-dump-hwdata issue-add-ssh-keys kexec-load
                    lunmask rpmconfigcheck serial-getty@ systemd-boot-check-no-failures
```

(Continued on next page)

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Platform Notes (Continued)

```
                     systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned
   indirect          pcscd wickedd


  ------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
   BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
   root=UUID=cc6cc077-0be5-45b2-9254-52358283bcfe
   splash=silent
   mitigations=auto
   quiet
   security=apparmor


  ------------------------------------------------------------
14. cpupower frequency-info
   analyzing CPU 0:
     Unable to determine current policy
     boost state support:
       Supported: yes
       Active: yes


  ------------------------------------------------------------
15. tuned-adm active
   Current active profile: throughput-performance


  ------------------------------------------------------------
16. sysctl
   kernel.numa_balancing            1
   kernel.randomize_va_space        2
   vm.compaction_proactiveness      20
   vm.dirty_background_bytes        0
   vm.dirty_background_ratio        10
   vm.dirty_bytes                   0
   vm.dirty_expire_centisecs        3000
   vm.dirty_ratio                   20
   vm.dirty_writeback_centisecs     500
   vm.dirtytime_expire_seconds      43200
   vm.extfrag_threshold             500
   vm.min_unmapped_ratio            1
   vm.nr_hugepages                  0
   vm.nr_hugepages_mempolicy        0
   vm.nr_overcommit_hugepages       0
   vm.swappiness                    10
   vm.watermark_boost_factor        15000
   vm.watermark_scale_factor        10
   vm.zone_reclaim_mode             0

  ------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage
   defrag           always defer defer+madvise [madvise] never
   enabled          [always] madvise never
   hpage_pmd_size   2097152
   shmem_enabled    always within_size advise [never] deny force


  ------------------------------------------------------------
18. /sys/kernel/mm/transparent_hugepage/khugepaged
   alloc_sleep_millisecs   60000
   defrag                  1
   max_ptes_none           511
   max_ptes_shared         256
   max_ptes_swap           64
```

(Continued on next page)

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Platform Notes (Continued)

```
     pages_to_scan           4096
     scan_sleep_millisecs   10000


   ----------------------------------------------------------
   19. OS release
     From /etc/*-release /etc/*-version
     os-release SUSE Linux Enterprise Server 15 SP5


   ----------------------------------------------------------
   20. Disk information
   SPEC is set to: /home/cpu2017
     Filesystem       Type   Size  Used Avail Use% Mounted on
     /dev/sda2        btrfs  371G  179G  191G  49% /home


   ----------------------------------------------------------
   21. /sys/devices/virtual/dmi/id
       Vendor:         HPE
       Product:        ProLiant DL360 Gen11
       Product Family: ProLiant
       Serial:         CNX2070DC4


   ----------------------------------------------------------
   22. dmidecode
     Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
     The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
     "DMTF SMBIOS" standard.
     Memory:
       16x Hynix HMCG88AGBRA193N 32 GB 2 rank 5600, configured at 5200


   ----------------------------------------------------------
   23. BIOS
   (This section combines info from /sys/devices and dmidecode.)
     BIOS Vendor:      HPE
     BIOS Version:     2.12
     BIOS Date:        12/13/2023
     BIOS Revision:    2.12
     Firmware Revision: 1.55
```

Compiler Version Notes

```
==============================================================================================
C       | 502.gcc_r(peak)
----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
        | 557.xz_r(base, peak)
----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
C       | 502.gcc_r(peak)
```

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Compiler Version Notes (Continued)

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--------------------------------------------------------------------------------
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Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------


================================================================================
C        | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
         | 557.xz_r(base, peak)
--------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------


================================================================================
C++      | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
         | 541.leela_r(base, peak)
--------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------


================================================================================
Fortran | 548.exchange2_r(base, peak)
--------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icx`

C++ benchmarks:
`icpx`

Fortran benchmarks:
`ifx`

Base Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`

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Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant DL360 Gen11
(2.90 GHz, Intel Xeon Gold 6542Y)

SPECrate®2017_int_base = 513

SPECrate®2017_int_peak = 529

CPU2017 License: 3	**Test Date:** Jan-2024
Test Sponsor: HPE	**Hardware Availability:** Feb-2024
Tested by: HPE	**Software Availability:** Dec-2023

Base Portability Flags (Continued)

541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

Peak Compiler Invocation

C benchmarks:
`icx`

C++ benchmarks:
`icpx`

Fortran benchmarks:
`ifx`

Peak Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`

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Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant DL360 Gen11
(2.90 GHz, Intel Xeon Gold 6542Y)

SPECrate®2017_int_base = 513

SPECrate®2017_int_peak = 529

CPU2017 License:	3	**Test Date:**	Jan-2024
Test Sponsor:	HPE	**Hardware Availability:**	Feb-2024
Tested by:	HPE	**Software Availability:**	Dec-2023

Peak Portability Flags (Continued)

502.gcc_r: `-D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Peak Optimization Flags

C benchmarks:

500.perlbench_r: `-w -std=c11 -m64 -Wl,-z,muldefs`
`-fprofile-generate`(pass 1)
`-fprofile-use=default.profdata`(pass 2) `-xCORE-AVX2`(pass 1)
`-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse`
`-funroll-loops -qopt-mem-layout-trans=4`
`-fno-strict-overflow`
`-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin`
`-lqkmalloc`

502.gcc_r: `-m32`
`-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/ia32_lin`
`-std=gnu89 -Wl,-z,muldefs -fprofile-generate`(pass 1)
`-fprofile-use=default.profdata`(pass 2) `-xCORE-AVX2`(pass 1)
`-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse`
`-funroll-loops -qopt-mem-layout-trans=4`
`-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc`

505.mcf_r: basepeak = yes

525.x264_r: `-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast`
`-ffast-math -flto -mfpmath=sse -funroll-loops`
`-qopt-mem-layout-trans=4 -fno-alias`
`-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin`
`-lqkmalloc`

557.xz_r: basepeak = yes

C++ benchmarks:

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Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant DL360 Gen11
(2.90 GHz, Intel Xeon Gold 6542Y)

SPECrate®2017_int_base = 513

SPECrate®2017_int_peak = 529

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jan-2024
Hardware Availability: Feb-2024
Software Availability: Dec-2023

Peak Optimization Flags (Continued)

520.omnetpp_r: `basepeak = yes`

523.xalancbmk_r: `basepeak = yes`

531.deepsjeng_r: `basepeak = yes`

541.leela_r: `basepeak = yes`

Fortran benchmarks:

548.exchange2_r: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.html`
`http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.xml`
`http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml`

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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