



# SPEC CPU®2017 Floating Point Rate Result

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## Supermicro

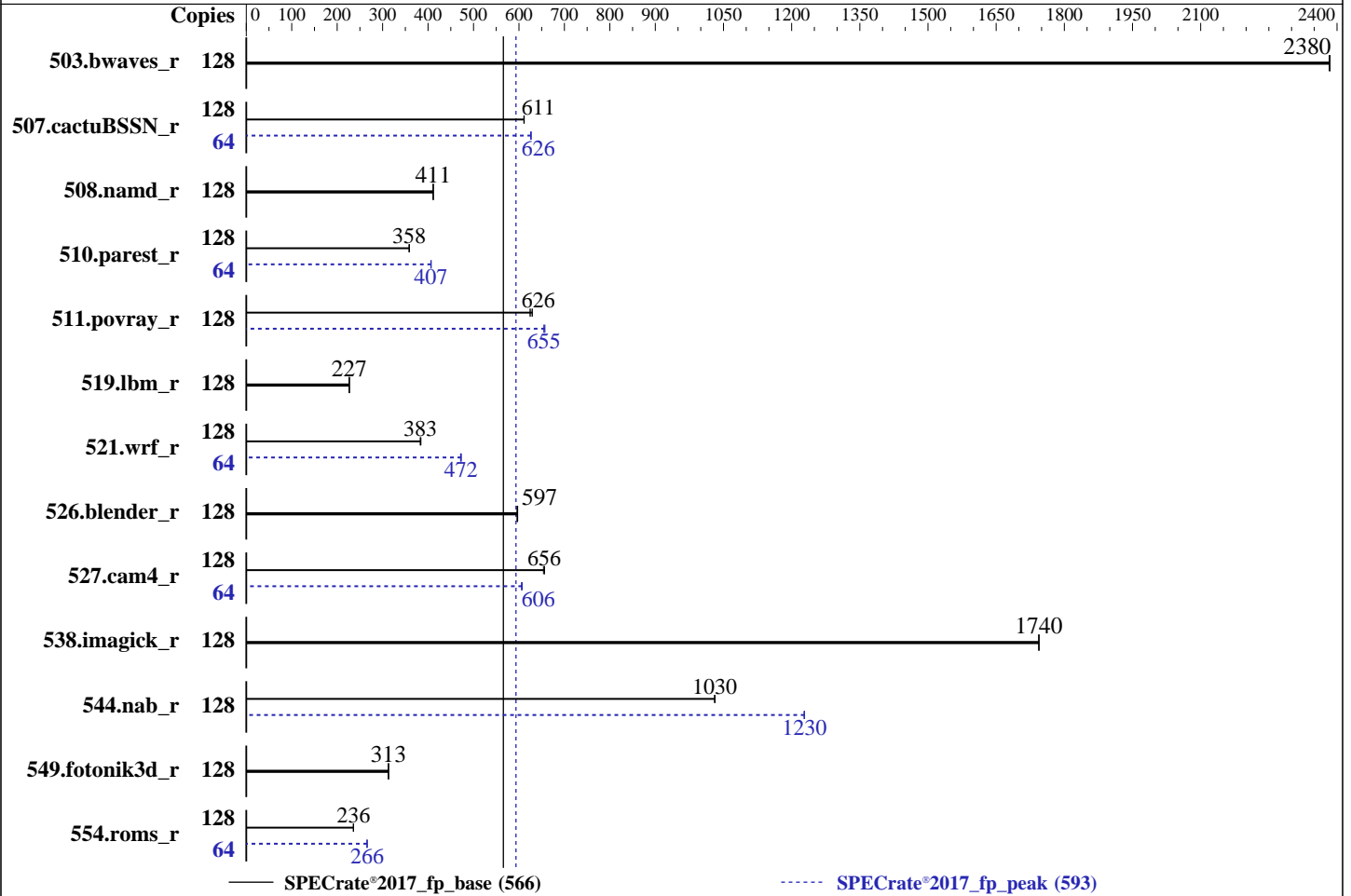
UP SuperServer SYS-521C-NR  
(X13SEDW-F, Intel Xeon Platinum 8592+)

SPECrate®2017\_fp\_base = 566

SPECrate®2017\_fp\_peak = 593

CPU2017 License: 001176  
Test Sponsor: Supermicro  
Tested by: Supermicro

Test Date: Nov-2023  
Hardware Availability: Dec-2023  
Software Availability: Jun-2023



### Hardware

CPU Name: Intel Xeon Platinum 8592+  
Max MHz: 3900  
Nominal: 1900  
Enabled: 64 cores, 1 chip, 2 threads/core  
Orderable: 1 chip  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 320 MB I+D on chip per chip  
Other: None  
Memory: 512 GB (8 x 64 GB 2Rx4 PC5-5600B-R)  
Storage: 1 x 240 GB SATA III SSD  
Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP5  
Kernel 5.14.21-150500.53-default  
Compiler: C/C++: Version 2023.2 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2023.2 of Intel Fortran Compiler for Linux;  
Parallel: No  
Firmware: Version 2.0 released Nov-2023  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	128	538	2380	<b><u>538</u></b>	<b><u>2380</u></b>	539	2380	128	538	2380	<b><u>538</u></b>	<b><u>2380</u></b>	539	2380
507.cactuBSSN_r	128	<b><u>265</u></b>	<b><u>611</u></b>	265	611	265	612	64	129	627	<b><u>129</u></b>	<b><u>626</u></b>	129	626
508.namd_r	128	296	411	296	411	<b><u>296</u></b>	<b><u>411</u></b>	128	296	411	296	411	<b><u>296</u></b>	<b><u>411</u></b>
510.parest_r	128	935	358	<b><u>934</u></b>	<b><u>358</u></b>	932	359	64	<b><u>412</u></b>	<b><u>407</u></b>	412	407	412	406
511.povray_r	128	479	624	<b><u>477</u></b>	<b><u>626</u></b>	475	630	128	<b><u>456</u></b>	<b><u>655</u></b>	456	655	455	657
519.lbm_r	128	<b><u>595</u></b>	<b><u>227</u></b>	595	227	595	227	128	<b><u>595</u></b>	<b><u>227</u></b>	595	227	595	227
521.wrf_r	128	<b><u>748</u></b>	<b><u>383</u></b>	746	384	749	383	64	<b><u>303</u></b>	<b><u>472</u></b>	304	471	303	473
526.blender_r	128	327	596	326	597	<b><u>327</u></b>	<b><u>597</u></b>	128	327	596	326	597	<b><u>327</u></b>	<b><u>597</u></b>
527.cam4_r	128	<b><u>341</u></b>	<b><u>656</u></b>	341	656	342	654	64	185	606	184	607	<b><u>185</u></b>	<b><u>606</u></b>
538.imagick_r	128	183	1740	<b><u>183</u></b>	<b><u>1740</u></b>	182	1740	128	183	1740	<b><u>183</u></b>	<b><u>1740</u></b>	182	1740
544.nab_r	128	209	1030	<b><u>209</u></b>	<b><u>1030</u></b>	209	1030	128	176	1230	<b><u>175</u></b>	<b><u>1230</u></b>	175	1230
549.fotonik3d_r	128	1596	313	<b><u>1594</u></b>	<b><u>313</u></b>	1593	313	128	1596	313	<b><u>1594</u></b>	<b><u>313</u></b>	1593	313
554.roms_r	128	<b><u>863</u></b>	<b><u>236</u></b>	862	236	863	236	64	382	266	383	266	<b><u>382</u></b>	<b><u>266</u></b>

SPECrate®2017\_fp\_base = **566**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

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### General Notes (Continued)

is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

#### BIOS Settings:

Power Technology = Custom  
Power Performance Tuning = BIOS Controls EPB  
ENERGY\_PERF\_BIAS\_CFG mode = Performance  
DCU Streamer Prefetcher = Disable  
SNC = Enable SNC2 (2-clusters)  
LLC Dead Line Alloc = Disable  
KTI Prefetch = Enable  
Stale AtoS = Disable  
Patrol Scrub = Disable

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Sat Nov 18 00:10:42 2023

SUT (System Under Test) info as seen by some common utilities.

#### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a  
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT\_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)  
x86\_64 x86\_64 x86\_64 GNU/Linux

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## Platform Notes (Continued)

```
-----
2. w
  00:10:42 up 2 days,  4:28,  2 users,  load average: 76.15, 115.01, 122.89
USER      TTY      FROM          LOGIN@      IDLE        JCPU   PCPU   WHAT
root      tty1     -              Wed19       6:28m     1.04s   0.03s  -bash
root      tty2     -              Thu14      13:37m     0.31s   0.31s  -bash
```

```
-----
3. Username
  From environment variable $USER:  root
```

```
-----
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority     (-e) 0
  file size               (blocks, -f) unlimited
  pending signals        (-i) 2062410
  max locked memory      (kbytes, -l) 64
  max memory size        (kbytes, -m) unlimited
  open files             (-n) 1024
  pipe size              (512 bytes, -p) 8
  POSIX message queues   (bytes, -q) 819200
  real-time priority     (-r) 0
  stack size            (kbytes, -s) unlimited
  cpu time              (seconds, -t) unlimited
  max user processes     (-u) 2062410
  virtual memory         (kbytes, -v) unlimited
  file locks            (-x) unlimited
```

```
-----
5. sysinfo process ancestry
  /usr/lib/systemd/systemd --switched-root --system --deserialize 30
  login -- root
  -bash
  -bash
  runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 -c
  ic2023.2-lin-core-avx512-rate-20230622.cfg --define smt-on --define cores=64 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
  runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 --configfile
  ic2023.2-lin-core-avx512-rate-20230622.cfg --define smt-on --define cores=64 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
  --runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
  specperl $SPEC/bin/sysinfo
  $SPEC = /home/cpu2017
```

```
-----
6. /proc/cpuinfo
  model name      : INTEL(R) XEON(R) PLATINUM 8592+
  vendor_id      : GenuineIntel
  cpu family     : 6
  model          : 207
  stepping       : 2
  microcode      : 0x210001a0
  bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
  cpu cores     : 64
  siblings       : 128
  1 physical ids (chips)
```

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### Platform Notes (Continued)

128 processors (hardware threads)  
physical id 0: core ids 0-63  
physical id 0: apicids 0-127

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

-----  
7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                128
On-line CPU(s) list:   0-127
Vendor ID:             GenuineIntel
Model name:            INTEL(R) XEON(R) PLATINUM 8592+
CPU family:            6
Model:                207
Thread(s) per core:    2
Core(s) per socket:    64
Socket(s):             1
Stepping:              2
Frequency boost:       enabled
CPU max MHz:           1901.0000
CPU min MHz:           800.0000
BogoMIPS:              3800.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                    clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                    lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                    nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                    ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                    sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                    lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                    invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                    vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep
                    bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                    avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                    xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                    cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
                    avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                    avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                    cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                    arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities

Virtualization:        VT-x
L1d cache:             3 MiB (64 instances)
L1i cache:             2 MiB (64 instances)
L2 cache:              128 MiB (64 instances)
L3 cache:              320 MiB (1 instance)
NUMA node(s):         2
NUMA node0 CPU(s):    0-31,64-95
NUMA node1 CPU(s):    32-63,96-127
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:    Not affected
Vulnerability Mds:     Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp

```

(Continued on next page)



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### Platform Notes (Continued)

Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRBS-eIBRS SW sequence  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	3M	12	Data	1	64	1	64
L1i	32K	2M	8	Instruction	1	64	1	64
L2	2M	128M	16	Unified	2	2048	1	64
L3	320M	320M	20	Unified	3	262144	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0-31,64-95
node 0 size: 257639 MB
node 0 free: 227100 MB
node 1 cpus: 32-63,96-127
node 1 size: 257993 MB
node 1 free: 232400 MB
node distances:
node  0  1
  0: 10 12
  1: 12 10
```

9. /proc/meminfo

```
MemTotal: 528007696 kB
```

10. who -r

```
run-level 3 Nov 15 19:43
```

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

```
Default Target Status
multi-user running
```

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wickd wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info firewalld gpm grub2-once haveged haveged-switch-root ipmi ipmievd issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures systemd-network-generator systemd-sysexit systemd-time-wait-sync systemd-timesyncd udisks2 vncserver@
indirect	wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
```

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### Platform Notes (Continued)

```
root=UUID=72565a43-6e2d-4580-bfa9-6df7225e0661
splash=silent
mitigations=auto
quiet
security=apparmor
```

```
-----
14. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 800 MHz and 1.90 GHz.
                   The governor "ondemand" may decide which speed to use
                   within this range.

  boost state support:
    Supported: yes
    Active: yes
```

```
-----
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space     2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio     10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs    10000
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5
```

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### Platform Notes (Continued)

#### 19. Disk information

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	xf	221G	60G	162G	28%	/

#### 20. /sys/devices/virtual/dmi/id

```
Vendor:      PM_202207070954
Product:    PPM_202207070954
Product Family: Family
Serial:     PS_202207070954
```

#### 21. dmidecode

Additional information from dmidecode 3.4 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

##### Memory:

8x Micron Technology MTC40F2046S1RC56BD1 64 GB 2 rank 5600

#### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

```
BIOS Vendor:   American Megatrends International, LLC.
BIOS Version:  2.0
BIOS Date:     11/07/2023
BIOS Revision: 5.32
```

### Compiler Version Notes

```
=====  
C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)  
=====
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

```
=====  
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)  
=====
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

```
=====  
C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)  
=====
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

```
=====  
C++, C, Fortran | 507.cactuBSSN_r(base, peak)  
=====
```

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### Compiler Version Notes (Continued)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
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-----  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)  
-----

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
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-----  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
-----

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
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### Base Compiler Invocation

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifx

Benchmarks using both Fortran and C:  
ifx icx

Benchmarks using both C and C++:  
icpx icx

Benchmarks using Fortran, C, and C++:  
icpx icx ifx

### Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## Supermicro

UP SuperServer SYS-521C-NR  
(X13SEDW-F, Intel Xeon Platinum 8592+)

SPECrate®2017\_fp\_base = 566

SPECrate®2017\_fp\_peak = 593

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Nov-2023  
**Hardware Availability:** Dec-2023  
**Software Availability:** Jun-2023

## Base Portability Flags (Continued)

508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

### C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

### C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

### Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
```

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# SPEC CPU®2017 Floating Point Rate Result

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## Supermicro

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**Tested by:** Supermicro

**Test Date:** Nov-2023  
**Hardware Availability:** Dec-2023  
**Software Availability:** Jun-2023

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

```
544.nab_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int
```

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## Supermicro

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## Peak Optimization Flags (Continued)

544.nab\_r (continued):

```
-qopt-zmm-usage=high -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

C++ benchmarks:

508.namd\_r: basepeak = yes

```
510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

```
554.roms_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
```

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## Supermicro

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**CPU2017 License:** 001176

**Test Sponsor:** Supermicro

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**Test Date:** Nov-2023

**Hardware Availability:** Dec-2023

**Software Availability:** Jun-2023

## Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-EMR-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-EMR-revA.xml>

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