



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

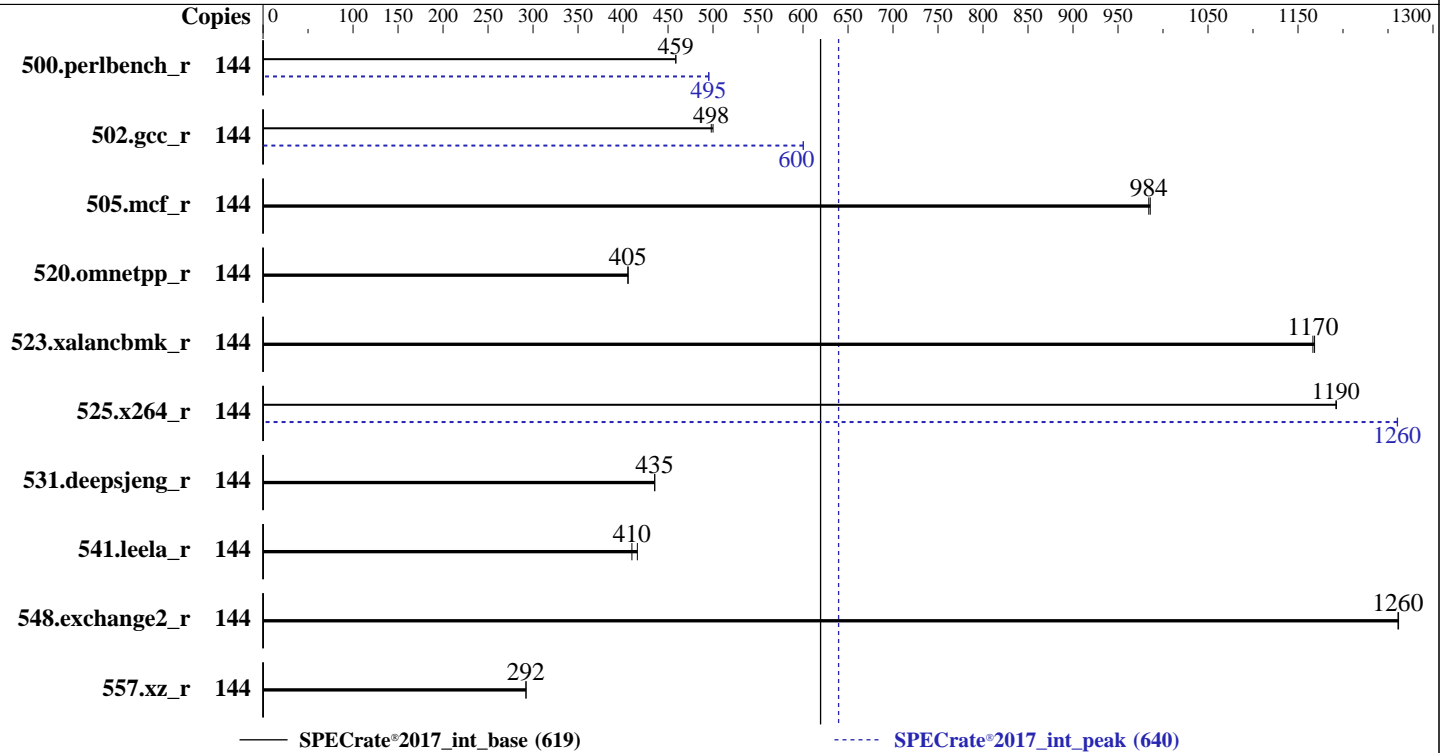
SPECrate®2017\_int\_base = 619

PowerEdge XE9640 (Intel Xeon Platinum 8452Y)

SPECrate®2017\_int\_peak = 640

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Jun-2023  
Hardware Availability: Sep-2023  
Software Availability: Dec-2022



### Hardware

CPU Name: Intel Xeon Platinum 8452Y  
Max MHz: 3200  
Nominal: 2000  
Enabled: 72 cores, 2 chips, 2 threads/core  
Orderable: 1,2 chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 67.5 MB I+D on chip per chip  
Other: None  
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)  
Storage: 90 GB on tmpfs  
Other: None

### Software

OS: Red Hat Enterprise Linux 9.0 (Plow)  
5.14.0-70.13.1.el9\_0.x86\_64  
Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;  
Parallel: No  
Firmware: Version 0.5.35 released Jun-2023  
File System: tmpfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 619

PowerEdge XE9640 (Intel Xeon Platinum 8452Y)

SPECrate®2017\_int\_peak = 640

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Jun-2023  
Hardware Availability: Sep-2023  
Software Availability: Dec-2022

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	144	500	459	<b>500</b>	<b>459</b>			144	463	496	<b>463</b>	<b>495</b>		
502.gcc_r	144	408	500	<b>409</b>	<b>498</b>			144	340	600	<b>340</b>	<b>600</b>		
505.mcf_r	144	236	986	<b>236</b>	<b>984</b>			144	236	986	<b>236</b>	<b>984</b>		
520.omnetpp_r	144	466	406	<b>466</b>	<b>405</b>			144	466	406	<b>466</b>	<b>405</b>		
523.xalancbmk_r	144	<b>130</b>	<b>1170</b>	130	1170			144	<b>130</b>	<b>1170</b>	130	1170		
525.x264_r	144	211	1190	<b>212</b>	<b>1190</b>			144	<b>200</b>	<b>1260</b>	200	1260		
531.deepsjeng_r	144	<b>379</b>	<b>435</b>	379	435			144	<b>379</b>	<b>435</b>	379	435		
541.leela_r	144	<b>582</b>	<b>410</b>	573	416			144	<b>582</b>	<b>410</b>	573	416		
548.exchange2_r	144	299	1260	<b>299</b>	<b>1260</b>			144	299	1260	<b>299</b>	<b>1260</b>		
557.xz_r	144	<b>532</b>	<b>292</b>	532	292			144	<b>532</b>	<b>292</b>	532	292		

SPECrate®2017\_int\_base = 619

SPECrate®2017\_int\_peak = 640

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk\_r / 623.xalancbmk\_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 [https://www.spec.org/cpu2017/Docs/runrules.html#rule\\_1.4](https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4)), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
"/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/lib/ia32:/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/je5.0.1-32"
MALLOCONF = "retain:true"
```



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 619

PowerEdge XE9640 (Intel Xeon Platinum 8452Y)

SPECrate®2017\_int\_peak = 640

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jun-2023

Hardware Availability: Sep-2023

Software Availability: Dec-2022

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
 memory using Red Hat Enterprise Linux 8.4  
 Transparent Huge Pages enabled by default  
 Prior to runcpu invocation  
 Filesystem page cache synced and cleared with:  
`sync; echo 3> /proc/sys/vm/drop_caches`  
 runcpu command invoked through numactl i.e.:  
`numactl --interleave=all runcpu <etc>`  
 jemalloc, a general purpose malloc implementation  
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)  
 is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)  
 is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)  
 is mitigated in the system as tested and documented.

Benchmark run from a 90 GB ramdisk created with the cmd: "mount -t tmpfs -o size=90G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS settings:

```

      ADDDC Setting : Disabled
      DIMM Self Healing on
      Uncorrectable Memory Error : Disabled
      Virtualization Technology : Disabled
      DCU Streamer Prefetcher : Disabled
      Sub NUMA Cluster : 4-way Clustering
      LLC Prefetch : Disabled
      Dead Line LLC Alloc : Disabled
      Optimizer Mode : Enabled

      System Profile : Custom
      CPU Power Management : Maximum Performance
      CLE : Disabled
      C States : Autonomous
      Memory Patrol Scrub : Disabled
      Energy Efficiency Policy : Performance
      PCI ASPM L1 Link
      Power Management : Disabled
  
```

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.0/bin/sysinfo  
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
 running on localhost.localdomain Mon Jun 12 22:40:45 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. `uname -a`
2. `w`
3. Username
4. `ulimit -a`
5. `sysinfo process ancestry`
6. `/proc/cpuinfo`

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 619

PowerEdge XE9640 (Intel Xeon Platinum 8452Y)

SPECrate®2017\_int\_peak = 640

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Jun-2023  
Hardware Availability: Sep-2023  
Software Availability: Dec-2022

## Platform Notes (Continued)

- 7. lscpu
- 8. numactl --hardware
- 9. /proc/meminfo
- 10. who -r
- 11. Systemd service manager version: systemd 250 (250-6.el9\_0)
- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. cpupower frequency-info
- 15. sysctl
- 16. /sys/kernel/mm/transparent\_hugepage
- 17. /sys/kernel/mm/transparent\_hugepage/khugepaged
- 18. OS release
- 19. Disk information
- 20. /sys/devices/virtual/dmi/id
- 21. dmidecode
- 22. BIOS

```
1. uname -a
Linux localhost.localdomain 5.14.0-70.13.1.el9_0.x86_64 #1 SMP PREEMPT Thu Apr 14 12:42:38 EDT 2022 x86_64
x86_64 x86_64 GNU/Linux
```

```
2. w
22:40:45 up 2:06, 1 user, load average: 0.11, 0.03, 0.01
USER TTY LOGIN@ IDLE JCPU PCPU WHAT
root tty1 22:39 29.00s 1.19s 0.00s /bin/bash ./dell-run-speccpu.sh rate --define
DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-adddcD=1 --define DL-BIOS-VirtD=1 --define DL-BIOS-SNC=4
--define DL-VERS=v4.6 --define DL-IQC=1 --output_format html,pdf,txt
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 4123936
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 4123936
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
login -- root
-bash
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 619

PowerEdge XE9640 (Intel Xeon Platinum 8452Y)

SPECrate®2017\_int\_peak = 640

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jun-2023

Hardware Availability: Sep-2023

Software Availability: Dec-2022

## Platform Notes (Continued)

```

/bin/bash ./DELL_rate.sh
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-addcd=1
--define DL-BIOS-VirtD=1 --define DL-BIOS-SNC=4 --define DL-VERS=v4.6 --define DL-LQC=1 --output_format
html,pdf,txt
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-addcd=1
--define DL-BIOS-VirtD=1 --define DL-BIOS-SNC=4 --define DL-VERS=v4.6 --define DL-LQC=1 --output_format
html,pdf,txt
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=144 -c
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=72 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all --iterations 2 --define
DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-addcd=1 --define DL-BIOS-VirtD=1 --define DL-BIOS-SNC=4
--define DL-BIOS-VERS=v4.6 --define DL-LQC=1 --output_format html,pdf,txt intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=144 --configfile
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=72 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --iterations 2
--define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-addcd=1 --define DL-BIOS-VirtD=1 --define
DL-BIOS-SNC=4 --define DL-VERS=v4.6 --define DL-LQC=1 --output_format html,pdf,txt --nopower --runmode
rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.0

```

```

-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) Platinum 8452Y
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 8
microcode      : 0x2b0004b1
bugs           : spectre_v1 spectre_v2 spec_store_bypass swappg
cpu cores      : 36
siblings       : 72
2 physical ids (chips)
144 processors (hardware threads)
physical id 0: core ids 0-35
physical id 1: core ids 0-35
physical id 0: apicids 0-71
physical id 1: apicids 128-199
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

```

-----
7. lscpu

From lscpu from util-linux 2.37.4:
Architecture:      x86_64
CPU op-mode(s):   32-bit, 64-bit
Address sizes:     52 bits physical, 57 bits virtual
Byte Order:       Little Endian
CPU(s):           144
On-line CPU(s) list: 0-143
Vendor ID:        GenuineIntel
BIOS Vendor ID:   Intel
Model name:       Intel(R) Xeon(R) Platinum 8452Y
BIOS Model name:  Intel(R) Xeon(R) Platinum 8452Y
CPU family:       6
Model:            143

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

### SPECrate®2017\_int\_base = 619

### PowerEdge XE9640 (Intel Xeon Platinum 8452Y)

### SPECrate®2017\_int\_peak = 640

**CPU2017 License:** 6573  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Test Date:** Jun-2023  
**Hardware Availability:** Sep-2023  
**Software Availability:** Dec-2022

## Platform Notes (Continued)

```

Thread(s) per core:      2
Core(s) per socket:     36
Socket(s):              2
Stepping:               8
BogoMIPS:               4000.00
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                        x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                        abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                        cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1
                        avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                        xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                        cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                        arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
                        vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
                        bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
                        tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_l1d arch_capabilities

L1d cache:              3.4 MiB (72 instances)
L1i cache:              2.3 MiB (72 instances)
L2 cache:               144 MiB (72 instances)
L3 cache:               135 MiB (2 instances)
NUMA node(s):          8
NUMA node0 CPU(s):     0,6,14,20,26,46,48,56,64,78,86,90,92,98,118,120,128,136
NUMA node1 CPU(s):     2,10,16,22,32,38,44,52,68,74,82,88,94,104,110,116,124,140
NUMA node2 CPU(s):     8,18,28,30,34,42,50,58,66,72,80,100,102,106,114,122,130,138
NUMA node3 CPU(s):     4,12,24,36,40,54,60,62,70,76,84,96,108,112,126,132,134,142
NUMA node4 CPU(s):     1,15,23,29,43,51,57,67,69,73,87,95,101,115,123,129,139,141
NUMA node5 CPU(s):     5,11,19,27,31,37,41,47,55,77,83,91,99,103,109,113,119,127
NUMA node6 CPU(s):     3,9,17,25,33,45,53,59,63,75,81,89,97,105,117,125,131,135
NUMA node7 CPU(s):     7,13,21,35,39,49,61,65,71,79,85,93,107,111,121,133,137,143
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:       Not affected
Vulnerability Mds:        Not affected
Vulnerability Meltdown:   Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:  Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:  Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds:      Not affected
Vulnerability Tsx async abort: Not affected

```

From `lscpu --cache:`

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	3.4M	12	Data	1	64	1	64
L1i	32K	2.3M	8	Instruction	1	64	1	64
L2	2M	144M	16	Unified	2	2048	1	64
L3	67.5M	135M	15	Unified	3	73728	1	64

8. `numactl --hardware`

NOTE: a `numactl 'node'` might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0,6,14,20,26,46,48,56,64,78,86,90,92,98,118,120,128,136
node 0 size: 127960 MB
node 0 free: 118427 MB
node 1 cpus: 2,10,16,22,32,38,44,52,68,74,82,88,94,104,110,116,124,140
node 1 size: 129020 MB

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 619

PowerEdge XE9640 (Intel Xeon Platinum 8452Y)

SPECrate®2017\_int\_peak = 640

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Jun-2023  
Hardware Availability: Sep-2023  
Software Availability: Dec-2022

## Platform Notes (Continued)

```

node 1 free: 128781 MB
node 2 cpus: 8,18,28,30,34,42,50,58,66,72,80,100,102,106,114,122,130,138
node 2 size: 129020 MB
node 2 free: 128713 MB
node 3 cpus: 4,12,24,36,40,54,60,62,70,76,84,96,108,112,126,132,134,142
node 3 size: 129020 MB
node 3 free: 128782 MB
node 4 cpus: 1,15,23,29,43,51,57,67,69,73,87,95,101,115,123,129,139,141
node 4 size: 129020 MB
node 4 free: 128651 MB
node 5 cpus: 5,11,19,27,31,37,41,47,55,77,83,91,99,103,109,113,119,127
node 5 size: 128983 MB
node 5 free: 128727 MB
node 6 cpus: 3,9,17,25,33,45,53,59,63,75,81,89,97,105,117,125,131,135
node 6 size: 129020 MB
node 6 free: 128795 MB
node 7 cpus: 7,13,21,35,39,49,61,65,71,79,85,93,107,111,121,133,137,143
node 7 size: 129000 MB
node 7 free: 128768 MB
node distances:
node  0  1  2  3  4  5  6  7
  0:  10  12  12  12  21  21  21  21
  1:  12  10  12  12  21  21  21  21
  2:  12  12  10  12  21  21  21  21
  3:  12  12  12  10  21  21  21  21
  4:  21  21  21  21  10  12  12  12
  5:  21  21  21  21  12  10  12  12
  6:  21  21  21  21  12  12  10  12
  7:  21  21  21  21  12  12  12  10

```

```

-----
9. /proc/meminfo
MemTotal:      1055789584 kB

```

```

-----
10. who -r
run-level 3 Jun 12 20:34

```

```

-----
11. Systemd service manager version: systemd 250 (250-6.e19_0)
Default Target Status
multi-user      running

```

```

-----
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online
accounts-daemon atd auditd avahi-daemon bluetooth chronyd crond cups dbus-broker firewallld
gdm getty@ insights-client-boot irqbalance iscsi iscsi-onboot kdump libstoragemgmt
low-memory-monitor lvm2-monitor mcelog mdmonitor microcode multipathd nis-domainname
nvme-fc-boot-connections ostree-remount power-profiles-daemon qemu-guest-agent rhsmcertd
rsyslog rtkit-daemon selinux-autorelabel-mark smartd sshd sssd switcheroo-control
systemd-network-generator udisks2 upower vgauthd vmtoolsd
enabled-runtime systemd-remount-fs
disabled arp-ethers blk-availability brltty canberra-system-bootup canberra-system-shutdown
canberra-system-shutdown-reboot chrony-wait cni-dhcp console-getty cpupower cups-browsed
dbus-daemon debug-shell dnsmasq iprdump iprinit iprupdate iscsid iscsiuiio kpatch kvm_stat
ledmon man-db-restart-cache-update nftables nvme-fc-autoconnect podman podman-auto-update
podman-restart psacct ras-mc-ctl rasdaemon rdisc rhcd rhsm rhsm-facts rpmdm-rebuild
serial-getty@ speech-dispatcherd sshd-keygen@ systemd-boot-check-no-failures

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_int\_base = 619

PowerEdge XE9640 (Intel Xeon Platinum 8452Y)

SPECrate®2017\_int\_peak = 640

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jun-2023

Hardware Availability: Sep-2023

Software Availability: Dec-2022

### Platform Notes (Continued)

indirect systemd-pstore systemd-sysext wpa\_supplicant  
spice-vdagentd sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo

-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-70.13.1.el9\_0.x86\_64  
root=/dev/mapper/rhel-root  
ro  
crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M  
resume=/dev/mapper/rhel-swap  
rd.lvm.lv=rhel/root  
rd.lvm.lv=rhel/swap  
rhgb  
quiet

-----  
14. cpupower frequency-info  
analyzing CPU 0:  
Unable to determine current policy  
boost state support:  
Supported: yes  
Active: yes

-----  
15. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 2  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 20  
vm.dirty\_writeback\_centisecs 500  
vm.dirtytime\_expire\_seconds 43200  
vm.extfrag\_threshold 500  
vm.min\_unmapped\_ratio 1  
vm.nr\_hugepages 0  
vm.nr\_hugepages\_mempolicy 0  
vm.nr\_overcommit\_hugepages 0  
vm.swappiness 60  
vm.watermark\_boost\_factor 15000  
vm.watermark\_scale\_factor 10  
vm.zone\_reclaim\_mode 0

-----  
16. /sys/kernel/mm/transparent\_hugepage  
defrag always defer defer+madvice [madvice] never  
enabled [always] madvice never  
hpage\_pmd\_size 2097152  
shmem\_enabled always within\_size advise [never] deny force

-----  
17. /sys/kernel/mm/transparent\_hugepage/khugepaged  
alloc\_sleep\_millisecs 60000  
defrag 1  
max\_ptes\_none 511  
max\_ptes\_shared 256  
max\_ptes\_swap 64  
pages\_to\_scan 4096

(Continued on next page)





# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 619

PowerEdge XE9640 (Intel Xeon Platinum 8452Y)

SPECrate®2017\_int\_peak = 640

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Jun-2023  
Hardware Availability: Sep-2023  
Software Availability: Dec-2022

## Platform Notes (Continued)

scan\_sleep\_millisecs 10000

-----  
18. OS release  
From /etc/\*-release /etc/\*-version  
os-release Red Hat Enterprise Linux 9.0 (Plow)  
redhat-release Red Hat Enterprise Linux release 9.0 (Plow)  
system-release Red Hat Enterprise Linux release 9.0 (Plow)

-----  
19. Disk information  
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.0  
Filesystem Type Size Used Avail Use% Mounted on  
tmpfs tmpfs 90G 4.2G 86G 5% /mnt/ramdisk

-----  
20. /sys/devices/virtual/dmi/id  
Vendor: Dell Inc.  
Product: PowerEdge XE9640  
Product Family: PowerEdge  
Serial: MS01513

-----  
21. dmidecode  
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
14x 00AD063200AD HMC94MEBRA109N 64 GB 2 rank 4800  
2x 00CE00B300CE M321R8GA0BB0-CQKEG 64 GB 2 rank 4800

-----  
22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Dell Inc.  
BIOS Version: 0.5.35  
BIOS Date: 06/12/2023  
BIOS Revision: 0.5

## Compiler Version Notes

=====  
C | 502.gcc\_r(peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

-----  
(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 619

PowerEdge XE9640 (Intel Xeon Platinum 8452Y)

SPECrate®2017\_int\_peak = 640

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Jun-2023  
Hardware Availability: Sep-2023  
Software Availability: Dec-2022

## Compiler Version Notes (Continued)

C | 502.gcc\_r(peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
-----

=====  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
-----

=====  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
| 541.leela\_r(base, peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 548.exchange2\_r(base, peak)

-----  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifx

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 619

PowerEdge XE9640 (Intel Xeon Platinum 8452Y)

SPECrate®2017\_int\_peak = 640

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jun-2023

Hardware Availability: Sep-2023

Software Availability: Dec-2022

## Base Portability Flags (Continued)

541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64\_lin  
-lqkmallo

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64\_lin  
-lqkmallo

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto  
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64\_lin  
-lqkmallo

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 619

PowerEdge XE9640 (Intel Xeon Platinum 8452Y)

SPECrate®2017\_int\_peak = 640

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jun-2023

Hardware Availability: Sep-2023

Software Availability: Dec-2022

## Peak Portability Flags (Continued)

```

502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

```

## Peak Optimization Flags

C benchmarks:

```

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: basepeak = yes

```

C++ benchmarks:

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 619

PowerEdge XE9640 (Intel Xeon Platinum 8452Y)

SPECrate®2017\_int\_peak = 640

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jun-2023

Hardware Availability: Sep-2023

Software Availability: Dec-2022

## Peak Optimization Flags (Continued)

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.5.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.5.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2023-06-12 22:40:44-0400.

Report generated on 2024-01-29 18:07:54 by CPU2017 PDF formatter v6716.

Originally published on 2023-09-13.