



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Dell Inc.

### SPECrate®2017\_fp\_base = 626

### PowerEdge MX760c (Intel Xeon Gold 6442Y)

### SPECrate®2017\_fp\_peak = 644

CPU2017 License: 6573

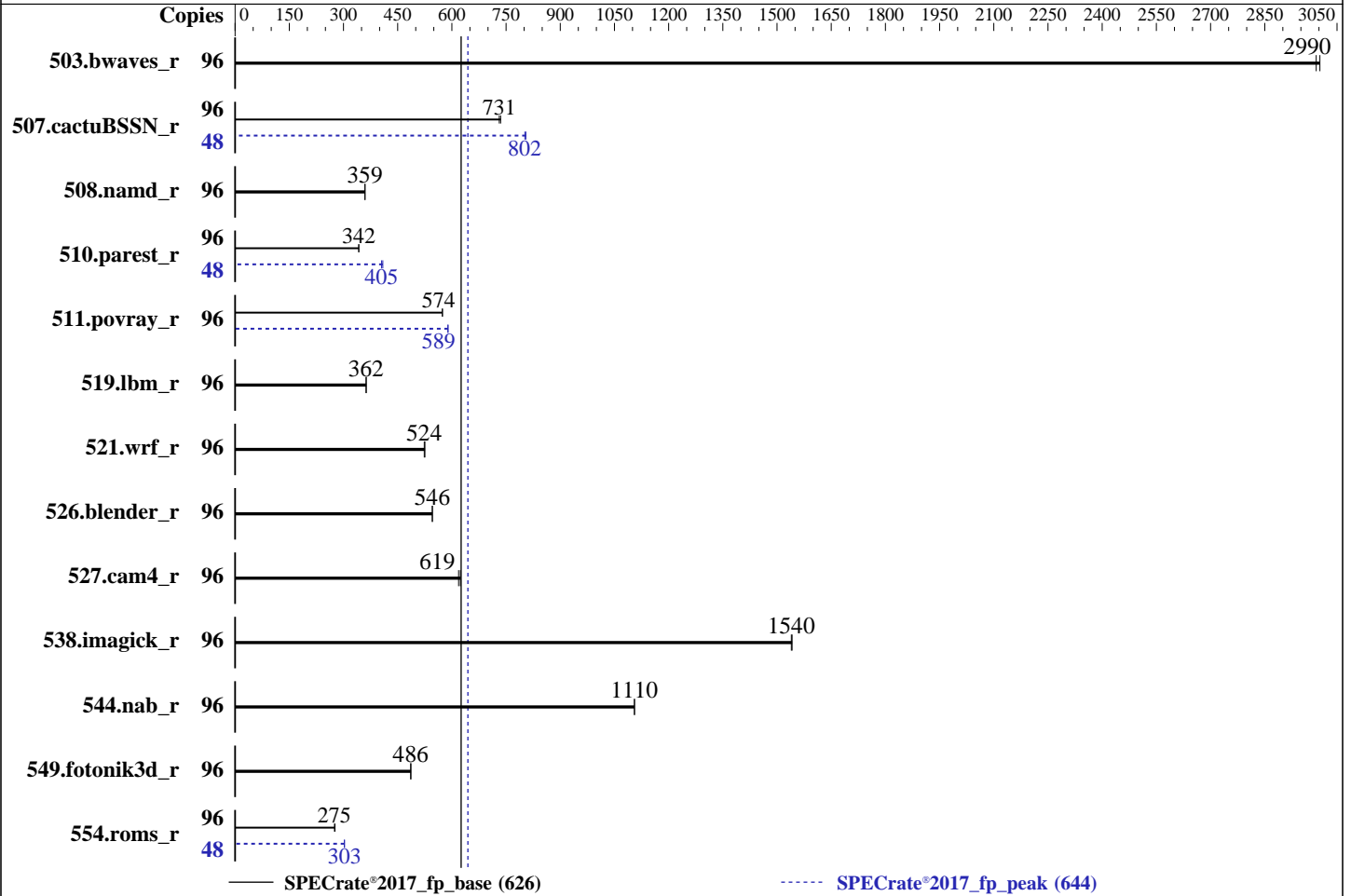
Test Date: Feb-2023

Test Sponsor: Dell Inc.

Hardware Availability: Apr-2023

Tested by: Dell Inc.

Software Availability: Dec-2022



### Hardware

CPU Name: Intel Xeon Gold 6442Y  
 Max MHz: 4000  
 Nominal: 2600  
 Enabled: 48 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 60 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)  
 Storage: 125 GB on tmpfs  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP4 5.14.21-150400.22-default  
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Version 0.4.5 released Jan-2023  
 File System: tmpfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	96	321	3000	<u>322</u>	<u>2990</u>			96	321	3000	<u>322</u>	<u>2990</u>		
507.cactuBSSN_r	96	165	735	<u>166</u>	<u>731</u>			48	75.5	805	<u>75.8</u>	<u>802</u>		
508.namd_r	96	<u>254</u>	<u>359</u>	254	360			96	<u>254</u>	<u>359</u>	254	360		
510.parest_r	96	<u>734</u>	<u>342</u>	733	342			48	307	409	<u>310</u>	<u>405</u>		
511.povray_r	96	391	574	<u>391</u>	<u>574</u>			96	380	590	<u>381</u>	<u>589</u>		
519.lbm_r	96	<u>279</u>	<u>362</u>	279	363			96	<u>279</u>	<u>362</u>	279	363		
521.wrf_r	96	<u>411</u>	<u>524</u>	409	525			96	<u>411</u>	<u>524</u>	409	525		
526.blender_r	96	268	546	<u>268</u>	<u>546</u>			96	268	546	<u>268</u>	<u>546</u>		
527.cam4_r	96	<u>271</u>	<u>619</u>	269	624			96	<u>271</u>	<u>619</u>	269	624		
538.imagick_r	96	155	1540	<u>155</u>	<u>1540</u>			96	155	1540	<u>155</u>	<u>1540</u>		
544.nab_r	96	<u>146</u>	<u>1110</u>	146	1110			96	<u>146</u>	<u>1110</u>	146	1110		
549.fotonik3d_r	96	769	487	<u>770</u>	<u>486</u>			96	769	487	<u>770</u>	<u>486</u>		
554.roms_r	96	552	277	<u>555</u>	<u>275</u>			48	252	303	<u>252</u>	<u>303</u>		

SPECrate®2017\_fp\_base = **626**

SPECrate®2017\_fp\_peak = **644**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH =  
"/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/je5.0.1-64"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default

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## General Notes (Continued)

Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS settings:

ADDDC Setting : Disabled  
DIMM Self Healing on  
Uncorrectable Memory Error : Disabled  
Virtualization Technology : Disabled  
DCU Streamer Prefetcher : Disabled  
Sub NUMA Cluster : 4-way Clustering  
LLC Prefetch : Disabled  
Dead Line LLC Alloc : Disabled  
Optimizer Mode : Enabled  
  
System Profile : Custom  
CPU Power Management : Maximum Performance  
C1E : Disabled  
C States : Autonomous  
Memory Patrol Scrub : Disabled  
Energy Efficiency Policy : Performance  
PCI ASPM L1 Link  
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.0/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Fri Feb 24 18:22:09 2023

SUT (System Under Test) info as seen by some common utilities.

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## Platform Notes (Continued)

### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

```
1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
18:22:09 up 3:44, 1 user, load average: 74.43, 90.56, 93.43
USER      TTY      FROM          LOGIN@      IDLE        JCPU      PCPU      WHAT
root     ttyl      -              14:39       3:41m      1.36s     0.00s    /bin/bash ./dell-run-specrate.sh
--iterations 2 --output_format csv,html,pdf,txt -define Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
```

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## Platform Notes (Continued)

```

core file size      (blocks, -c) unlimited
data seg size      (kbytes, -d) unlimited
scheduling priority      (-e) 0
file size          (blocks, -f) unlimited
pending signals     (-i) 4124591
max locked memory   (kbytes, -l) 64
max memory size     (kbytes, -m) unlimited
open files          (-n) 1024
pipe size           (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority  (-r) 0
stack size          (kbytes, -s) unlimited
cpu time            (seconds, -t) unlimited
max user processes  (-u) 4124591
virtual memory      (kbytes, -v) unlimited
file locks          (-x) unlimited

```

### 5. sysinfo process ancestry

```

/usr/lib/systemd/systemd --switched-root --system --deserialize 29
login -- root
-bash
/bin/bash ./DELL_rate.sh
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-specrate.sh --iterations 2 --output_format csv,html,pdf,txt -define
  Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc
/bin/bash ./dell-run-specrate.sh --iterations 2 --output_format csv,html,pdf,txt -define
  Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=96 -c
  ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=48 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak -o all --iterations 2
  --output_format csv,html,pdf,txt -define Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=96 --configfile
  ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=48 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --iterations 2
  --output_format csv,html,pdf,txt --define Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc --nopower --runmode rate
  --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.0

```

### 6. /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) Gold 6442Y
vendor_id      : GenuineIntel
cpu family     : 6

```

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## Platform Notes (Continued)

```
model          : 143
stepping       : 8
microcode      : 0x2b000181
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 24
siblings       : 48
2 physical ids (chips)
96 processors (hardware threads)
physical id 0: core ids 0-23
physical id 1: core ids 0-23
physical id 0: apicids 0-47
physical id 1: apicids 128-175
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.2:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          46 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 96
On-line CPU(s) list:   0-95
Vendor ID:              GenuineIntel
Model name:             Intel(R) Xeon(R) Gold 6442Y
CPU family:             6
Model:                  143
Thread(s) per core:    2
Core(s) per socket:    24
Socket(s):              2
Stepping:               8
BogoMIPS:               5200.00
```

```
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                        x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                        abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                        cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle
                        avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                        xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                        cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                        arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
```

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## Platform Notes (Continued)

```
vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcndq la57 rdpid
bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
```

L1d cache: 2.3 MiB (48 instances)  
L1i cache: 1.5 MiB (48 instances)  
L2 cache: 96 MiB (48 instances)  
L3 cache: 120 MiB (2 instances)  
NUMA node(s): 4

NUMA node0 CPU(s): 0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68,72,76,80,84,88,92  
NUMA node1 CPU(s): 2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70,74,78,82,86,90,94  
NUMA node2 CPU(s): 1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69,73,77,81,85,89,93  
NUMA node3 CPU(s): 3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63,67,71,75,79,83,87,91,95

Vulnerability Itlb multihit: Not affected  
Vulnerability L1tf: Not affected  
Vulnerability Mds: Not affected  
Vulnerability Meltdown: Not affected

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp  
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	2.3M	12	Data	1	64	1	64
L1i	32K	1.5M	8	Instruction	1	64	1	64
L2	2M	96M	16	Unified	2	2048	1	64
L3	60M	120M	15	Unified	3	65536	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68,72,76,80,84,88,92
node 0 size: 257421 MB
node 0 free: 242780 MB
node 1 cpus: 2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70,74,78,82,86,90,94
node 1 size: 258007 MB
node 1 free: 239132 MB
node 2 cpus: 1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69,73,77,81,85,89,93
node 2 size: 258041 MB
node 2 free: 248263 MB
node 3 cpus: 3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63,67,71,75,79,83,87,91,95
node 3 size: 257698 MB
node 3 free: 247891 MB
node distances:
node  0  1  2  3
```

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## Platform Notes (Continued)

```
0: 10 12 21 21
1: 12 10 21 21
2: 21 21 10 12
3: 21 21 12 10
```

```
-----
9. /proc/meminfo
   MemTotal:          1055918024 kB
```

```
-----
10. who -r
    run-level 3 Feb 24 14:38
```

```
-----
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
    Default Target Status
    multi-user      running
```

```
-----
12. Services, from systemctl list-unit-files
    STATE          UNIT FILES
    enabled        apparmor auditd cron firewalld getty@ haveged irqbalance issue-generator kbdsettings kdump
                   kdump-early lvm2-monitor postfix purge-kernels rollback sshd wicked wickedd-auto4
                   wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
    enabled-runtime systemd-remount-fs
    disabled       blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell
                   ebttables grub2-once haveged-switch-root issue-add-ssh-keys kexec-load lunmask nfs
                   nfs-blkmap rpcbind rpmconfigcheck rsyncd serial-getty@ systemd-boot-check-no-failures
                   systemd-network-generator systemd-sysexec systemd-time-wait-sync systemd-timesyncd tuned
    indirect       wickedd
```

```
-----
13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
    root=UUID=4068cb7a-41a3-4eea-ab49-bc6f15bea24c
    splash=silent
    mitigations=auto
    quiet
    security=apparmor
    crashkernel=314M,high
    crashkernel=72M,low
```

```
-----
14. cpupower frequency-info
    analyzing CPU 0:
    Unable to determine current policy
    boost state support:
```

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## Platform Notes (Continued)

Supported: yes

Active: yes

### 15. tuned-adm active

It seems that tuned daemon is not running, preset profile is not activated.  
Preset profile: throughput-performance

### 16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	20
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	60
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	0

### 17. /sys/kernel/mm/transparent\_hugepage

defrag	always	defer	defer+madvise	[madvise]	never
enabled	[always]	madvise	never		
hpage_pmd_size	2097152				
shmem_enabled	always	within_size	advise	[never]	deny force

### 18. /sys/kernel/mm/transparent\_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

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## Platform Notes (Continued)

### 19. OS release

```
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4
```

### 20. Disk information

```
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.0
Filesystem      Type  Size  Used Avail Use% Mounted on
tmpfs            tmpfs 125G   44G   82G   35% /mnt/ramdisk
```

### 21. /sys/devices/virtual/dmi/id

```
Vendor:      Dell Inc.
Product:     PowerEdge MX760c
Product Family: PowerEdge
Serial:      SL6M701
```

### 22. dmidecode

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

#### Memory:

```
12x 002C00B3002C MTC40F2046S1RC48BA1 64 GB 2 rank 4800
1x  002C0632002C MTC40F2046S1RC48BA1 64 GB 2 rank 4800
3x  002C069D002C MTC40F2046S1RC48BA1 64 GB 2 rank 4800
```

### 23. BIOS

(This section combines info from /sys/devices and dmidecode.)

```
BIOS Vendor:      Dell Inc.
BIOS Version:     0.4.5
BIOS Date:        01/19/2023
BIOS Revision:    0.4
```

## Compiler Version Notes

```
=====  
C          | 519.lbm_r(base, peak) 538.imagick_r(base, peak)  
          | 544.nab_r(base, peak)
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2023.0.0 Build 20221201
```

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# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 626

PowerEdge MX760c (Intel Xeon Gold 6442Y)

SPECrate®2017\_fp\_peak = 644

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Feb-2023  
Hardware Availability: Apr-2023  
Software Availability: Dec-2022

## Compiler Version Notes (Continued)

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=====  
C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2023.0.0 Build 20221201

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=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2023.0.0 Build 20221201

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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2023.0.0 Build 20221201

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=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2023.0.0 Build 20221201

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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2023.0.0 Build 20221201

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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version  
2023.0.0 Build 20221201

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=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak)  
| 554.roms\_r(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version  
2023.0.0 Build 20221201

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=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
=====

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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SPECrate®2017\_fp\_peak = 644

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Feb-2023

Hardware Availability: Apr-2023

Software Availability: Dec-2022

## Compiler Version Notes (Continued)

-----

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version  
2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64

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# SPEC CPU®2017 Floating Point Rate Result

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CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Feb-2023

Hardware Availability: Apr-2023

Software Availability: Dec-2022

## Base Portability Flags (Continued)

554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

### C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsaphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

### C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsaphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

### Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsaphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```



# SPEC CPU®2017 Floating Point Rate Result

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PowerEdge MX760c (Intel Xeon Gold 6442Y)

SPECrate®2017\_fp\_peak = 644

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Feb-2023

Hardware Availability: Apr-2023

Software Availability: Dec-2022

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

544.nab\_r: basepeak = yes

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids  
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -mprefer-vector-width=512  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

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Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Feb-2023

Hardware Availability: Apr-2023

Software Availability: Dec-2022

## Peak Optimization Flags (Continued)

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

```
554.roms_r: -w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.3.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.3.xml>



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**Test Date:** Feb-2023

**Hardware Availability:** Apr-2023

**Software Availability:** Dec-2022

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