SPEC® CPU2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL385 Gen10
(2.35 GHz, AMD EPYC 7452)

SPECrate2017_fp_base =   352
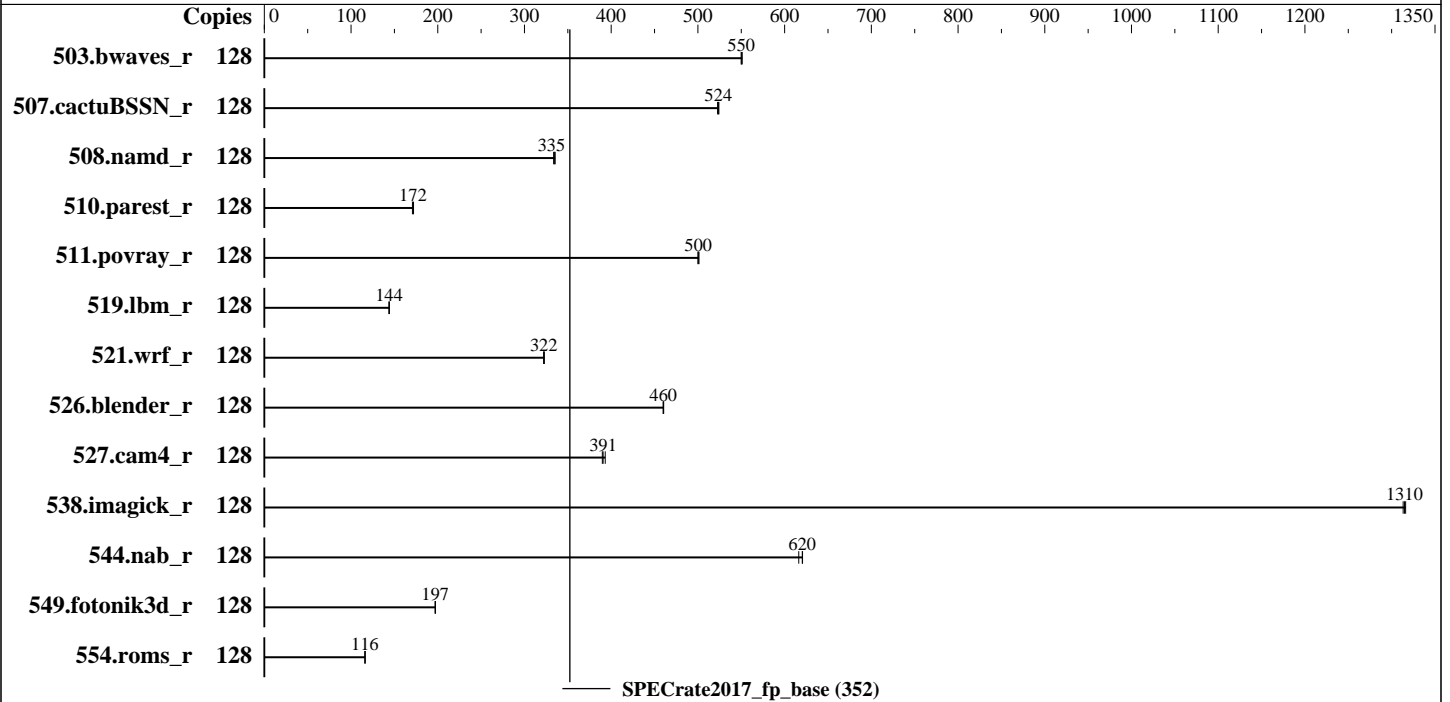
SPECrate2017_fp_peak = Not Run

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2019

Hardware Availability: Aug-2019

Software Availability: Aug-2019

Copies	
503.bwaves_r	**128** 550
507.cactuBSSN_r	**128** 524
508.namd_r	**128** 335
510.parest_r	**128** 172
511.povray_r	**128** 500
519.lbm_r	**128** 144
521.wrf_r	**128** 322
526.blender_r	**128** 460
527.cam4_r	**128** 391
538.imagick_r	**128** 1310
544.nab_r	**128** 620
549.fotonik3d_r	**128** 197
554.roms_r	**128** 116

SPECrate2017_fp_base (352)

Hardware

CPU Name: AMD EPYC 7452
 Max MHz.:   3350
 Nominal: 2350
Enabled: 64 cores, 2 chips, 2 threads/core
Orderable: 1, 2 chip(s)
Cache L1:   32 KB I + 32 KB D on chip per core
 L2:   512 KB I+D on chip per core
 L3:   128 MB I+D on chip per chip, 16 MB shared / 4 cores
 Other: None
Memory: 1 TB (16 x 64 GB 4Rx4 PC4-2933Y-L)
Storage: 1 x 400 GB SAS SSD, RAID 0
Other: None

Software

OS: SUSE Linux Enterprise Server 15 (x86_64) SP1 Kernel 4.12.14-195-default
Compiler: C/C++/Fortran: Version 2.0.0 of AOCC
Parallel: No
Firmware: HPE BIOS Version A40 07/15/2019 released Aug-2019
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: jemalloc: jemalloc memory allocator library V5.2.0;

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Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
503.bwaves_r	128	2329	551	**2334**	**550**	2334	550							
507.cactuBSSN_r	128	310	523	309	524	**309**	**524**							
508.namd_r	128	363	335	**363**	**335**	365	334							
510.parest_r	128	1946	172	1961	171	**1952**	**172**							
511.povray_r	128	596	501	598	500	**597**	**500**							
519.lbm_r	128	937	144	938	144	**937**	**144**							
521.wrf_r	128	**889**	**322**	888	323	890	322							
526.blender_r	128	423	461	424	460	**424**	**460**							
527.cam4_r	128	569	393	**573**	**391**	574	390							
538.imagick_r	128	242	1310	242	1320	**242**	**1310**							
544.nab_r	128	**347**	**620**	347	621	350	616							
549.fotonik3d_r	128	**2530**	**197**	2529	197	2531	197							
554.roms_r	128	**1751**	**116**	1753	116	1750	116							

SPECrate2017_fp_base = 352

SPECrate2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu

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Operating System Notes (Continued)

```
dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were
all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)
```

General Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017_A2/amd_rate_aocc200_rome_A_lib/64;
/home/cpu2017_A2/amd_rate_aocc200_rome_A_lib/32:"
MALLOC_CONF = "retain:true"

Binaries were compiled on a system with 2p AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc: configured and built with GCC 9.1.0 in Ubuntu 19.04
with flags: -Ofast -march=znver2. jemalloc 5.2.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2
```

Platform Notes

```
BIOS Configuration
 Thermal Configuration set to Maximum Cooling
 Determinism Control set to Manual
 Performance Determinism set to Power Deterministic
 Minimum Processor Idle Power Core C-State set to C6 State
 Memory Patrol Scrubbing set to Disabled
 Workload Profile set to General Throughput Compute
 NUMA memory domains per socket set to Four memory domains per socket
 Sysinfo program /home/cpu2017_A2/bin/sysinfo
 Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
 running on dl385gen10-rome-32c Sat Jul 20 03:52:38 2019

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : AMD EPYC 7452 32-Core Processor
        2  "physical id"s (chips)
        128 "processors"
```

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Software Availability: Aug-2019

Platform Notes (Continued)

```
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 32
      siblings  : 64
      physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
      25 26 27 28 29 30 31
      physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
      25 26 27 28 29 30 31

  From lscpu:
      Architecture:        x86_64
      CPU op-mode(s):      32-bit, 64-bit
      Byte Order:          Little Endian
      Address sizes:       48 bits physical, 48 bits virtual
      CPU(s):              128
      On-line CPU(s) list: 0-127
      Thread(s) per core:  2
      Core(s) per socket:  32
      Socket(s):           2
      NUMA node(s):        8
      Vendor ID:           AuthenticAMD
      CPU family:          23
      Model:               49
      Model name:          AMD EPYC 7452 32-Core Processor
      Stepping:            0
      CPU MHz:             2350.000
      CPU max MHz:         2350.0000
      CPU min MHz:         1500.0000
      BogoMIPS:            4691.13
      Virtualization:      AMD-V
      L1d cache:           32K
      L1i cache:           32K
      L2 cache:            512K
      L3 cache:            16384K
      NUMA node0 CPU(s):   0-7,64-71
      NUMA node1 CPU(s):   8-15,72-79
      NUMA node2 CPU(s):   16-23,80-87
      NUMA node3 CPU(s):   24-31,88-95
      NUMA node4 CPU(s):   32-39,96-103
      NUMA node5 CPU(s):   40-47,104-111
      NUMA node6 CPU(s):   48-55,112-119
      NUMA node7 CPU(s):   56-63,120-127
      Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
      pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
      constant_tsc rep_good nopl xtopology nonstop_tsc cpuid extd_apicid aperfmperf pni
      pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c
      rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
```

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CPU2017 License: 3		**Test Date:**	Jul-2019
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Tested by:	HPE	**Software Availability:**	Aug-2019

Platform Notes (Continued)

```
    osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_l2 mwaitx cpb
    cat_l3 cdp_l3 hw_pstate ssbd ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2
    cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves
    cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr arat npt
    lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
    pfthreshold avic v_vmsave_vmload vgif umip rdpid overflow_recov succor smca


 /proc/cpuinfo cache data
    cache size : 512 KB


 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 8 nodes (0-7)
   node 0 cpus: 0 1 2 3 4 5 6 7 64 65 66 67 68 69 70 71
   node 0 size: 128729 MB
   node 0 free: 128432 MB
   node 1 cpus: 8 9 10 11 12 13 14 15 72 73 74 75 76 77 78 79
   node 1 size: 129020 MB
   node 1 free: 128733 MB
   node 2 cpus: 16 17 18 19 20 21 22 23 80 81 82 83 84 85 86 87
   node 2 size: 129020 MB
   node 2 free: 128747 MB
   node 3 cpus: 24 25 26 27 28 29 30 31 88 89 90 91 92 93 94 95
   node 3 size: 129008 MB
   node 3 free: 128746 MB
   node 4 cpus: 32 33 34 35 36 37 38 39 96 97 98 99 100 101 102 103
   node 4 size: 128990 MB
   node 4 free: 128710 MB
   node 5 cpus: 40 41 42 43 44 45 46 47 104 105 106 107 108 109 110 111
   node 5 size: 129020 MB
   node 5 free: 128757 MB
   node 6 cpus: 48 49 50 51 52 53 54 55 112 113 114 115 116 117 118 119
   node 6 size: 129020 MB
   node 6 free: 128766 MB
   node 7 cpus: 56 57 58 59 60 61 62 63 120 121 122 123 124 125 126 127
   node 7 size: 128810 MB
   node 7 free: 128559 MB
   node distances:
   node   0   1   2   3   4   5   6   7
     0:  10  12  12  12  32  32  32  32
     1:  12  10  12  12  32  32  32  32
     2:  12  12  10  12  32  32  32  32
     3:  12  12  12  10  32  32  32  32
     4:  32  32  32  32  10  12  12  12
     5:  32  32  32  32  12  10  12  12
     6:  32  32  32  32  12  12  10  12
     7:  32  32  32  32  12  12  12  10
```

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SPECrate2017_fp_peak = Not Run

CPU2017 License: 3
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Platform Notes (Continued)

```
From /proc/meminfo
   MemTotal:       1056379836 kB
   HugePages_Total:        0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="SLES"
      VERSION="15-SP1"
      VERSION_ID="15.1"
      PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
      ID="sles"
      ID_LIKE="suse"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:15:sp1"

uname -a:
   Linux dl385gen10-rome-32c 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019
   (8fba516) x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown):            Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline, IBPB: conditional,
IBRS_FW, STIBP: conditional, RSB filling

run-level 3 Jul 19 12:57

SPEC is set to: /home/cpu2017_A2
   Filesystem      Type   Size  Used Avail Use% Mounted on
   /dev/sda2       btrfs  371G   52G  318G  14% /home

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS HPE A40 07/15/2019
  Memory:
   16x HPE P03054-091 64 GB 4 rank 2933
   16x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)
```

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Compiler Version Notes

```
==============================================================================
  CC   519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------


==============================================================================
  CXXC 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------


==============================================================================
  FC   503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------


==============================================================================
  CC   511.povray_r(base) 521.wrf_r(base) 526.blender_r(base) 527.cam4_r(base)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
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  AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------


==============================================================================
  FC   507.cactuBSSN_r(base)
```

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Compiler Version Notes (Continued)

```
--------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
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Target: x86_64-unknown-linux-gnu
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AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
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AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
--------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`clang`

C++ benchmarks:
`clang++`

Fortran benchmarks:
`flang`

Benchmarks using both Fortran and C:
`flang clang`

Benchmarks using both C and C++:
`clang++ clang`

Benchmarks using Fortran, C, and C++:
`clang++ clang flang`

Base Portability Flags

503.bwaves_r: `-DSPEC_LP64`
507.cactuBSSN_r: `-DSPEC_LP64`
508.namd_r: `-DSPEC_LP64`

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Base Portability Flags (Continued)

510.parest_r: `-DSPEC_LP64`
511.povray_r: `-DSPEC_LP64`
519.lbm_r: `-DSPEC_LP64`
521.wrf_r: `-DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64`
526.blender_r: `-funsigned-char -D__BOOL_DEFINED -DSPEC_LP64`
527.cam4_r: `-DSPEC_CASE_FLAG -DSPEC_LP64`
538.imagick_r: `-DSPEC_LP64`
544.nab_r: `-DSPEC_LP64`
549.fotonik3d_r: `-DSPEC_LP64`
554.roms_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -lmvec -lamdlibm -ljemalloc
-lflang
```

C++ benchmarks:
```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-partial-unswitch -z muldefs -lmvec -lamdlibm
-ljemalloc -lflang
```

Fortran benchmarks:
```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using both Fortran and C:
```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
```

(Continued on next page)

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):
```
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -funroll-loops -Mrecursive -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using both C and C++:
```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch -z muldefs
-lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using Fortran, C, and C++:
```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch
-funroll-loops -Mrecursive -z muldefs -Kieee -fno-finite-math-only
-lmvec -lamdlibm -ljemalloc -lflang
```

The flags files that were used to format this result can be browsed at
  http://www.spec.org/cpu2017/flags/aocc200-flags-A1.html
  http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revE.html

You can also download the XML flags sources by saving the following links:
  http://www.spec.org/cpu2017/flags/aocc200-flags-A1.xml
  http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revE.xml

SPEC CPU2017 Floating Point Rate Result

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Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant DL385 Gen10
(2.35 GHz, AMD EPYC 7452)

SPECrate2017_fp_base = 352

SPECrate2017_fp_peak = Not Run

CPU2017 License: 3	**Test Date:** Jul-2019
Test Sponsor: HPE	**Hardware Availability:** Aug-2019
Tested by: HPE	**Software Availability:** Aug-2019

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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