



SPEC® CPU2017 Floating Point Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLaint DL385 Gen10

(2.00 GHz, AMD EPYC 7702)

SPECrate2017_fp_base = 490

SPECrate2017_fp_peak = 540

CPU2017 License: 3

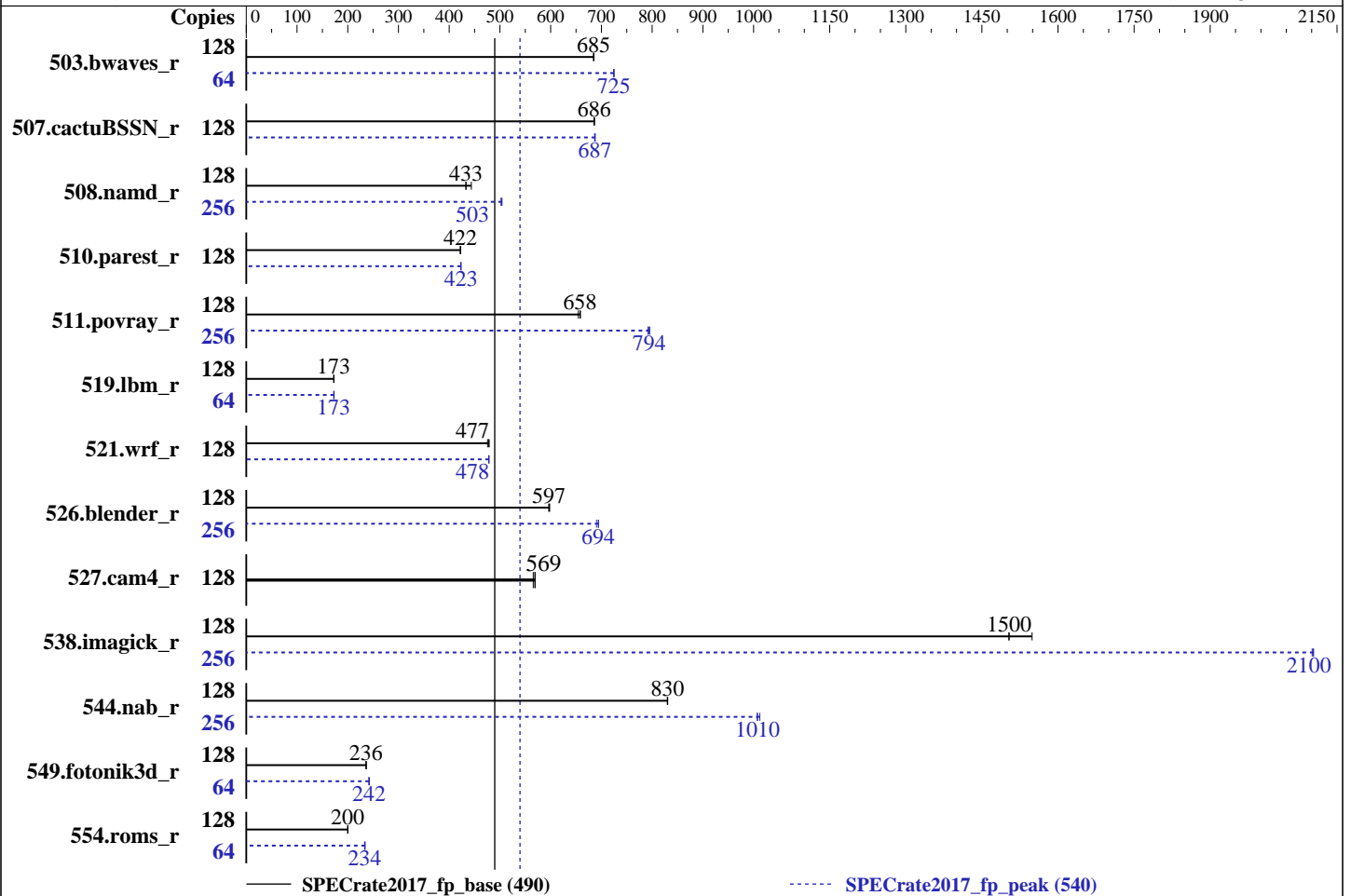
Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2019

Hardware Availability: Aug-2019

Software Availability: Aug-2019



Hardware

CPU Name: AMD EPYC 7702
 Max MHz.: 3350
 Nominal: 2000
 Enabled: 128 cores, 2 chips, 2 threads/core
 Orderable: 1, 2 chip(s)
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 512 KB I+D on chip per core
 L3: 256 MB I+D on chip per chip, 16 MB shared / 4 cores
 Other: None
 Memory: 1 TB (16 x 64 GB 4Rx4 PC4-2933Y-L)
 Storage: 1 x 400 GB SAS SSD RAID 0
 Other: None

Software

OS: SLES Enterprise Server 15 (x86_64) SP1
 Kernel 4.12.14-193-default
 Compiler: C/C++/Fortran: Version 2.0.0 of AOCC
 Parallel: No
 Firmware: HPE BIOS Version A40 07/15/2019 released Aug-2019
 File System: btrfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc: jemalloc memory allocator library V5.2.0;



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	128	1873	685	1875	685	<u>1874</u>	<u>685</u>	64	886	724	885	725	<u>886</u>	<u>725</u>
507.cactuBSSN_r	128	236	686	236	686	<u>236</u>	<u>686</u>	128	236	687	236	688	<u>236</u>	<u>687</u>
508.namd_r	128	281	433	274	444	<u>281</u>	<u>433</u>	256	<u>484</u>	<u>503</u>	484	502	482	504
510.parest_r	128	793	422	794	422	<u>793</u>	<u>422</u>	128	791	423	792	423	<u>791</u>	<u>423</u>
511.povray_r	128	457	654	<u>454</u>	<u>658</u>	454	659	256	752	795	755	792	<u>753</u>	<u>794</u>
519.lbm_r	128	781	173	782	173	<u>782</u>	<u>173</u>	64	<u>391</u>	<u>173</u>	391	173	390	173
521.wrf_r	128	599	479	<u>600</u>	<u>477</u>	603	476	128	599	479	600	478	<u>600</u>	<u>478</u>
526.blender_r	128	327	596	326	598	<u>327</u>	<u>597</u>	256	562	694	565	690	<u>562</u>	<u>694</u>
527.cam4_r	128	<u>393</u>	<u>569</u>	393	569	396	566	128	<u>393</u>	<u>569</u>	393	569	396	566
538.imagick_r	128	212	1500	206	1550	<u>212</u>	<u>1500</u>	256	303	2100	<u>303</u>	<u>2100</u>	303	2100
544.nab_r	128	259	831	<u>259</u>	<u>830</u>	260	830	256	<u>427</u>	<u>1010</u>	426	1010	428	1010
549.fotonik3d_r	128	2110	236	<u>2112</u>	<u>236</u>	2112	236	64	<u>1030</u>	<u>242</u>	1030	242	1031	242
554.roms_r	128	1017	200	1015	200	<u>1016</u>	<u>200</u>	64	436	233	434	234	<u>435</u>	<u>234</u>

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu

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Operating System Notes (Continued)

dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)

The kernel used in this result is a pre-released version of SLES15 SP1, Kernel 4.12.14-195.1. This kernel should be equivalent, or within the run rules allowing the use of pre-released software.

General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017/amd_rate_aocc200_rome_A_lib/64;/home/cpu2017/amd_rate_aocc200_rome_A_lib/32:"  
MALLOCCONF = "retain:true"
```

Binaries were compiled on a system with 2p AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC 9.1.0 in Ubuntu 19.04

with flags: -Ofast -march=znver2. jemalloc 5.2.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2>

Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling

Performance Determinism set to Power Deterministic

Processor Power and Utilization Monitoring set to Disabled

Minimum Processor Idle Power Core C-State set to C6 State

Memory Patrol Scrubbing set to Disabled

Workload Profile set to General Throughput Compute

Last-Level Cache (LLC) As NUMA Node set to Disabled

NUMA memory domains per socket set to Four memory domains per socket

Determinism Control set to Manual

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9

running on linux-p14n Sat Jul 20 03:49:36 2019

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

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Platform Notes (Continued)

From /proc/cpuinfo

model name : AMD EPYC 7702 64-Core Processor

2 "physical id"s (chips)

256 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 64

siblings : 128

physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55 56 57 58 59 60 61 62 63

physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55 56 57 58 59 60 61 62 63

From lscpu:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 48 bits physical, 48 bits virtual
CPU(s): 256
On-line CPU(s) list: 0-255
Thread(s) per core: 2
Core(s) per socket: 64
Socket(s): 2
NUMA node(s): 32
Vendor ID: AuthenticAMD
CPU family: 23
Model: 49
Model name: AMD EPYC 7702 64-Core Processor
Stepping: 0
CPU MHz: 2000.000
CPU max MHz: 2000.0000
CPU min MHz: 1500.0000
BogoMIPS: 3992.65
Virtualization: AMD-V
L1d cache: 32K
L1i cache: 32K
L2 cache: 512K
L3 cache: 16384K
NUMA node0 CPU(s): 0-3,128-131
NUMA node1 CPU(s): 4-7,132-135
NUMA node2 CPU(s): 8-11,136-139
NUMA node3 CPU(s): 12-15,140-143
NUMA node4 CPU(s): 16-19,144-147
NUMA node5 CPU(s): 20-23,148-151
NUMA node6 CPU(s): 24-27,152-155

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Platform Notes (Continued)

```

NUMA node7 CPU(s): 28-31,156-159
NUMA node8 CPU(s): 32-35,160-163
NUMA node9 CPU(s): 36-39,164-167
NUMA node10 CPU(s): 40-43,168-171
NUMA node11 CPU(s): 44-47,172-175
NUMA node12 CPU(s): 48-51,176-179
NUMA node13 CPU(s): 52-55,180-183
NUMA node14 CPU(s): 56-59,184-187
NUMA node15 CPU(s): 60-63,188-191
NUMA node16 CPU(s): 64-67,192-195
NUMA node17 CPU(s): 68-71,196-199
NUMA node18 CPU(s): 72-75,200-203
NUMA node19 CPU(s): 76-79,204-207
NUMA node20 CPU(s): 80-83,208-211
NUMA node21 CPU(s): 84-87,212-215
NUMA node22 CPU(s): 88-91,216-219
NUMA node23 CPU(s): 92-95,220-223
NUMA node24 CPU(s): 96-99,224-227
NUMA node25 CPU(s): 100-103,228-231
NUMA node26 CPU(s): 104-107,232-235
NUMA node27 CPU(s): 108-111,236-239
NUMA node28 CPU(s): 112-115,240-243
NUMA node29 CPU(s): 116-119,244-247
NUMA node30 CPU(s): 120-123,248-251
NUMA node31 CPU(s): 124-127,252-255

```

```

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl xtopology nonstop_tsc cpuid extd_apicid aperfmperf pni
pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx
f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse
3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_l2 mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd ibrs ibpb stibp vmmcall fsgsbase
bmi1 avx2 smep bmi2 cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec
xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf
xsaveerptr arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid
decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif umip rdpid
overflow_recov succor smca

```

```

/proc/cpuinfo cache data
cache size : 512 KB

```

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 32 nodes (0-31)
node 0 cpus: 0 1 2 3 128 129 130 131
node 0 size: 32034 MB
node 0 free: 31709 MB

```

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Platform Notes (Continued)

```

node 1 cpus: 4 5 6 7 132 133 134 135
node 1 size: 32254 MB
node 1 free: 31976 MB
node 2 cpus: 8 9 10 11 136 137 138 139
node 2 size: 32254 MB
node 2 free: 31978 MB
node 3 cpus: 12 13 14 15 140 141 142 143
node 3 size: 32253 MB
node 3 free: 31965 MB
node 4 cpus: 16 17 18 19 144 145 146 147
node 4 size: 32254 MB
node 4 free: 31980 MB
node 5 cpus: 20 21 22 23 148 149 150 151
node 5 size: 32254 MB
node 5 free: 31977 MB
node 6 cpus: 24 25 26 27 152 153 154 155
node 6 size: 32224 MB
node 6 free: 31943 MB
node 7 cpus: 28 29 30 31 156 157 158 159
node 7 size: 32253 MB
node 7 free: 31975 MB
node 8 cpus: 32 33 34 35 160 161 162 163
node 8 size: 32254 MB
node 8 free: 31975 MB
node 9 cpus: 36 37 38 39 164 165 166 167
node 9 size: 32254 MB
node 9 free: 31979 MB
node 10 cpus: 40 41 42 43 168 169 170 171
node 10 size: 32254 MB
node 10 free: 31977 MB
node 11 cpus: 44 45 46 47 172 173 174 175
node 11 size: 32253 MB
node 11 free: 31973 MB
node 12 cpus: 48 49 50 51 176 177 178 179
node 12 size: 32254 MB
node 12 free: 31976 MB
node 13 cpus: 52 53 54 55 180 181 182 183
node 13 size: 32254 MB
node 13 free: 31974 MB
node 14 cpus: 56 57 58 59 184 185 186 187
node 14 size: 32254 MB
node 14 free: 31980 MB
node 15 cpus: 60 61 62 63 188 189 190 191
node 15 size: 32241 MB
node 15 free: 31966 MB
node 16 cpus: 64 65 66 67 192 193 194 195
node 16 size: 32254 MB

```

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```

node 16 free: 31975 MB
node 17 cpus: 68 69 70 71 196 197 198 199
node 17 size: 32254 MB
node 17 free: 31977 MB
node 18 cpus: 72 73 74 75 200 201 202 203
node 18 size: 32254 MB
node 18 free: 31959 MB
node 19 cpus: 76 77 78 79 204 205 206 207
node 19 size: 32253 MB
node 19 free: 31976 MB
node 20 cpus: 80 81 82 83 208 209 210 211
node 20 size: 32254 MB
node 20 free: 31965 MB
node 21 cpus: 84 85 86 87 212 213 214 215
node 21 size: 32254 MB
node 21 free: 31963 MB
node 22 cpus: 88 89 90 91 216 217 218 219
node 22 size: 32254 MB
node 22 free: 31977 MB
node 23 cpus: 92 93 94 95 220 221 222 223
node 23 size: 32253 MB
node 23 free: 31982 MB
node 24 cpus: 96 97 98 99 224 225 226 227
node 24 size: 32254 MB
node 24 free: 31980 MB
node 25 cpus: 100 101 102 103 228 229 230 231
node 25 size: 32254 MB
node 25 free: 31982 MB
node 26 cpus: 104 105 106 107 232 233 234 235
node 26 size: 32254 MB
node 26 free: 31973 MB
node 27 cpus: 108 109 110 111 236 237 238 239
node 27 size: 32253 MB
node 27 free: 31982 MB
node 28 cpus: 112 113 114 115 240 241 242 243
node 28 size: 32254 MB
node 28 free: 31978 MB
node 29 cpus: 116 117 118 119 244 245 246 247
node 29 size: 32254 MB
node 29 free: 31980 MB
node 30 cpus: 120 121 122 123 248 249 250 251
node 30 size: 32254 MB
node 30 free: 31980 MB
node 31 cpus: 124 125 126 127 252 253 254 255
node 31 size: 32250 MB
node 31 free: 31981 MB
node distances:

```

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Platform Notes (Continued)

node	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19
20	21	22	23	24	25	26	27	28	29	30	31									
0:	10	11	11	11	12	12	12	12	12	12	12	12	12	12	12	12	32	32	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
1:	11	10	11	11	12	12	12	12	12	12	12	12	12	12	12	12	32	32	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
2:	11	11	10	11	12	12	12	12	12	12	12	12	12	12	12	12	32	32	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
3:	11	11	11	10	12	12	12	12	12	12	12	12	12	12	12	12	32	32	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
4:	12	12	12	12	10	11	11	11	12	12	12	12	12	12	12	12	32	32	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
5:	12	12	12	12	11	10	11	11	12	12	12	12	12	12	12	12	32	32	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
6:	12	12	12	12	11	11	10	11	12	12	12	12	12	12	12	12	32	32	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
7:	12	12	12	12	11	11	11	10	12	12	12	12	12	12	12	12	32	32	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
8:	12	12	12	12	12	12	12	12	12	10	11	11	11	11	12	12	32	32	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
9:	12	12	12	12	12	12	12	12	12	11	10	11	11	11	12	12	32	32	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
10:	12	12	12	12	12	12	12	12	11	11	10	11	11	11	12	12	32	32	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
11:	12	12	12	12	12	12	12	12	11	11	11	10	12	12	12	12	32	32	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
12:	12	12	12	12	12	12	12	12	12	12	12	12	12	12	10	11	11	11	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
13:	12	12	12	12	12	12	12	12	12	12	12	12	12	12	11	10	11	11	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
14:	12	12	12	12	12	12	12	12	12	12	12	12	12	12	11	11	10	11	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
15:	12	12	12	12	12	12	12	12	12	12	12	12	12	12	11	11	11	10	32	32
32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
16:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	10	11	11	11
12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12
17:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	11	10	11	11
12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12
18:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	11	11	10	11
12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12
19:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	11	11	11	10
12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12
20:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12
10	11	11	11	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12
21:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12
11	10	11	11	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12
22:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12

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Platform Notes (Continued)

```

11 11 10 11 12 12 12 12 12 12 12 12
23: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
11 11 11 10 12 12 12 12 12 12 12 12
24: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 10 11 11 11 12 12 12 12
25: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 11 10 11 11 12 12 12 12
26: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 11 11 10 11 12 12 12 12
27: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 11 11 11 10 12 12 12 12
28: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 12 12 12 12 10 11 11 11
29: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 12 12 12 12 11 10 11 11
30: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 12 12 12 12 11 11 10 11
31: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 12 12 12 12 11 11 11 10

```

From /proc/meminfo

```

MemTotal:      1056632380 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/*release* /etc/*version*

```

os-release:
NAME="SLE_HPC"
VERSION="15-SP1"
VERSION_ID="15.1"
PRETTY_NAME="SUSE Linux Enterprise High Performance Computing 15 SP1"
ID="sle_hpc"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sle_hpc:15:sp1"

```

uname -a:

```

Linux linux-pl4n 4.12.14-193-default #1 SMP Tue Apr 9 13:29:32 UTC 2019 (85c4156)
x86_64 x86_64 x86_64 GNU/Linux

```

Kernel self-reported vulnerability status:

```

CVE-2017-5754 (Meltdown):      Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline, IBPB: conditional,
IBRS_FW, STIBP: conditional, RSB filling

```

(Continued on next page)



SPEC CPU2017 Floating Point Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLaint DL385 Gen10

(2.00 GHz, AMD EPYC 7702)

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SPECrate2017_fp_peak = 540

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jul-2019
Hardware Availability: Aug-2019
Software Availability: Aug-2019

Platform Notes (Continued)

run-level 3 Jul 19 17:12

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdc2	btrfs	371G	20G	350G	6%	/home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE A40 07/15/2019

Memory:

16x UNKNOWN NOT AVAILABLE

16x UNKNOWN NOT AVAILABLE 64 GB 4 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

=====
CC 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
=====

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
=====

=====
CXXC 508.namd_r(base, peak) 510.parest_r(base, peak)
=====

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
=====

=====
FC 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base,
peak)
=====

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu

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Hardware Availability: Aug-2019
Software Availability: Aug-2019

Compiler Version Notes (Continued)

Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin

=====
CC 511.povray_r(base, peak) 521.wrf_r(base, peak) 526.blender_r(base, peak)
527.cam4_r(base, peak)

AOCCLLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
AOCCLLVM.2.0.0.B179.2019_06_12) (based on LLVM AOCCLLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
AOCCLLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
AOCCLLVM.2.0.0.B179.2019_06_12) (based on LLVM AOCCLLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin

=====
FC 507.cactuBSSN_r(base, peak)

AOCCLLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
AOCCLLVM.2.0.0.B179.2019_06_12) (based on LLVM AOCCLLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
AOCCLLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
AOCCLLVM.2.0.0.B179.2019_06_12) (based on LLVM AOCCLLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
AOCCLLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
AOCCLLVM.2.0.0.B179.2019_06_12) (based on LLVM AOCCLLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin

Base Compiler Invocation

C benchmarks:
clang

(Continued on next page)



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Base Compiler Invocation (Continued)

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

```

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -D__BOOL_DEFINED -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

```

Base Optimization Flags

C benchmarks:

```

-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -lmvec -lamdlibm -ljemalloc

```

(Continued on next page)



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Base Optimization Flags (Continued)

C benchmarks (continued):

-lflang

C++ benchmarks:

-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-partial-unswitch -z muldefs -lmvec -lamdlibm
-ljemalloc -lflang

Fortran benchmarks:

-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang

Benchmarks using both Fortran and C:

-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -funroll-loops -Mrecursive -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang

Benchmarks using both C and C++:

-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch -z muldefs
-lmvec -lamdlibm -ljemalloc -lflang

Benchmarks using Fortran, C, and C++:

-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize

(Continued on next page)



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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```

-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch
-funroll-loops -Mrecursive -z muldefs -Kieee -fno-finite-math-only
-lmvec -lamdlibm -ljemalloc -lflang

```

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags



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Test Date: Jul-2019

Hardware Availability: Aug-2019

Software Availability: Aug-2019

Peak Optimization Flags

C benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5 -mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -lmvec -lamdlibm -ljemalloc -lflang
```

C++ benchmarks:

```
508.namd_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -flv-function-specialization
-mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -lmvec -lamdlibm -ljemalloc
-lflang
```

```
510.parest_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -Ofast -march=znver2
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -lmvec -lamdlibm -ljemalloc
-lflang
```

Fortran benchmarks:

```
503.bwaves_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3
-march=znver2 -funroll-loops -Mrecursive
-mllvm -vector-library=LIBMVEC -Kieee
-fno-finite-math-only -lmvec -lamdlibm -ljemalloc
```

(Continued on next page)



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Peak Optimization Flags (Continued)

503.bwaves_r (continued):

-lflang

549.fotonik3d_r: Same as 503.bwaves_r

```
554.roms_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc
-lflang
```

Benchmarks using both Fortran and C:

```
521.wrf_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -O3 -funroll-loops
-Mrecursive -Kieee -fno-finite-math-only -lmvec
-lamdlibm -ljemalloc -lflang
```

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
```

(Continued on next page)



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Peak Optimization Flags (Continued)

511.povray_r (continued):

```
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000 -lmvec -lamdlibm
-ljemalloc -lflang
```

526.blender_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize

```
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000 -lmvec -lamdlibm
-ljemalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5 -mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch -mllvm -loop-unswitch-threshold=200000
-O3 -funroll-loops -Mrecursive -Kieee -fno-finite-math-only -lmvec
-lamdlibm -ljemalloc -lflang
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc200-flags-A1.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc200-flags-A1.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revE.xml>



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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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