



SPEC® CPU2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

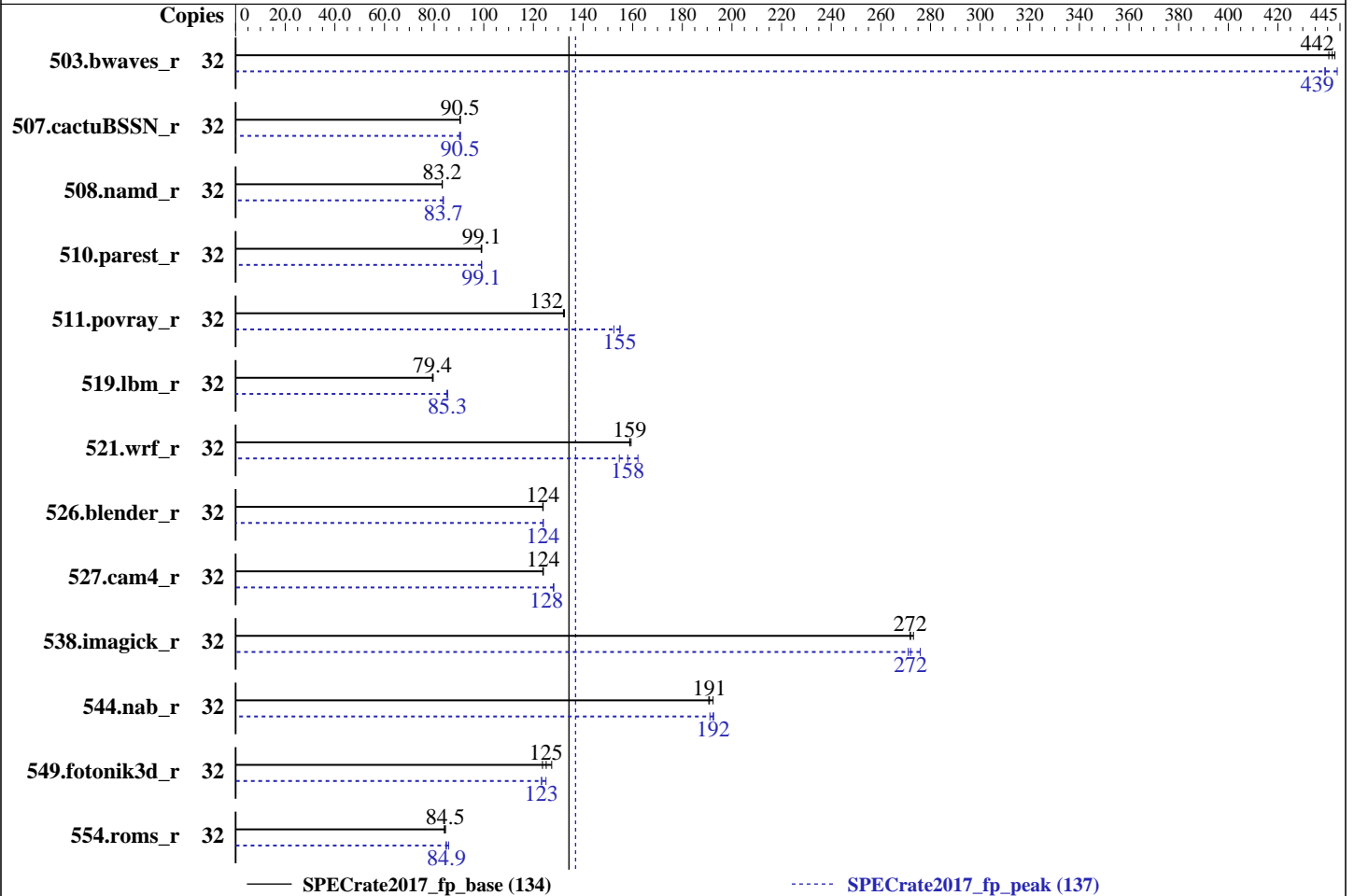
(3.20 GHz, Intel Xeon Gold 6134M)

SPECrate2017_fp_base = 134

SPECrate2017_fp_peak = 137

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Nov-2018
Hardware Availability: Oct-2018
Software Availability: Mar-2018



Hardware

CPU Name: Intel Xeon Gold 6134M
 Max MHz.: 3700
 Nominal: 3200
 Enabled: 16 cores, 2 chips, 2 threads/core
 Orderable: 1, 2 chip(s)
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 24.75 MB I+D on chip per chip
 Other: None
 Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)
 Storage: 1 x 600 GB SATA SSD, RAID 0
 Other: None

Software

OS: SUSE Linux Enterprise Server 12 (x86_64) SP3
 Kernel 4.4.120-94.17-default
 Compiler: C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux;
 Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux
 Parallel: No
 Firmware: HPE BIOS Version U32 10/15/2018 released Oct-2018
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: None



SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.20 GHz, Intel Xeon Gold 6134M)

SPECrate2017_fp_base = 134

SPECrate2017_fp_peak = 137

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Nov-2018
Hardware Availability: Oct-2018
Software Availability: Mar-2018

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	32	724	443	<u>726</u>	<u>442</u>	728	440	32	723	444	<u>731</u>	<u>439</u>	731	439
507.cactuBSSN_r	32	448	90.5	447	90.5	<u>448</u>	<u>90.5</u>	32	449	90.2	<u>448</u>	<u>90.5</u>	447	90.6
508.namd_r	32	<u>365</u>	<u>83.2</u>	365	83.3	365	83.2	32	363	83.7	<u>363</u>	<u>83.7</u>	364	83.5
510.parest_r	32	844	99.1	846	98.9	<u>845</u>	<u>99.1</u>	32	<u>845</u>	<u>99.1</u>	844	99.1	845	99.0
511.povray_r	32	564	132	566	132	<u>565</u>	<u>132</u>	32	490	152	482	155	<u>483</u>	<u>155</u>
519.lbm_r	32	425	79.3	<u>425</u>	<u>79.4</u>	424	79.6	32	395	85.4	<u>395</u>	<u>85.3</u>	396	85.2
521.wrf_r	32	451	159	<u>451</u>	<u>159</u>	450	159	32	464	155	<u>454</u>	<u>158</u>	442	162
526.blender_r	32	393	124	<u>394</u>	<u>124</u>	394	124	32	393	124	<u>393</u>	<u>124</u>	393	124
527.cam4_r	32	452	124	<u>452</u>	<u>124</u>	452	124	32	<u>437</u>	<u>128</u>	437	128	437	128
538.imagick_r	32	<u>293</u>	<u>272</u>	293	272	291	273	32	289	276	<u>293</u>	<u>272</u>	294	271
544.nab_r	32	282	191	280	192	<u>282</u>	<u>191</u>	32	282	191	<u>280</u>	<u>192</u>	280	193
549.fotonik3d_r	32	979	127	<u>996</u>	<u>125</u>	1009	124	32	1012	123	997	125	<u>1012</u>	<u>123</u>
554.roms_r	32	602	84.5	<u>602</u>	<u>84.5</u>	605	84.1	32	<u>599</u>	<u>84.9</u>	600	84.8	592	85.8

SPECrate2017_fp_base = 134

SPECrate2017_fp_peak = 137

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
IRQ balance service was stopped using "systemctl stop irqbalance.service"
Tuned-adm profile was set to Throughput-Performance using "tuned-adm profile throughput-performance"
Numa Balancing disabled using "echo 0 > /proc/sys/kernel/numa_balancing"
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
```

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

(Continued on next page)



SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.20 GHz, Intel Xeon Gold 6134M)

SPECrate2017_fp_base = 134

SPECrate2017_fp_peak = 137

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2018

Hardware Availability: Oct-2018

Software Availability: Mar-2018

General Notes (Continued)

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling

Memory Patrol Scrubbing set to Disabled

LLC Prefetch set to Enabled

LLC Dead Line Allocation set to Disabled

Stale A to S set to Disabled

Workload Profile set to General Throughput Compute

Minimum Processor Idle Power Core C-State set to C1E State

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9

running on dl360-8180-sles-mk Tue Nov 6 11:46:16 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6134M CPU @ 3.20GHz

2 "physical id"s (chips)

32 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 8

siblings : 16

physical 0: cores 0 2 3 9 16 19 26 27

physical 1: cores 0 2 3 9 16 19 26 27

From lscpu:

Architecture: x86_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

CPU(s): 32

On-line CPU(s) list: 0-31

Thread(s) per core: 2

(Continued on next page)



SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.20 GHz, Intel Xeon Gold 6134M)

SPECrate2017_fp_base = 134

SPECrate2017_fp_peak = 137

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Nov-2018
Hardware Availability: Oct-2018
Software Availability: Mar-2018

Platform Notes (Continued)

```

Core(s) per socket:      8
Socket(s):               2
NUMA node(s):           4
Vendor ID:               GenuineIntel
CPU family:              6
Model:                   85
Model name:              Intel(R) Xeon(R) Gold 6134M CPU @ 3.20GHz
Stepping:                4
CPU MHz:                 3192.517
BogoMIPS:                6385.03
Virtualization:         VT-x
L1d cache:               32K
L1i cache:               32K
L2 cache:                1024K
L3 cache:                25344K
NUMA node0 CPU(s):      0-3,16-19
NUMA node1 CPU(s):      4-7,20-23
NUMA node2 CPU(s):      8-11,24-27
NUMA node3 CPU(s):      12-15,28-31
Flags:                   fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 sse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
dtherm intel_pt rsb_ctxsw spec_ctrl stibp retpoline kaiser tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmil hle avx2 smep bmi2 erms invpcid rtm
cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl
xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc pku ospke

```

```

/proc/cpuinfo cache data
cache size : 25344 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 16 17 18 19
node 0 size: 193096 MB
node 0 free: 192706 MB
node 1 cpus: 4 5 6 7 20 21 22 23
node 1 size: 193534 MB
node 1 free: 193105 MB
node 2 cpus: 8 9 10 11 24 25 26 27
node 2 size: 193534 MB
node 2 free: 193367 MB
node 3 cpus: 12 13 14 15 28 29 30 31
node 3 size: 193534 MB

```

(Continued on next page)



SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.20 GHz, Intel Xeon Gold 6134M)

SPECrate2017_fp_base = 134

SPECrate2017_fp_peak = 137

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Nov-2018
Hardware Availability: Oct-2018
Software Availability: Mar-2018

Platform Notes (Continued)

```
node 3 free: 193352 MB
node distances:
node   0   1   2   3
  0:  10  21  31  31
  1:  21  10  31  31
  2:  31  31  10  21
  3:  31  31  21  10
```

From /proc/meminfo

```
MemTotal:      792268840 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

From /etc/*release* /etc/*version*

SuSE-release:

```
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
```

```
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
```

os-release:

```
NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

uname -a:

```
Linux dl360-8180-sles-mk 4.4.120-94.17-default #1 SMP Wed Mar 14 17:23:00 UTC 2018
(cf3a7bb) x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2017-5754 (Meltdown):      Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB
```

run-level 3 Nov 6 11:44

SPEC is set to: /home/cpu2017

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   517G  45G  472G   9% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow

(Continued on next page)



SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.20 GHz, Intel Xeon Gold 6134M)

SPECrate2017_fp_base = 134

SPECrate2017_fp_peak = 137

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2018

Hardware Availability: Oct-2018

Software Availability: Mar-2018

Platform Notes (Continued)

hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U32 10/15/2018

Memory:

4x HPE 840758-091 32 GB 2 rank 2666

20x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

=====
CC 519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base, peak)

icc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
CC 519.lbm_r(peak)

icc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
CXXC 508.namd_r(base) 510.parest_r(base, peak)

icpc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
CXXC 508.namd_r(peak)

icpc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
CC 511.povray_r(base) 526.blender_r(base, peak)

icpc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

icc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

(Continued on next page)



SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.20 GHz, Intel Xeon Gold 6134M)

SPECrate2017_fp_base = 134

SPECrate2017_fp_peak = 137

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2018

Hardware Availability: Oct-2018

Software Availability: Mar-2018

Compiler Version Notes (Continued)

=====
CC 511.povray_r(peak)

icpc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

icc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
FC 507.cactuBSSN_r(base, peak)

icpc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

icc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

ifort (IFORT) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
FC 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)

ifort (IFORT) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
FC 554.roms_r(peak)

ifort (IFORT) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
CC 521.wrf_r(base) 527.cam4_r(base)

ifort (IFORT) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

icc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
CC 521.wrf_r(peak) 527.cam4_r(peak)

(Continued on next page)



SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.20 GHz, Intel Xeon Gold 6134M)

SPECrate2017_fp_base = 134

SPECrate2017_fp_peak = 137

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2018

Hardware Availability: Oct-2018

Software Availability: Mar-2018

Compiler Version Notes (Continued)

ifort (IFORT) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

icc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icc -m64 -std=c11

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:

icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:

icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64

507.cactuBSSN_r: -DSPEC_LP64

508.namd_r: -DSPEC_LP64

510.parest_r: -DSPEC_LP64

511.povray_r: -DSPEC_LP64

519.lbm_r: -DSPEC_LP64

521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian

526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char

527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG

538.imagick_r: -DSPEC_LP64

544.nab_r: -DSPEC_LP64

549.fotonik3d_r: -DSPEC_LP64

554.roms_r: -DSPEC_LP64



SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.20 GHz, Intel Xeon Gold 6134M)

SPECrate2017_fp_base = 134

SPECrate2017_fp_peak = 137

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2018

Hardware Availability: Oct-2018

Software Availability: Mar-2018

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

Benchmarks using both C and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3
```

Benchmarks using Fortran, C, and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64 -std=c11
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:

```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
icpc -m64 icc -m64 -std=c11 ifort -m64
```



SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.20 GHz, Intel Xeon Gold 6134M)

SPECrate2017_fp_base = 134

SPECrate2017_fp_peak = 137

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2018

Hardware Availability: Oct-2018

Software Availability: Mar-2018

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3
```

```
538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3
```

544.nab_r: Same as 538.imagick_r

C++ benchmarks:

```
508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3
```

```
510.parest_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -auto
-nostandard-realloc-lhs
```

549.fotonik3d_r: Same as 503.bwaves_r

```
554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

Benchmarks using both Fortran and C:

```
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

Benchmarks using both C and C++:

(Continued on next page)



SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.20 GHz, Intel Xeon Gold 6134M)

SPECrate2017_fp_base = 134

SPECrate2017_fp_peak = 137

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2018

Hardware Availability: Oct-2018

Software Availability: Mar-2018

Peak Optimization Flags (Continued)

```
511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3
```

```
526.blender_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3
```

Benchmarks using Fortran, C, and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html>

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2018-11-06 01:16:15-0500.

Report generated on 2019-02-05 13:14:46 by CPU2017 PDF formatter v6067.

Originally published on 2019-02-05.