SPEC® CPU2017 Integer Rate Result

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Huawei

Huawei 2488H V5 (Intel Xeon Gold 6138)

SPECrate2017_int_base = 383

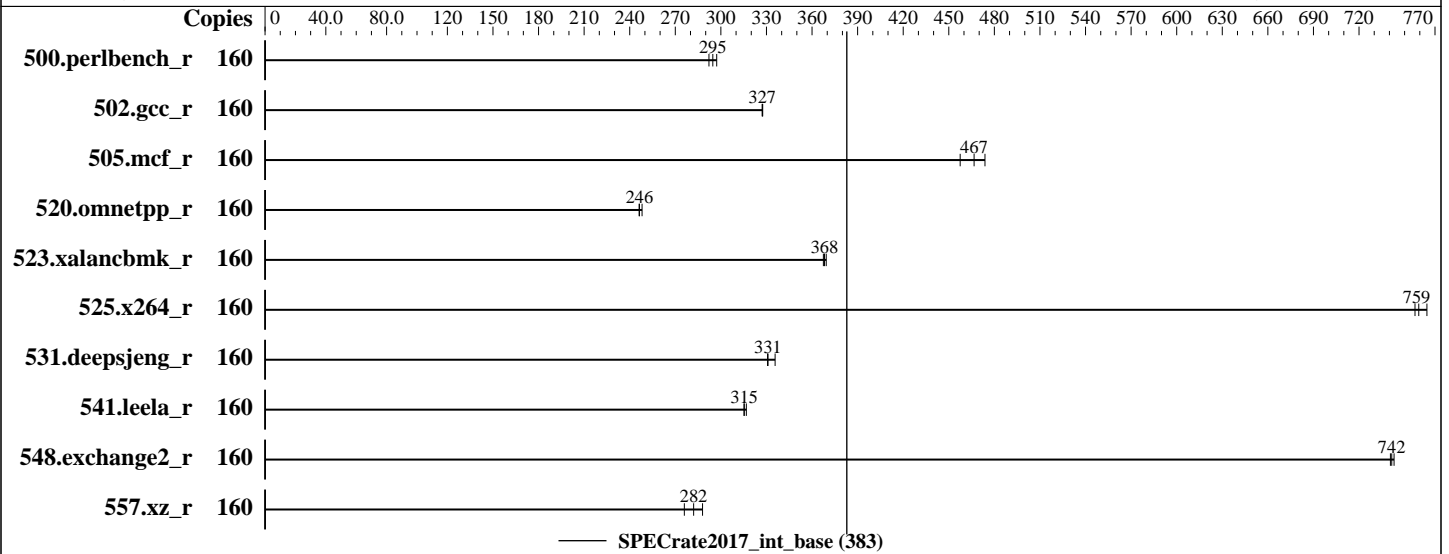SPECrate2017_int_peak = Not Run

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: May-2018

Hardware Availability: Feb-2018

Software Availability: Mar-2018

Copies — scale: 0, 40.0, 80.0, 120, 150, 180, 210, 240, 270, 300, 330, 360, 390, 420, 450, 480, 510, 540, 570, 600, 630, 660, 690, 720, 770

Benchmark	Copies	Result
500.perlbench_r	160	295
502.gcc_r	160	327
505.mcf_r	160	467
520.omnetpp_r	160	246
523.xalancbmk_r	160	368
525.x264_r	160	759
531.deepsjeng_r	160	331
541.leela_r	160	315
548.exchange2_r	160	742
557.xz_r	160	282

—— SPECrate2017_int_base (383)

Hardware

CPU Name: Intel Xeon Gold 6138

 Max MHz.: 3700

 Nominal: 2000

Enabled: 80 cores, 4 chips, 2 threads/core

Orderable: 2,4 chips

Cache L1: 32 KB I + 32 KB D on chip per core

 L2: 1 MB I+D on chip per core

 L3: 27.5 MB I+D on chip per chip

 Other: None

Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)

Storage: 1 x 900 GB SAS HDD 10K RPM, RAID 0

Other: None

Software

OS: SUSE Linux Enterprise Server 12 SP2

4.4.120-92.70-default

Compiler: C/C++: Version 18.0.0.128 of Intel C/C++

Compiler for Linux;

Fortran: Version 18.0.0.128 of Intel Fortran

Compiler for Linux

Parallel: No

Firmware: Version 0.80 released Feb-2018

File System: ext4

System State: Run level 5 (multi-user)

Base Pointers: 64-bit

Peak Pointers: Not Applicable

Other: jemalloc: jemalloc memory allocator library V5.0.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	160	872	292	857	297	**864**	**295**							
502.gcc_r	160	692	328	**692**	**327**	692	327							
505.mcf_r	160	546	474	**554**	**467**	565	458							
520.omnetpp_r	160	853	246	**852**	**246**	846	248							
523.xalancbmk_r	160	**459**	**368**	460	367	457	369							
525.x264_r	160	366	765	**369**	**759**	370	757							
531.deepsjeng_r	160	554	331	**554**	**331**	546	336							
541.leela_r	160	841	315	**840**	**315**	836	317							
548.exchange2_r	160	**565**	**742**	564	743	566	741							
557.xz_r	160	600	288	**613**	**282**	626	276							
SPECrate2017_int_base =	**383**													
SPECrate2017_int_peak =	**Not Run**													

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>   /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

jemalloc: configured and built at default for
32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4,

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General Notes (Continued)

and the system compiler gcc 4.8.5;
jemalloc: sources avilable from jemalloc.net or
https://github.com/jemalloc/jemalloc/releases;
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Platform Notes

```
 BIOS configuration:
 Sub NUMA Cluster (SNC) set to enabled
 IMC (Integrated memory controller) Interleaving set to 1 way interleave
 Xtended Prediction Table (XPT) Prefetch set to Enable
 Memory Patrol Scrub set to Disable
 Last Level Cache (LLC) Prefetch set to Disable
 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
 running on linux-oyf8 Tue May 29 03:16:43 2018

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 6138 CPU @ 2.00GHz
       4  "physical id"s (chips)
       160 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 20
       siblings  : 40
       physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
       physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
       physical 2: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
       physical 3: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

 From lscpu:
    Architecture:          x86_64
    CPU op-mode(s):        32-bit, 64-bit
    Byte Order:            Little Endian
    CPU(s):                160
    On-line CPU(s) list:   0-159
    Thread(s) per core:    2
```

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Platform Notes (Continued)

```
       Core(s) per socket:    20
       Socket(s):             4
       NUMA node(s):          8
       Vendor ID:             GenuineIntel
       CPU family:            6
       Model:                 85
       Model name:            Intel(R) Xeon(R) Gold 6138 CPU @ 2.00GHz
       Stepping:              4
       CPU MHz:               2001.000
       CPU max MHz:           2001.0000
       CPU min MHz:           1000.0000
       BogoMIPS:              4000.01
       Virtualization:        VT-x
       L1d cache:             32K
       L1i cache:             32K
       L2 cache:              1024K
       L3 cache:              28160K
       NUMA node0 CPU(s):     0-2,5,6,10-12,15,16,80-82,85,86,90-92,95,96
       NUMA node1 CPU(s):     3,4,7-9,13,14,17-19,83,84,87-89,93,94,97-99
       NUMA node2 CPU(s):     20-22,25,26,30-32,35,36,100-102,105,106,110-112,115,116
       NUMA node3 CPU(s):     23,24,27-29,33,34,37-39,103,104,107-109,113,114,117-119
       NUMA node4 CPU(s):     40-42,45,46,50-52,55,56,120-122,125,126,130-132,135,136
       NUMA node5 CPU(s):     43,44,47-49,53,54,57-59,123,124,127-129,133,134,137-139
       NUMA node6 CPU(s):     60-62,65,66,70-72,75,76,140-142,145,146,150-152,155,156
       NUMA node7 CPU(s):     63,64,67-69,73,74,77-79,143,144,147-149,153,154,157-159
       Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
     pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
     lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
     aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
     fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
     xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
     dtherm intel_pt rsb_ctxsw spec_ctrl stibp retpoline kaiser tpr_shadow vnmi
     flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
     cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl
     xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

  /proc/cpuinfo cache data
     cache size : 28160 KB

  From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
     available: 8 nodes (0-7)
     node 0 cpus: 0 1 2 5 6 10 11 12 15 16 80 81 82 85 86 90 91 92 95 96
     node 0 size: 191756 MB
     node 0 free: 190131 MB
     node 1 cpus: 3 4 7 8 9 13 14 17 18 19 83 84 87 88 89 93 94 97 98 99
     node 1 size: 193524 MB
```

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Platform Notes (Continued)

```
  node 1 free: 193021 MB
  node 2 cpus: 20 21 22 25 26 30 31 32 35 36 100 101 102 105 106 110 111 112 115 116
  node 2 size: 193524 MB
  node 2 free: 192759 MB
  node 3 cpus: 23 24 27 28 29 33 34 37 38 39 103 104 107 108 109 113 114 117 118 119
  node 3 size: 193524 MB
  node 3 free: 193030 MB
  node 4 cpus: 40 41 42 45 46 50 51 52 55 56 120 121 122 125 126 130 131 132 135 136
  node 4 size: 193524 MB
  node 4 free: 192963 MB
  node 5 cpus: 43 44 47 48 49 53 54 57 58 59 123 124 127 128 129 133 134 137 138 139
  node 5 size: 193524 MB
  node 5 free: 193065 MB
  node 6 cpus: 60 61 62 65 66 70 71 72 75 76 140 141 142 145 146 150 151 152 155 156
  node 6 size: 193524 MB
  node 6 free: 193019 MB
  node 7 cpus: 63 64 67 68 69 73 74 77 78 79 143 144 147 148 149 153 154 157 158 159
  node 7 size: 193367 MB
  node 7 free: 192871 MB
  node distances:
  node   0   1   2   3   4   5   6   7
    0:  10  20  20  20  20  20  20  20
    1:  20  10  20  20  20  20  20  20
    2:  20  20  10  20  20  20  20  20
    3:  20  20  20  10  20  20  20  20
    4:  20  20  20  20  10  20  20  20
    5:  20  20  20  20  20  10  20  20
    6:  20  20  20  20  20  20  10  20
    7:  20  20  20  20  20  20  20  10

From /proc/meminfo
   MemTotal:       1583380332 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 2
      # This file is deprecated and will be removed in a future service pack or release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
```

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Platform Notes (Continued)

```
        VERSION="12-SP2"
        VERSION_ID="12.2"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12:sp2"

 uname -a:
    Linux linux-oyf8 4.4.120-92.70-default #1 SMP Wed Mar 14 15:59:43 UTC 2018 (52a83de)
    x86_64 x86_64 x86_64 GNU/Linux

 run-level 5 May 28 10:39

 SPEC is set to: /home/cpu2017
    Filesystem      Type   Size  Used Avail Use% Mounted on
    /dev/sda4       btrfs  736G   14G  721G   2% /home

 Additional information from dmidecode follows.  WARNING: Use caution when you interpret
 this section. The 'dmidecode' program reads system data which is "intended to allow
 hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   BIOS INSYDE Corp. 0.80 02/24/2018
   Memory:
    48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
 CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
      557.xz_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
 CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
      541.leela_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
```

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Compiler Version Notes (Continued)

```
  FC  548.exchange2_r(base)
  -----------------------------------------------------------------------
  ifort (IFORT) 18.0.0 20170811
  Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
  -----------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Base Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
`-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

C++ benchmarks:
`-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

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Base Optimization Flags (Continued)

Fortran benchmarks:
```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

Base Other Flags

C benchmarks:
```
-m64 -std=c11
```

C++ benchmarks:
```
-m64
```

Fortran benchmarks:
```
-m64
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-05-28 15:16:42-0400.
Report generated on 2018-10-31 17:27:34 by CPU2017 PDF formatter v6067.
Originally published on 2018-06-26.