



# SPEC® CINT2006 Result

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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL560 Gen10  
(3.60 GHz, Intel Xeon Gold 5122)

**SPECint®2006 = Not Run**

**SPECint\_base2006 = 72.1**

**CPU2006 license:** 3

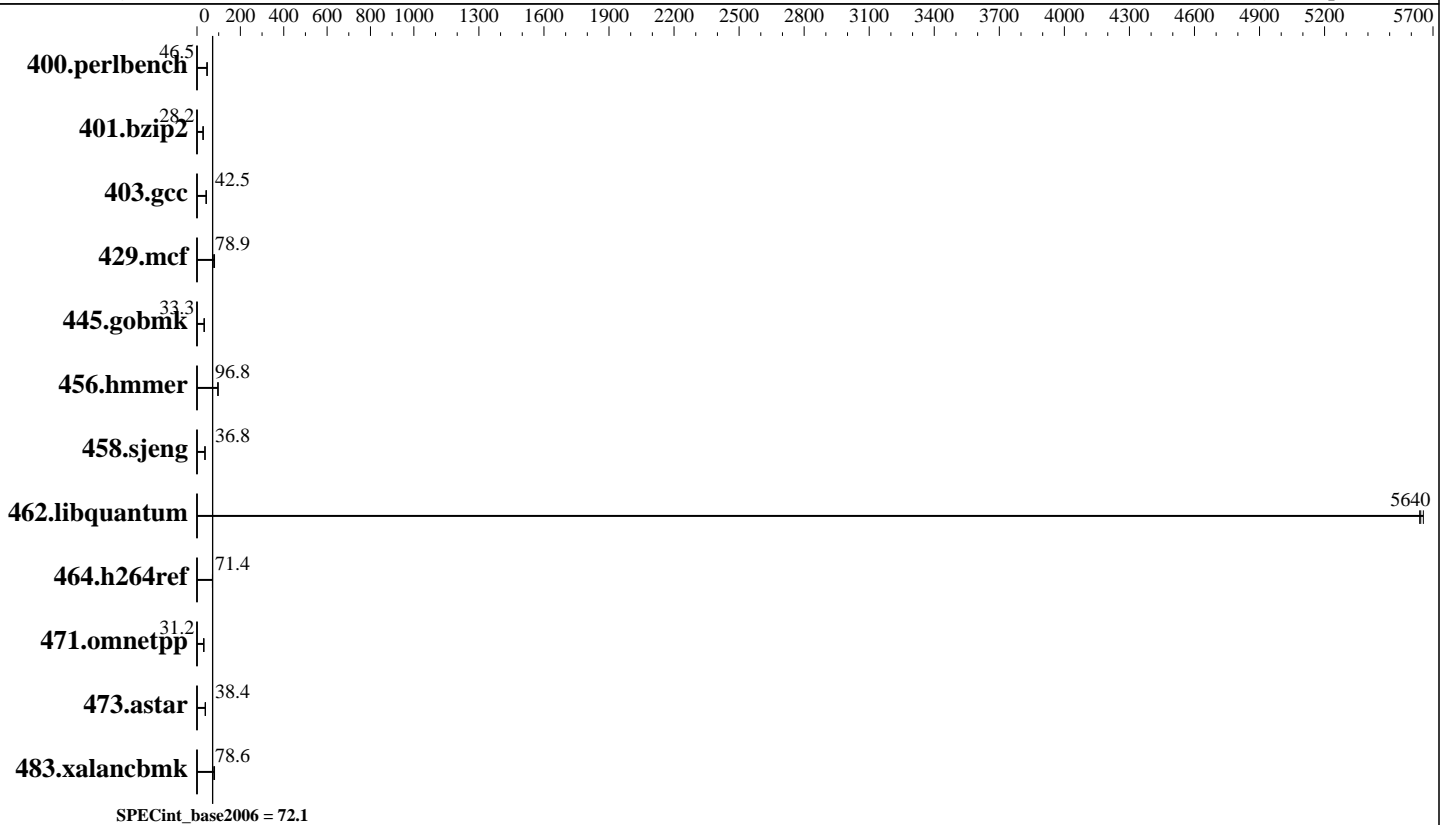
**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Nov-2017

**Hardware Availability:** Oct-2017

**Software Availability:** Apr-2017



## Hardware

**CPU Name:** Intel Xeon Gold 5122  
**CPU Characteristics:** Intel Turbo Boost Technology up to 3.70 GHz  
**CPU MHz:** 3600  
**FPU:** Integrated  
**CPU(s) enabled:** 16 cores, 4 chips, 4 cores/chip  
**CPU(s) orderable:** 1, 2, 4 chip(s)  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 1 MB I+D on chip per core  
**L3 Cache:** 16.5 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 384 GB (48 x 8 GB 2Rx8 PC4-2666V-R)  
**Disk Subsystem:** 1 x 960 GB SATA SSD, RAID 0  
**Other Hardware:** None

## Software

**Operating System:** Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 Kernel 3.10.0-514.el7.x86\_64  
**Compiler:** C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux  
**Auto Parallel:** Yes  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32/64-bit  
**Peak Pointers:** Not Applicable  
**Other Software:** Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	210	46.5	210	46.5	<b><u>210</u></b>	<b><u>46.5</u></b>						
401.bzip2	342	28.2	<b><u>342</u></b>	<b><u>28.2</u></b>	342	28.2						
403.gcc	<b><u>189</u></b>	<b><u>42.5</u></b>	189	42.5	190	42.4						
429.mcf	113	80.9	<b><u>116</u></b>	<b><u>78.9</u></b>	118	77.4						
445.gobmk	<b><u>315</u></b>	<b><u>33.3</u></b>	315	33.3	315	33.3						
456.hammer	96.7	96.5	96.3	96.9	<b><u>96.4</u></b>	<b><u>96.8</u></b>						
458.sjeng	<b><u>328</u></b>	<b><u>36.8</u></b>	328	36.9	329	36.8						
462.libquantum	3.66	5660	3.67	5640	<b><u>3.67</u></b>	<b><u>5640</u></b>						
464.h264ref	310	71.3	310	71.5	<b><u>310</u></b>	<b><u>71.4</u></b>						
471.omnetpp	201	31.2	200	31.3	<b><u>200</u></b>	<b><u>31.2</u></b>						
473.astar	182	38.6	<b><u>183</u></b>	<b><u>38.4</u></b>	183	38.4						
483.xalancbmk	85.3	80.9	88.1	78.3	<b><u>87.8</u></b>	<b><u>78.6</u></b>						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Filesystem page cache cleared with:  
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run  
IRQ balance service was stop using "service irqbalance stop"  
Tuned-adm profile was set to Throughput-Performance

## Platform Notes

### BIOS Configuration:

Intel Hyperthreading set to Disabled  
Thermal Configuration set to Maximum Cooling  
LLC Prefetch set to Enabled  
LLC Dead Line Allocation set to Disabled  
Stale A to S set to Enabled  
Memory Patrol Scrubbing set to Disabled  
Workload Profile set to General Peak Frequency Compute  
Energy/Performance Bias set to Maximum Performance  
Workload Profile set to Custom  
NUMA Group Size Optimization set to Flat  
Sysinfo program /home/cpu2006/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on DL560G10 Wed Nov 22 04:10:32 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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## Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5122 CPU @ 3.60GHz
 4 "physical id"s (chips)
16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 1 5 9 13
  physical 1: cores 0 5 9 13
  physical 2: cores 1 5 9 13
  physical 3: cores 1 2 5 11
cache size : 16896 KB

```

```

From /proc/meminfo
MemTotal:      395931392 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server

```

```

uname -a:
Linux DL560G10 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 22 03:50

```

SPEC is set to: /home/cpu2006
Filesystem                Type      Size  Used Avail Use% Mounted on
/dev/mapper/rhel_dl560g10-home xfs      839G   37G  803G   5% /home
Additional information from dmidecode:

```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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## Platform Notes (Continued)

Memory:

48x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch  
-auto-p32

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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