SPEC® CINT2006 Result

Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 480 Gen10
(2.60 GHz, Intel Xeon Silver 4112)

$SPECint®\_rate2006 =$ Not Run
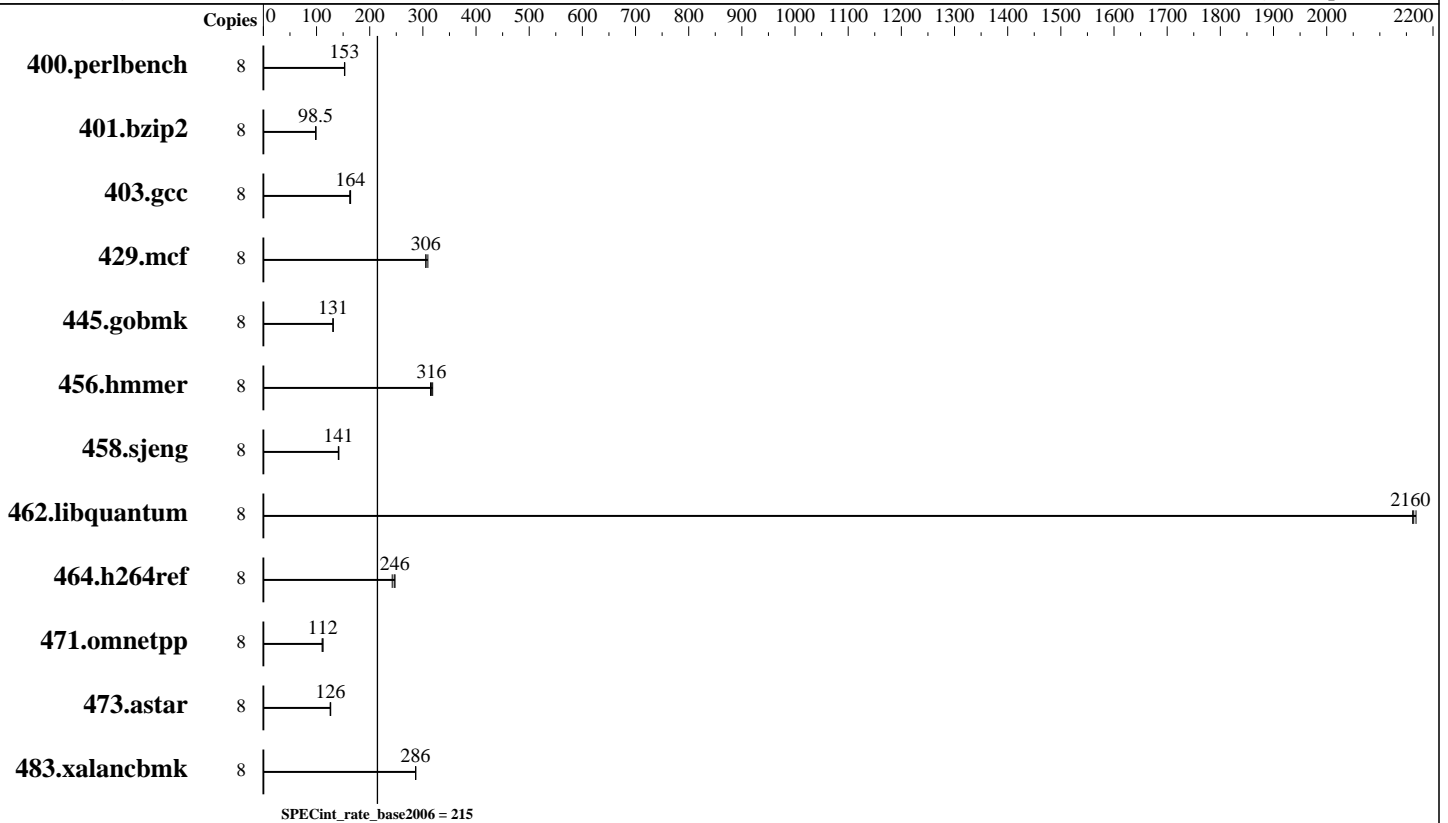
$SPECint\_rate\_base2006 =$ 215

CPU2006 license: 3	**Test date:** Dec-2017
Test sponsor: HPE	**Hardware Availability:** Oct-2017
Tested by: HPE	**Software Availability:** Apr-2017

Copies

Benchmark	Copies	Result
400.perlbench	8	153
401.bzip2	8	98.5
403.gcc	8	164
429.mcf	8	306
445.gobmk	8	131
456.hmmer	8	316
458.sjeng	8	141
462.libquantum	8	2160
464.h264ref	8	246
471.omnetpp	8	112
473.astar	8	126
483.xalancbmk	8	286

SPECint_rate_base2006 = 215

Hardware

CPU Name:	Intel Xeon Silver 4112
CPU Characteristics:	Intel Turbo Boost Technology up to 3.00 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chip(s)
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	8.25 MB I+D on chip per chip
Other Cache:	None
Memory:	192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)
Disk Subsystem:	1 x 960 GB SATA SSD, RAID 0
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 12 (x86_64) SP2 Kernel 4.4.21-69-default
Compiler:	C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux
Auto Parallel:	No
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V10.2

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Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	8	510	153	**512**	**153**	513	152							
401.bzip2	8	781	98.9	785	98.3	**784**	**98.5**							
403.gcc	8	397	162	**393**	**164**	393	164							
429.mcf	8	239	306	236	309	**239**	**306**							
445.gobmk	8	640	131	640	131	**640**	**131**							
456.hmmer	8	234	318	**236**	**316**	237	314							
458.sjeng	8	682	142	687	141	**684**	**141**							
462.libquantum	8	76.7	2160	76.5	2170	**76.6**	**2160**							
464.h264ref	8	**720**	**246**	715	248	731	242							
471.omnetpp	8	453	110	**447**	**112**	447	112							
473.astar	8	443	127	446	126	**446**	**126**							
483.xalancbmk	8	**193**	**286**	193	286	193	287							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The numactl mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate numactl commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"
    Transparent Huge Pages enabled by default
    Filesystem page cache cleared with:
     shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
    runspec command invoked through numactl i.e.:
     numactl --interleave=all runspec <etc>
    irqbalance disabled with "service irqbalance stop"
    tuned profile set wtih "tuned-adm profile throughput-performance"
    VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
    Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

Platform Notes

    BIOS Configuration:
     Thermal Configuration set to Maximum Cooling
     LLC Prefetch set to Enabled
     LLC Dead Line Allocation set to Disabled
     Memory Patrol Scrubbing set to Disabled
     Workload Profile set to General Throughput Compute
      Minimum Processor Idle Power Core C-State set to C1E State
     Workload Profile set to Custom

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Platform Notes (Continued)

```
  Sub-NUMA Cluster set to Disabled
Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-vjuj Thu Dec  7 16:26:30 2017

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
      1 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 8
      physical 0: cores 1 2 4 5
   cache size : 8448 KB

From /proc/meminfo
   MemTotal:      197753052 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 2
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP2"
      VERSION_ID="12.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux linux-vjuj 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
   (9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 7 16:20
```

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Platform Notes (Continued)

```
   SPEC is set to: /home/cpu2006
      Filesystem      Type  Size  Used Avail Use% Mounted on
      /dev/sda4       xfs   852G   18G  835G   3% /home
   Additional information from dmidecode:

      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS HPE I42 09/27/2017
    Memory:
    12x UNKNOWN NOT AVAILABLE
    12x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz, configured at 2400 MHz

   (End of data from sysinfo program)
   Regarding the sysinfo display about the memory installed, the correct amount of
   memory is 192 GB and the dmidecode description should have one line reading as:
      12x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz, configured at 2400 MHz
```

General Notes

```
   Environment variables set by runspec before the start of the run:
   LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

    Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
    memory using Redhat Enterprise Linux 7.2
```

Base Compiler Invocation

C benchmarks:
```
     icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

C++ benchmarks:
```
     icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

Base Portability Flags

```
    400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
       401.bzip2: -D_FILE_OFFSET_BITS=64
         403.gcc: -D_FILE_OFFSET_BITS=64
         429.mcf: -D_FILE_OFFSET_BITS=64
      445.gobmk: -D_FILE_OFFSET_BITS=64
      456.hmmer: -D_FILE_OFFSET_BITS=64
      458.sjeng: -D_FILE_OFFSET_BITS=64
   462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

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Base Portability Flags (Continued)

```
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
   473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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