



# SPEC<sup>®</sup> CFP2006 Result

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## Dell Inc.

PowerEdge M640 (Intel Xeon Platinum 8160, 2.10 GHz)

SPECfp<sup>®</sup>\_rate2006 = 1510

SPECfp\_rate\_base2006 = 1460

CPU2006 license: 55

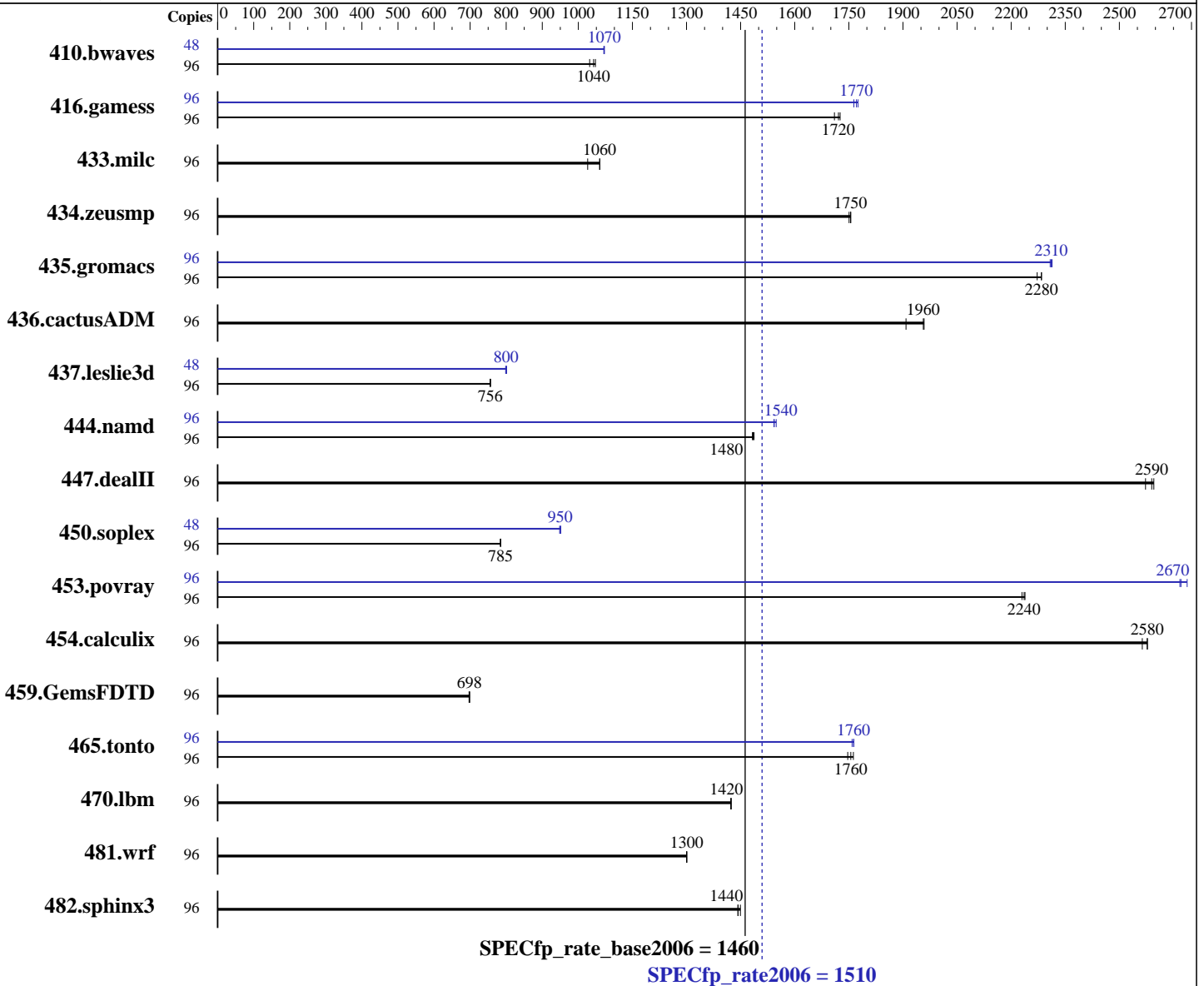
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Sep-2017

Hardware Availability: Sep-2017

Software Availability: Nov-2016



### Hardware

CPU Name: Intel Xeon Platinum 8160  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 48 cores, 2 chips, 24 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 SP3 (x86\_64) 4.4.70-2-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
 Auto Parallel: Yes  
 File System: btrfs  
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 33 MB I+D on chip per chip  
Other Cache: None  
Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R)  
Disk Subsystem: 1 x 960 GB SATA SSD  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	96	1265	1030	<u>1252</u>	<u>1040</u>	1246	1050	48	609	1070	608	1070	<u>609</u>	<u>1070</u>
416.gamess	96	1089	1730	1099	1710	<u>1092</u>	<u>1720</u>	96	<u>1061</u>	<u>1770</u>	1066	1760	1059	1780
433.milc	96	859	1030	832	1060	<u>832</u>	<u>1060</u>	96	859	1030	832	1060	<u>832</u>	<u>1060</u>
434.zeusmp	96	499	1750	<u>498</u>	<u>1750</u>	498	1760	96	499	1750	<u>498</u>	<u>1750</u>	498	1760
435.gromacs	96	300	2280	<u>300</u>	<u>2280</u>	302	2270	96	297	2310	296	2310	<u>297</u>	<u>2310</u>
436.cactusADM	96	601	1910	<u>586</u>	<u>1960</u>	586	1960	96	601	1910	<u>586</u>	<u>1960</u>	586	1960
437.leslie3d	96	1193	757	<u>1194</u>	<u>756</u>	1194	756	48	565	799	563	801	<u>564</u>	<u>800</u>
444.namd	96	518	1490	<u>519</u>	<u>1480</u>	519	1480	96	<u>499</u>	<u>1540</u>	497	1550	499	1540
447.dealII	96	<u>424</u>	<u>2590</u>	427	2570	423	2600	96	<u>424</u>	<u>2590</u>	427	2570	423	2600
450.soplex	96	<u>1020</u>	<u>785</u>	1020	785	1022	784	48	<u>421</u>	<u>950</u>	422	948	421	951
453.povray	96	229	2230	228	2240	<u>228</u>	<u>2240</u>	96	190	2690	<u>191</u>	<u>2670</u>	191	2670
454.calculix	96	<u>307</u>	<u>2580</u>	307	2580	309	2560	96	<u>307</u>	<u>2580</u>	307	2580	309	2560
459.GemsFDTD	96	1457	699	1461	697	<u>1459</u>	<u>698</u>	96	1457	699	1461	697	<u>1459</u>	<u>698</u>
465.tonto	96	541	1750	536	1760	<u>538</u>	<u>1760</u>	96	<u>536</u>	<u>1760</u>	537	1760	536	1760
470.lbm	96	926	1420	<u>927</u>	<u>1420</u>	927	1420	96	926	1420	<u>927</u>	<u>1420</u>	927	1420
481.wrf	96	824	1300	<u>825</u>	<u>1300</u>	825	1300	96	824	1300	<u>825</u>	<u>1300</u>	825	1300
482.sphinx3	96	<u>1297</u>	<u>1440</u>	1297	1440	1291	1450	96	<u>1297</u>	<u>1440</u>	1297	1440	1291	1450

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
Sub NUMA Cluster enabled  
Virtualization Technology disabled

Continued on next page



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PowerEdge M640 (Intel Xeon Platinum 8160, 2.10 GHz)

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## Platform Notes (Continued)

System Profile set to Custom  
 CPU Performance set to Maximum Performance  
 C States set to autonomous  
 C1E disabled  
 Uncore Frequency set to Dynamic  
 Energy Efficiency Policy set to Performance  
 Memory Patrol Scrub disabled  
 Logical Processor enabled  
 CPU Interconnect Bus Link Power Management disabled  
 PCI ASPM L1 Link Power Management disabled  
 Sysinfo program /root/cpu2006-1.2\_ic17u3/config/sysinfo.rev6993  
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
 running on linux-8d7c Wed Sep 13 11:28:55 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8160 CPU @ 2.10GHz
 2 "physical id"s (chips)
 96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 24
  siblings  : 48
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
 27 28 29
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
 27 28 29
cache size : 33792 KB
```

```
From /proc/meminfo
MemTotal:      196681728 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
```

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## Platform Notes (Continued)

```
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```
uname -a:
Linux linux-8d7c 4.4.70-2-default #1 SMP Wed Jun 7 15:12:06 UTC 2017
(4502c76) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 12 23:33
```

```
SPEC is set to: /root/cpu2006-1.2_ic17u3
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       btrfs 855G  7.0G 846G   1% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS Dell Inc. 1.0.0 08/10/2017
Memory:
12x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666 MHz
4x Not Specified Not Specified
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/root/cpu2006-1.2\_ic17u3/lib/ia32:/root/cpu2006-1.2\_ic17u3/lib/intel64:/root/cpu2006-1.2\_ic17u3/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2  
Transparent Huge Pages enabled by default  
Filesystem page cache cleared with:  
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64  
  
Fortran benchmarks:  
ifort -m64

Continued on next page



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## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

C++ benchmarks:  
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

Fortran benchmarks:  
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:  
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:  
icc -m64

Continued on next page



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## Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

`icpc -m64`

`450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

`433.milc: basepeak = yes`

`470.lbm: basepeak = yes`

`482.sphinx3: basepeak = yes`

C++ benchmarks:

`444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)`  
`-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)`  
`-no-prec-div(pass 2) -fno-alias -auto-ilp32`  
`-qopt-mem-layout-trans=3`

Continued on next page



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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-malloc-options=3  
-qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

### Fortran benchmarks:

410.bwaves: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: basepeak = yes

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
-qopt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml>

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