



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]_rate2006 = Not Run

PowerEdge M630 (Intel Xeon E5-2690 v4, 2.60 GHz)

SPECfp_rate_base2006 = 935

CPU2006 license: 55

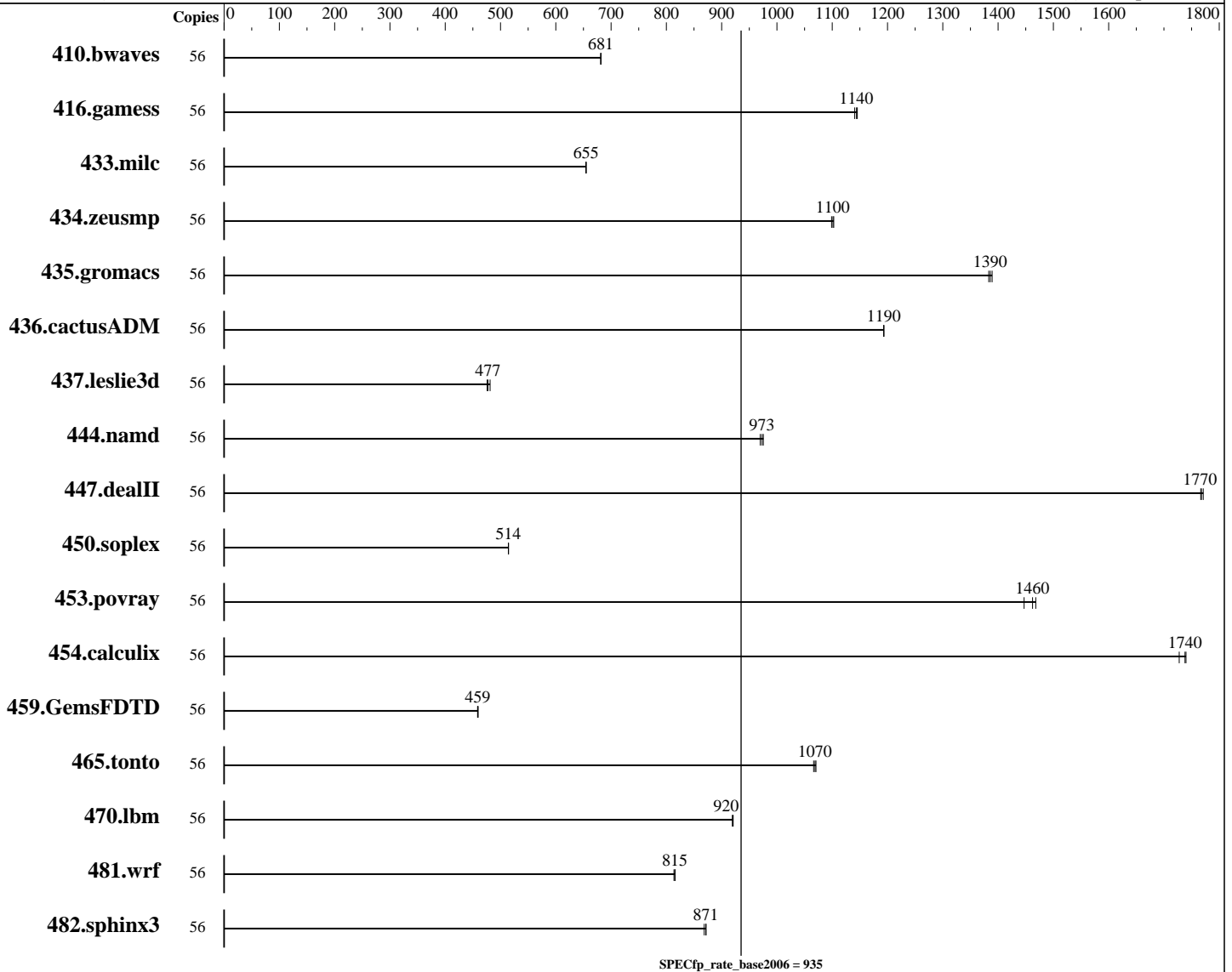
Test date: Jan-2017

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Sep-2016



Hardware

CPU Name: Intel Xeon E5-2690 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 12 SP1
 3.12.48-1-default
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++
 Compiler for Linux;
 Fortran: Version 17.0.0.098 of Intel Fortran
 Compiler for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx8 PC4-2400T-R)
Disk Subsystem: 300 GB 10000 RPM SAS
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	56	1116	682	<u>1117</u>	<u>681</u>	1117	681							
416.gamess	56	957	1150	<u>959</u>	<u>1140</u>	961	1140							
433.milc	56	786	654	<u>785</u>	<u>655</u>	785	655							
434.zeusmp	56	462	1100	<u>463</u>	<u>1100</u>	464	1100							
435.gromacs	56	288	1390	289	1380	<u>288</u>	<u>1390</u>							
436.cactusADM	56	561	1190	<u>561</u>	<u>1190</u>	561	1190							
437.leslie3d	56	1094	481	1106	476	<u>1103</u>	<u>477</u>							
444.namd	56	460	975	<u>462</u>	<u>973</u>	463	970							
447.dealII	56	363	1770	362	1770	<u>362</u>	<u>1770</u>							
450.soplex	56	<u>908</u>	<u>514</u>	908	515	908	514							
453.povray	56	<u>204</u>	<u>1460</u>	206	1450	203	1470							
454.calculix	56	<u>266</u>	<u>1740</u>	266	1740	267	1730							
459.GemsFDTD	56	<u>1295</u>	<u>459</u>	1293	460	1296	459							
465.tonto	56	515	1070	<u>516</u>	<u>1070</u>	517	1070							
470.lbm	56	<u>836</u>	<u>920</u>	836	921	837	919							
481.wrf	56	<u>767</u>	<u>815</u>	767	816	769	814							
482.sphinx3	56	1257	868	<u>1252</u>	<u>871</u>	1252	872							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Snoop Mode set to Cluster on Die
Virtualization Technology disabled

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Platform Notes (Continued)

System Profile set to Custom
 CPU Performance set to Maximum Performance
 C States set to Autonomous
 C1E disabled
 Energy Efficient Turbo enabled
 Uncore Frequency set to Dynamic
 Energy Efficiency Policy set to Balanced Performance
 Memory Patrol Scrub disabled
 Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6993
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
 running on linux Mon Jan 23 08:09:57 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2690 v4@ 2.60GHz
 2 "physical id"s (chips)
 56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 14
  siblings     : 28
  physical 0   : cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1   : cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size     : 17920 KB
```

```
From /proc/meminfo
MemTotal:      264567592 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
```

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Platform Notes (Continued)

CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:

```
Linux linux 3.12.48-1-default #1 SMP Fri Sep 18 13:49:47 UTC 2015 (a83966d)
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jan 23 02:10

SPEC is set to: /root/cpu2006-1.2

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        xfs   271G  12G  259G   5% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 2.3.5 11/10/2016

Memory:

```
16x 00CE00B300CE M393A2K43BB1-CRC 16 GB 2 rank 2400 MHz
8x Not Specified Not Specified
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh10.2"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge13G-revE.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge13G-revE.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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