SPEC® CINT2006 Result
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Dell Inc.

PowerEdge R730 (Intel Xeon E5-2609 v4, 1.70 GHz)

SPECint®2006 = 37.4

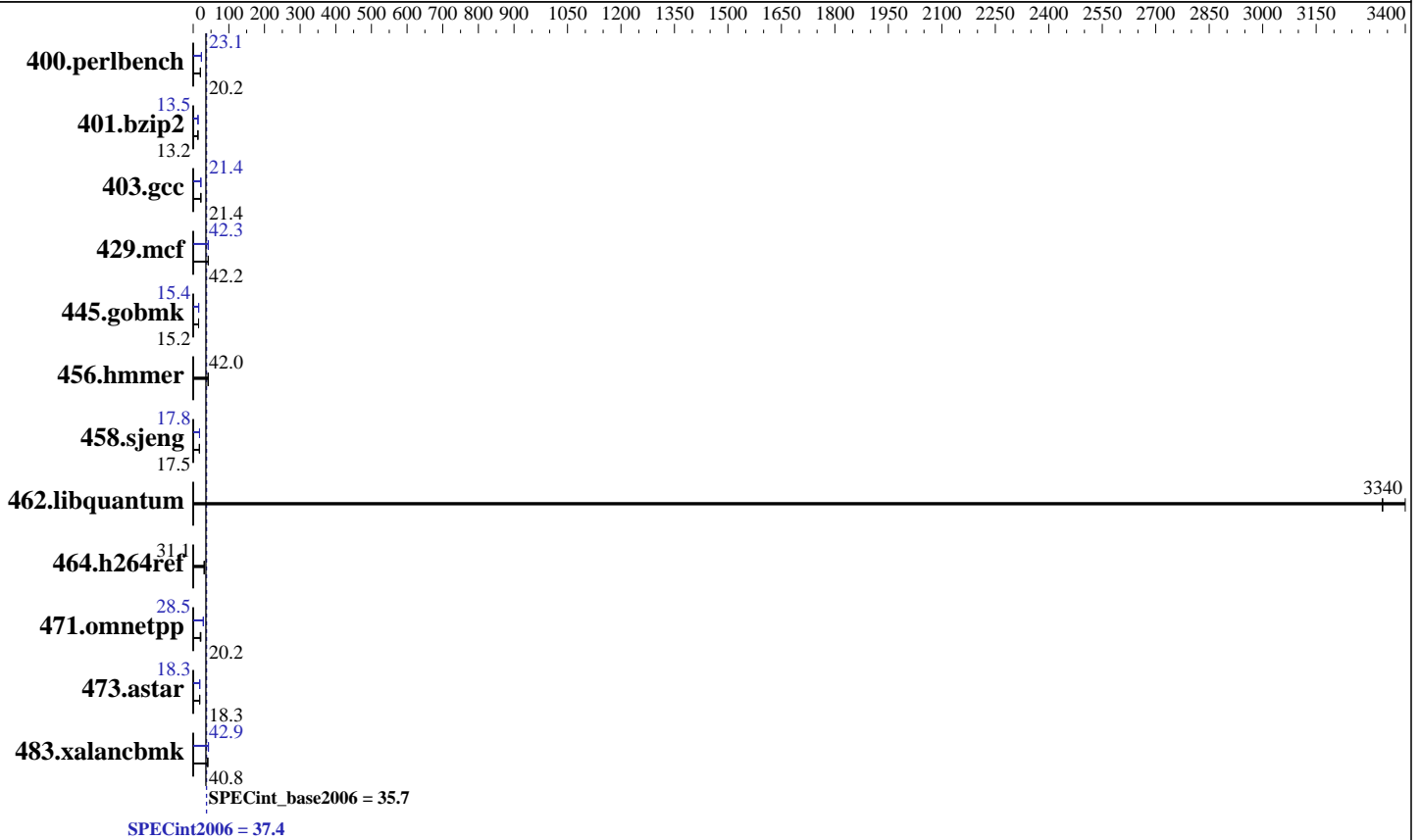SPECint_base2006 = 35.7

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Feb-2017
Hardware Availability: Oct-2016
Software Availability: Sep-2016

Scale: 0 100 200 300 400 500 600 700 800 900 1050 1200 1350 1500 1650 1800 1950 2100 2250 2400 2550 2700 2850 3000 3150 3400

- 400.perlbench: 23.1 / 20.2
- 401.bzip2: 13.5 / 13.2
- 403.gcc: 21.4 / 21.4
- 429.mcf: 42.3 / 42.2
- 445.gobmk: 15.4 / 15.2
- 456.hmmer: 42.0
- 458.sjeng: 17.8 / 17.5
- 462.libquantum: 3340
- 464.h264ref: 31.1
- 471.omnetpp: 28.5 / 20.2
- 473.astar: 18.3 / 18.3
- 483.xalancbmk: 42.9 / 40.8

SPECint_base2006 = 35.7

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Hardware

CPU Name:	Intel Xeon E5-2609 v4
CPU Characteristics:	
CPU MHz:	1700
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	20 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx8 PC4-2400T-R, running at 1866 MHz)
Disk Subsystem:	2 x 2000 GB 7200 RPM SAS RAID 0
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 12 3.12.28-4-default
Compiler:	C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	483	20.2	482	20.3	**483**	**20.2**	424	23.1	422	23.2	**422**	**23.1**
401.bzip2	726	13.3	**729**	**13.2**	729	13.2	**715**	**13.5**	714	13.5	716	13.5
403.gcc	**376**	**21.4**	376	21.4	376	21.4	**376**	**21.4**	377	21.4	375	21.5
429.mcf	217	42.1	**216**	**42.2**	214	42.5	216	42.2	**215**	**42.3**	214	42.7
445.gobmk	**691**	**15.2**	691	15.2	691	15.2	682	15.4	682	15.4	**682**	**15.4**
456.hmmer	**222**	**42.0**	222	42.0	222	42.1	**222**	**42.0**	222	42.0	222	42.1
458.sjeng	**692**	**17.5**	692	17.5	693	17.5	680	17.8	680	17.8	**680**	**17.8**
462.libquantum	**6.21**	**3340**	6.21	3340	6.09	3400	**6.21**	**3340**	6.21	3340	6.09	3400
464.h264ref	712	31.1	**713**	**31.1**	715	30.9	712	31.1	**713**	**31.1**	715	30.9
471.omnetpp	**309**	**20.2**	312	20.0	289	21.6	220	28.4	219	28.5	**219**	**28.5**
473.astar	**385**	**18.3**	386	18.2	384	18.3	384	18.3	**383**	**18.3**	383	18.3
483.xalancbmk	168	41.1	**169**	**40.8**	171	40.4	162	42.5	161	42.9	**161**	**42.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```
BIOS settings:
Snoop Mode set to Opportunistic Snoop Broadcast
Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Energy Efficient Turbo disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Sysinfo program /root/previous-cpu2006-1.2/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-0171 Mon Feb 27 16:10:36 2017

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo
```

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Platform Notes (Continued)

```
From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2609 v4@ 1.70GHz
      2 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 8
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7
   cache size : 20480 KB

From /proc/meminfo
   MemTotal:       264567484 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 0
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12"
      VERSION_ID="12"
      PRETTY_NAME="SUSE Linux Enterprise Server 12"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
   Linux linux-0171 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
   (9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 27 14:02

SPEC is set to: /root/previous-cpu2006-1.2
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      ext4  246G   19G  226G   8% /
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
```

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Platform Notes (Continued)

```
   hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS Dell Inc. 2.3.4 11/08/2016
   Memory:
    7x 00AD063200AD HMA82GR7MFR8N-UH 16 GB 2 rank 2400 MHz, configured at 1866
    MHz
    9x 00CE00B300CE M393A2K43BB1-CRC 16 GB 2 rank 2400 MHz, configured at 1866
    MHz
    8x Not Specified Not Specified

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/previous-cpu2006-1.2/libs/32:/root/previous-cpu2006-1.2/libs/64:/root/previous-cpu2006-1.2/sh10.2"
OMP_NUM_THREADS = "16"
 The Dell PowerEdge R730 and the PowerEdge R730xd models are electronically equivalent.
 The results have been measured on a Dell PowerEdge R730xd model.
 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
     icc -m64
```

C++ benchmarks:
```
     icpc -m64
```

Base Portability Flags

```
   400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
       401.bzip2: -DSPEC_CPU_LP64
         403.gcc: -DSPEC_CPU_LP64
         429.mcf: -DSPEC_CPU_LP64
       445.gobmk: -DSPEC_CPU_LP64
       456.hmmer: -DSPEC_CPU_LP64
       458.sjeng: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
      464.h264ref: -DSPEC_CPU_LP64
      471.omnetpp: -DSPEC_CPU_LP64
        473.astar: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch -auto-p32`

C++ benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

400.perlbench: `icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32`

445.gobmk: `icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32`

C++ benchmarks (except as noted below):
`icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-D_FILE_OFFSET_BITS=64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`

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Peak Portability Flags (Continued)

```
    462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
       464.h264ref: -DSPEC_CPU_LP64
       471.omnetpp: -D_FILE_OFFSET_BITS=64
          473.astar: -DSPEC_CPU_LP64
     483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
                   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -qopt-prefetch

       401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
                   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div -auto-ilp32 -qopt-prefetch

          403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
                   -qopt-malloc-options=3 -auto-ilp32

          429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
                   -qopt-prefetch -auto-p32

        445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
                   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2)

       456.hmmer: basepeak = yes

        458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
                   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -unroll4

    462.libquantum: basepeak = yes

       464.h264ref: basepeak = yes
```

C++ benchmarks:

```
    471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
                   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -qopt-ra-region-strategy=block
                   -Wl,-z,muldefs -L/sh10.2 -lsmartheap

          473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
                   -auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64
```

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Peak Optimization Flags (Continued)

483.xalancbmk: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch`
`-Wl,-z,muldefs -L/sh10.2 -lsmartheap`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge13G-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge13G-revE.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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