



SPEC[®] CFP2006 Result

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Lenovo Group Limited

Lenovo ThinkServer RS160
(2.10 GHz, Intel Xeon E3-1240L v5)

SPECfp[®]2006 = **87.6**

SPECfp_base2006 = **85.4**

CPU2006 license: 9017

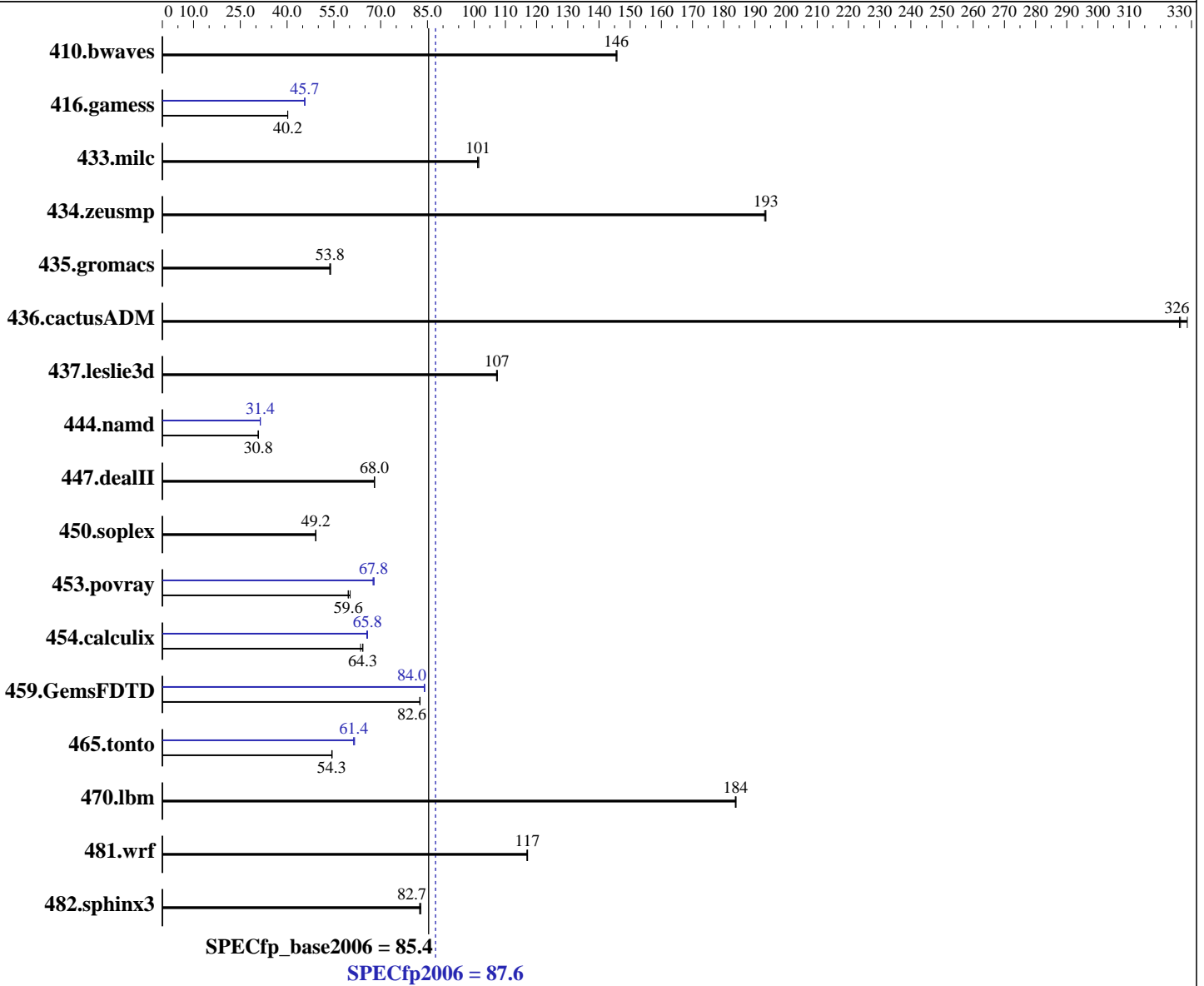
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Nov-2016

Hardware Availability: Sep-2016

Software Availability: Mar-2016



Hardware

CPU Name: Intel Xeon E3-1240L v5
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 7.2
 Kernel 3.10.0-327.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (2 x 8 GB 2Rx8 PC4-2133P-E)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	93.2	146	93.4	146	<u>93.4</u>	<u>146</u>	93.2	146	93.4	146	<u>93.4</u>	<u>146</u>
416.gamess	487	40.2	<u>487</u>	<u>40.2</u>	487	40.2	429	45.6	429	45.7	<u>429</u>	<u>45.7</u>
433.milc	<u>90.7</u>	<u>101</u>	90.5	101	90.8	101	<u>90.7</u>	<u>101</u>	90.5	101	90.8	101
434.zeusmp	47.0	194	<u>47.0</u>	<u>193</u>	47.1	193	47.0	194	<u>47.0</u>	<u>193</u>	47.1	193
435.gromacs	132	54.0	133	53.7	<u>133</u>	<u>53.8</u>	132	54.0	133	53.7	<u>133</u>	<u>53.8</u>
436.cactusADM	<u>36.6</u>	<u>326</u>	36.6	326	36.4	329	<u>36.6</u>	<u>326</u>	36.6	326	36.4	329
437.leslie3d	<u>87.6</u>	<u>107</u>	87.5	107	87.7	107	<u>87.6</u>	<u>107</u>	87.5	107	87.7	107
444.namd	<u>261</u>	<u>30.8</u>	260	30.8	261	30.7	<u>255</u>	<u>31.4</u>	255	31.4	255	31.4
447.dealII	<u>168</u>	<u>68.0</u>	168	68.0	168	68.0	<u>168</u>	<u>68.0</u>	168	68.0	168	68.0
450.soplex	170	49.1	<u>169</u>	<u>49.2</u>	169	49.3	170	49.1	<u>169</u>	<u>49.2</u>	169	49.3
453.povray	<u>89.2</u>	<u>59.6</u>	88.3	60.2	89.3	59.6	78.8	67.5	<u>78.5</u>	<u>67.8</u>	78.4	67.9
454.calculix	128	64.3	<u>128</u>	<u>64.3</u>	130	63.6	125	65.8	<u>125</u>	<u>65.8</u>	126	65.6
459.GemsFDTD	<u>128</u>	<u>82.6</u>	129	82.6	128	82.6	<u>126</u>	<u>84.0</u>	126	84.0	126	84.1
465.tonto	181	54.5	181	54.3	<u>181</u>	<u>54.3</u>	<u>160</u>	<u>61.4</u>	160	61.4	160	61.6
470.lbm	<u>74.8</u>	<u>184</u>	74.8	184	74.7	184	<u>74.8</u>	<u>184</u>	74.8	184	74.7	184
481.wrf	95.4	117	95.6	117	<u>95.4</u>	<u>117</u>	95.4	117	95.6	117	<u>95.4</u>	<u>117</u>
482.sphinx3	<u>236</u>	<u>82.7</u>	236	82.5	235	82.9	<u>236</u>	<u>82.7</u>	236	82.5	235	82.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS configuration:
Hyper-Threading set to Disabled
Boot performance mode set to Turbo Performance
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on RS160 Thu Dec 8 04:33:58 2016

This section contains SUT (System Under Test) info as seen by
Continued on next page



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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E3-1240L v5 @ 2.10GHz
  1 "physical id"s (chips)
  4 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 4
    siblings  : 4
    physical 0: cores 0 1 2 3
  cache size : 8192 KB

```

```

From /proc/meminfo
MemTotal:      16376016 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

```

```

uname -a:
Linux RS160 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015 x86_64
x86_64 x86_64 GNU/Linux

```

run-level 3 Dec 7 23:38

```

SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda5        xfs   687G  4.0G  683G   1% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO VB1TS152 09/29/2016

Memory:

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Platform Notes (Continued)

2x Not Specified Not Specified
2x Samsung M391A1G43EB1-CPB 8 GB 2 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-alloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.xml>

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