



SPEC[®] CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 480 Gen9
(2.10 GHz, Intel Xeon E5-2695 v4)

SPECfp[®]2006 = 116

SPECfp_base2006 = 111

CPU2006 license: 3

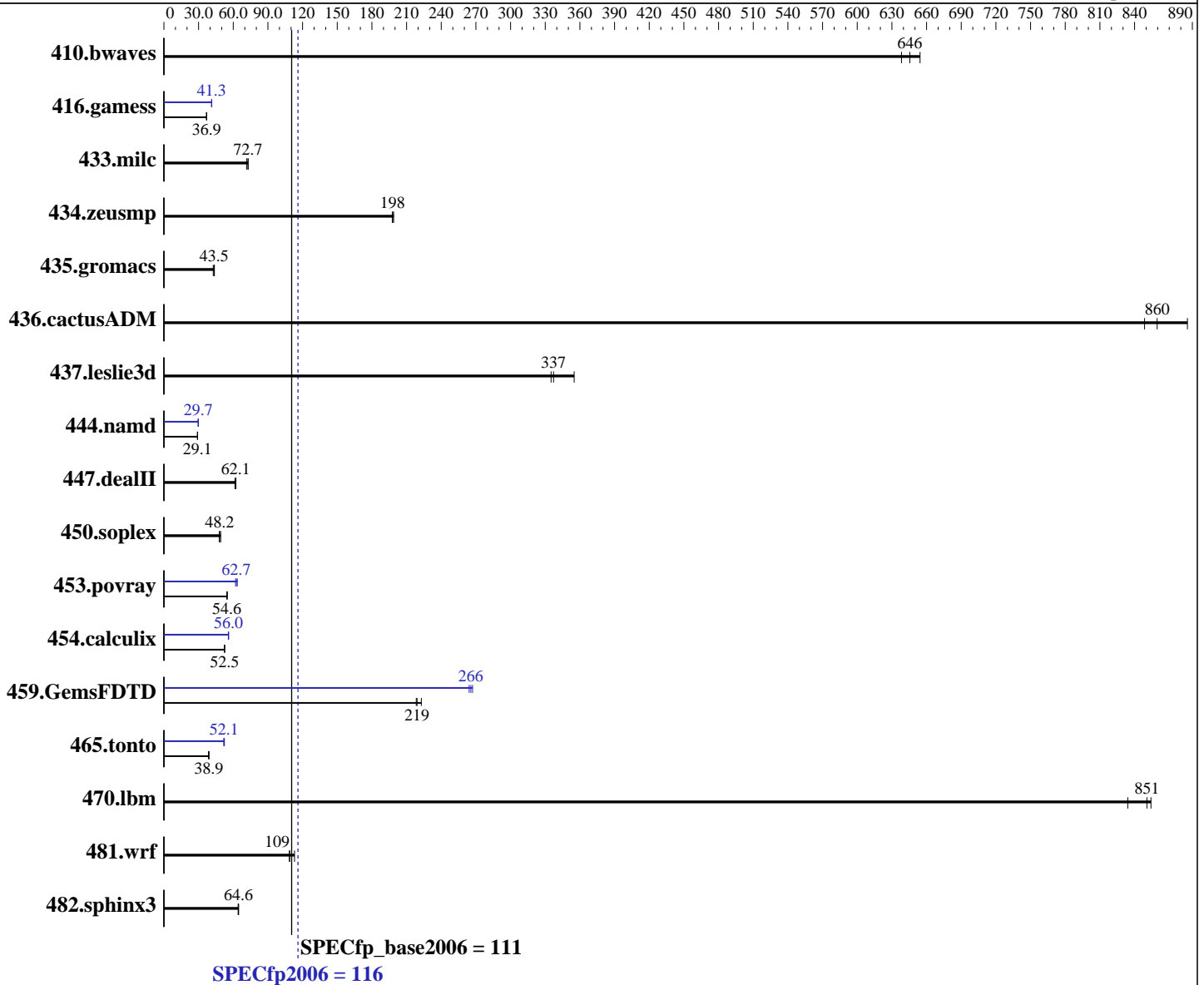
Test sponsor: HPE

Tested by: HPE

Test date: Nov-2016

Hardware Availability: Dec-2016

Software Availability: Sep-2016



Hardware

CPU Name: Intel Xeon E5-2695 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP1
 Kernel 3.12.49-11-default
 Compiler: C/C++: Version 17.0.0.098 of Intel C++ Studio XE for Linux;
 Fortran: Version 17.0.0.098 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 480 Gen9

(2.10 GHz, Intel Xeon E5-2695 v4)

SPECfp2006 = 116

SPECfp_base2006 = 111

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Nov-2016

Hardware Availability: Dec-2016

Software Availability: Sep-2016

L3 Cache: 45 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 600 GB 10 K SAS, RAID 0
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	20.8	654	<u>21.1</u>	<u>646</u>	21.3	638	20.8	654	<u>21.1</u>	<u>646</u>	21.3	638
416.gamess	530	36.9	530	36.9	<u>530</u>	<u>36.9</u>	472	41.4	<u>474</u>	<u>41.3</u>	475	41.3
433.milc	128	71.5	126	72.9	<u>126</u>	<u>72.7</u>	128	71.5	126	72.9	<u>126</u>	<u>72.7</u>
434.zeusmp	<u>46.0</u>	<u>198</u>	46.0	198	45.8	199	<u>46.0</u>	<u>198</u>	46.0	198	45.8	199
435.gromacs	163	43.7	167	42.7	<u>164</u>	<u>43.5</u>	163	43.7	167	42.7	<u>164</u>	<u>43.5</u>
436.cactusADM	<u>13.9</u>	<u>860</u>	13.5	886	14.1	849	<u>13.9</u>	<u>860</u>	13.5	886	14.1	849
437.leslie3d	28.1	335	26.5	355	<u>27.9</u>	<u>337</u>	28.1	335	26.5	355	<u>27.9</u>	<u>337</u>
444.namd	276	29.1	<u>276</u>	<u>29.1</u>	276	29.1	270	29.7	<u>270</u>	<u>29.7</u>	270	29.7
447.dealII	186	61.5	<u>184</u>	<u>62.1</u>	184	62.3	186	61.5	<u>184</u>	<u>62.1</u>	184	62.3
450.soplex	174	47.9	170	49.1	<u>173</u>	<u>48.2</u>	174	47.9	170	49.1	<u>173</u>	<u>48.2</u>
453.povray	<u>97.5</u>	<u>54.6</u>	97.6	54.5	97.0	54.8	83.7	63.5	85.8	62.0	<u>84.9</u>	<u>62.7</u>
454.calculix	157	52.5	157	52.4	<u>157</u>	<u>52.5</u>	148	55.7	<u>147</u>	<u>56.0</u>	147	56.0
459.GemsFDTD	47.6	223	<u>48.4</u>	<u>219</u>	48.6	218	39.7	267	40.2	264	<u>39.9</u>	<u>266</u>
465.tonto	<u>253</u>	<u>38.9</u>	255	38.6	253	39.0	188	52.4	<u>189</u>	<u>52.1</u>	190	51.8
470.lbm	16.5	834	<u>16.2</u>	<u>851</u>	16.1	854	16.5	834	<u>16.2</u>	<u>851</u>	16.1	854
481.wrf	98.8	113	103	109	<u>103</u>	<u>109</u>	98.8	113	103	109	<u>103</u>	<u>109</u>
482.sphinx3	304	64.2	302	64.6	<u>302</u>	<u>64.6</u>	304	64.2	302	64.6	<u>302</u>	<u>64.6</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration:
Power Profile set to Custom
Power Regulator to Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C6 State
Minimum Processor Idle Power Package C-State set to No Package State
Energy/Performance Bias set to Maximum Performance
Collaborative Power Control set to Disabled
QPI Snoop Configuration set to Home Snoop

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 480 Gen9
(2.10 GHz, Intel Xeon E5-2695 v4)

SPECfp2006 = 116

SPECfp_base2006 = 111

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Nov-2016

Hardware Availability: Dec-2016

Software Availability: Sep-2016

Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-xxgs Mon Nov 7 20:17:08 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2695 v4 @ 2.10GHz
 2 "physical id"s (chips)
 72 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 18
  siblings  : 36
  physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
  physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 46080 KB
```

From /proc/meminfo

```
MemTotal: 264542576 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

/usr/bin/lsb_release -d

```
SUSE Linux Enterprise Server 12 SP1
```

From /etc/*release* /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

uname -a:

```
Linux linux-xxgs 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 480 Gen9
(2.10 GHz, Intel Xeon E5-2695 v4)

SPECfp2006 = 116

SPECfp_base2006 = 111

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Nov-2016

Hardware Availability: Dec-2016

Software Availability: Sep-2016

Platform Notes (Continued)

(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 7 20:16

SPEC is set to: /home/cpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	424G	3.9G	420G	1%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I37 09/14/2016

Memory:

8x UNKNOWN NOT AVAILABLE

16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as: 16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh10.2"

OMP_NUM_THREADS = "36"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 480 Gen9

(2.10 GHz, Intel Xeon E5-2695 v4)

SPECfp2006 =

116

SPECfp_base2006 =

111

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Nov-2016

Hardware Availability: Dec-2016

Software Availability: Sep-2016

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deall: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 480 Gen9

(2.10 GHz, Intel Xeon E5-2695 v4)

SPECfp2006 =

116

SPECfp_base2006 =

111

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Nov-2016

Hardware Availability: Dec-2016

Software Availability: Sep-2016

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0
-qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3
-auto -unroll4

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 480 Gen9

(2.10 GHz, Intel Xeon E5-2695 v4)

SPECfp2006 =

116

SPECfp_base2006 =

111

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Nov-2016

Hardware Availability: Dec-2016

Software Availability: Sep-2016

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revD.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revD.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Nov 29 19:08:00 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 29 November 2016.