



SPEC[®] CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECfp[®]_rate2006 = 947

Huawei CH121 V3 (Intel Xeon E5-2680 v4)

SPECfp_rate_base2006 = 922

CPU2006 license: 3175

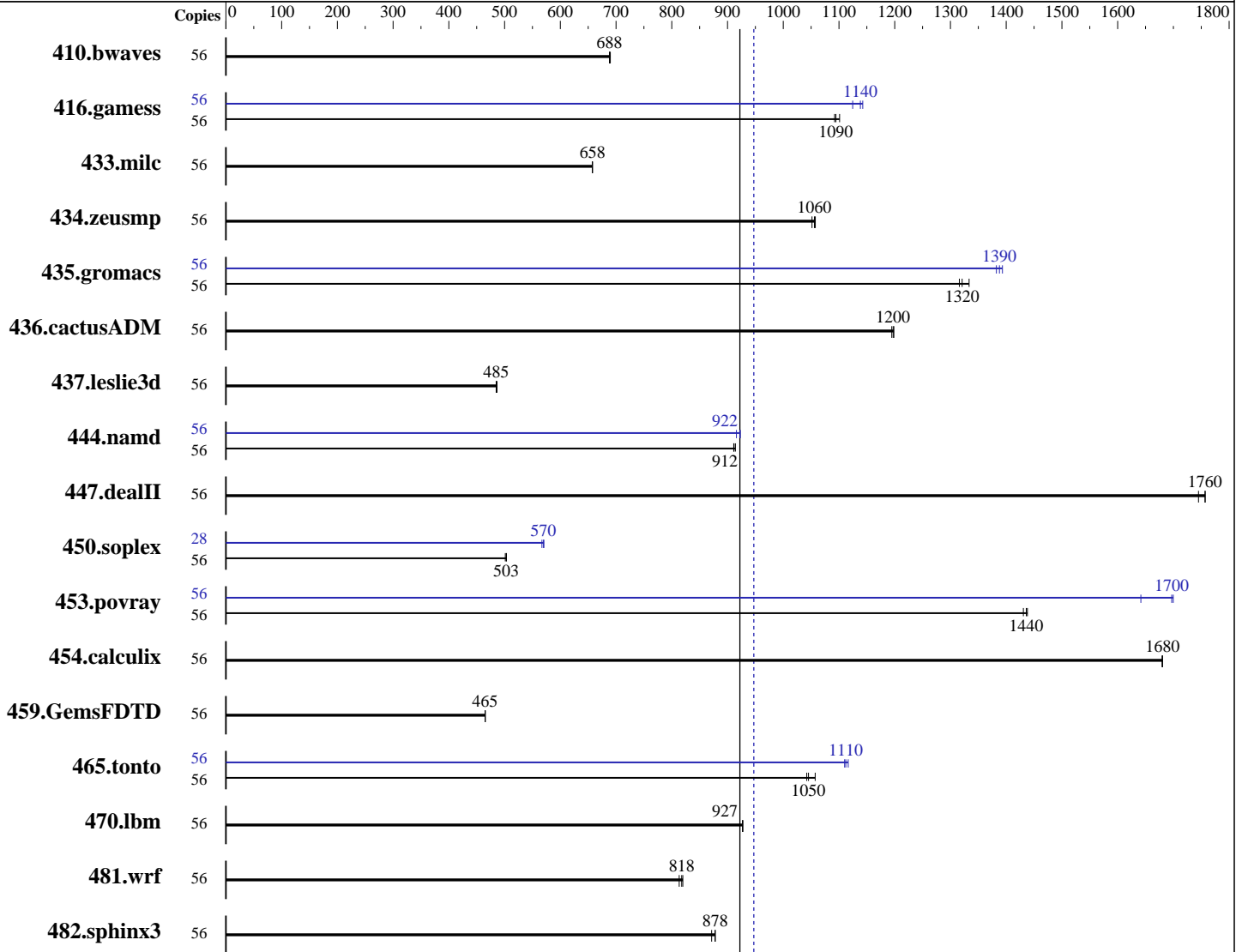
Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



SPECfp_rate_base2006 = 922

SPECfp_rate2006 = 947

Hardware

CPU Name: Intel Xeon E5-2680 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 947

Huawei CH121 V3 (Intel Xeon E5-2680 v4)

SPECfp_rate_base2006 = 922

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	56	1103	690	<u>1106</u>	<u>688</u>	1106	688	56	1103	690	<u>1106</u>	<u>688</u>	1106	688
416.gamess	56	1004	1090	<u>1002</u>	<u>1090</u>	996	1100	56	960	1140	<u>963</u>	<u>1140</u>	975	1120
433.milc	56	782	658	782	657	<u>782</u>	<u>658</u>	56	782	658	782	657	<u>782</u>	<u>658</u>
434.zeusmp	56	482	1060	<u>483</u>	<u>1060</u>	485	1050	56	482	1060	<u>483</u>	<u>1060</u>	485	1050
435.gromacs	56	304	1320	300	1330	<u>303</u>	<u>1320</u>	56	287	1390	<u>288</u>	<u>1390</u>	289	1380
436.cactusADM	56	558	1200	560	1190	<u>559</u>	<u>1200</u>	56	558	1200	560	1190	<u>559</u>	<u>1200</u>
437.leslie3d	56	1086	485	1082	486	<u>1085</u>	<u>485</u>	56	1086	485	1082	486	<u>1085</u>	<u>485</u>
444.namd	56	493	911	491	914	<u>493</u>	<u>912</u>	56	<u>487</u>	<u>922</u>	490	916	487	923
447.dealII	56	<u>365</u>	<u>1760</u>	367	1750	365	1760	56	<u>365</u>	<u>1760</u>	367	1750	365	1760
450.soplex	56	<u>929</u>	<u>503</u>	928	503	932	501	28	409	570	<u>410</u>	<u>570</u>	412	567
453.povray	56	208	1430	<u>207</u>	<u>1440</u>	207	1440	56	181	1640	<u>176</u>	<u>1700</u>	175	1700
454.calculix	56	275	1680	<u>275</u>	<u>1680</u>	275	1680	56	275	1680	<u>275</u>	<u>1680</u>	275	1680
459.GemsFDTD	56	<u>1277</u>	<u>465</u>	1278	465	1277	465	56	<u>1277</u>	<u>465</u>	1278	465	1277	465
465.tonto	56	<u>527</u>	<u>1050</u>	521	1060	529	1040	56	494	1120	<u>495</u>	<u>1110</u>	496	1110
470.lbm	56	<u>830</u>	<u>927</u>	830	927	830	927	56	<u>830</u>	<u>927</u>	830	927	830	927
481.wrf	56	763	820	769	813	<u>765</u>	<u>818</u>	56	763	820	769	813	<u>765</u>	<u>818</u>
482.sphinx3	56	<u>1243</u>	<u>878</u>	1252	872	1243	878	56	<u>1243</u>	<u>878</u>	1252	872	1243	878

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to COD mode

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 947

Huawei CH121 V3 (Intel Xeon E5-2680 v4)

SPECfp_rate_base2006 = 922

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

Platform Notes (Continued)

```

Set Patrol Scrub to Disable
Baseboard Management Controller used to adjust the fan speed to 100%
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-4m6y Sun Nov 6 07:10:00 2016

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2680 v4@ 2.40GHz
 2 "physical id"s (chips)
 56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 14
  siblings  : 28
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 17920 KB

```

```

From /proc/meminfo
MemTotal:      528824412 kB
HugePages_Total:      0
Hugepagesize:   2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux linux-4m6y 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 947

Huawei CH121 V3 (Intel Xeon E5-2680 v4)

SPECfp_rate_base2006 = 922

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

Platform Notes (Continued)

run-level 3 Nov 4 13:03 last=5

SPEC is set to: /spec16

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	xfs	456G	113G	344G	25%	/

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.32 09/14/2016

Memory:

8x NO DIMM NO DIMM

16x Samsung M393A4K40BB1-CRC 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

The Huawei CH121 V3 and Huawei CH222 V3

are electronically equivalent.

The results have been measured on a Huawei CH121 V3 model

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 947

Huawei CH121 V3 (Intel Xeon E5-2680 v4)

SPECfp_rate_base2006 = 922

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 947

Huawei CH121 V3 (Intel Xeon E5-2680 v4)

SPECfp_rate_base2006 = 922

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -D_FILE_OFFSET_BITS=64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 947

Huawei CH121 V3 (Intel Xeon E5-2680 v4)

SPECfp_rate_base2006 = 922

CPU2006 license: 3175

Test date: Nov-2016

Test sponsor: Huawei

Hardware Availability: Mar-2016

Tested by: Huawei

Software Availability: Dec-2015

Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 947

Huawei CH121 V3 (Intel Xeon E5-2680 v4)

SPECfp_rate_base2006 = 922

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Nov 30 10:45:58 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 29 November 2016.