



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = 99.5

Huawei CH226 V3 (Intel Xeon E5-2620 v4)

SPECfp\_base2006 = 94.6

CPU2006 license: 3175

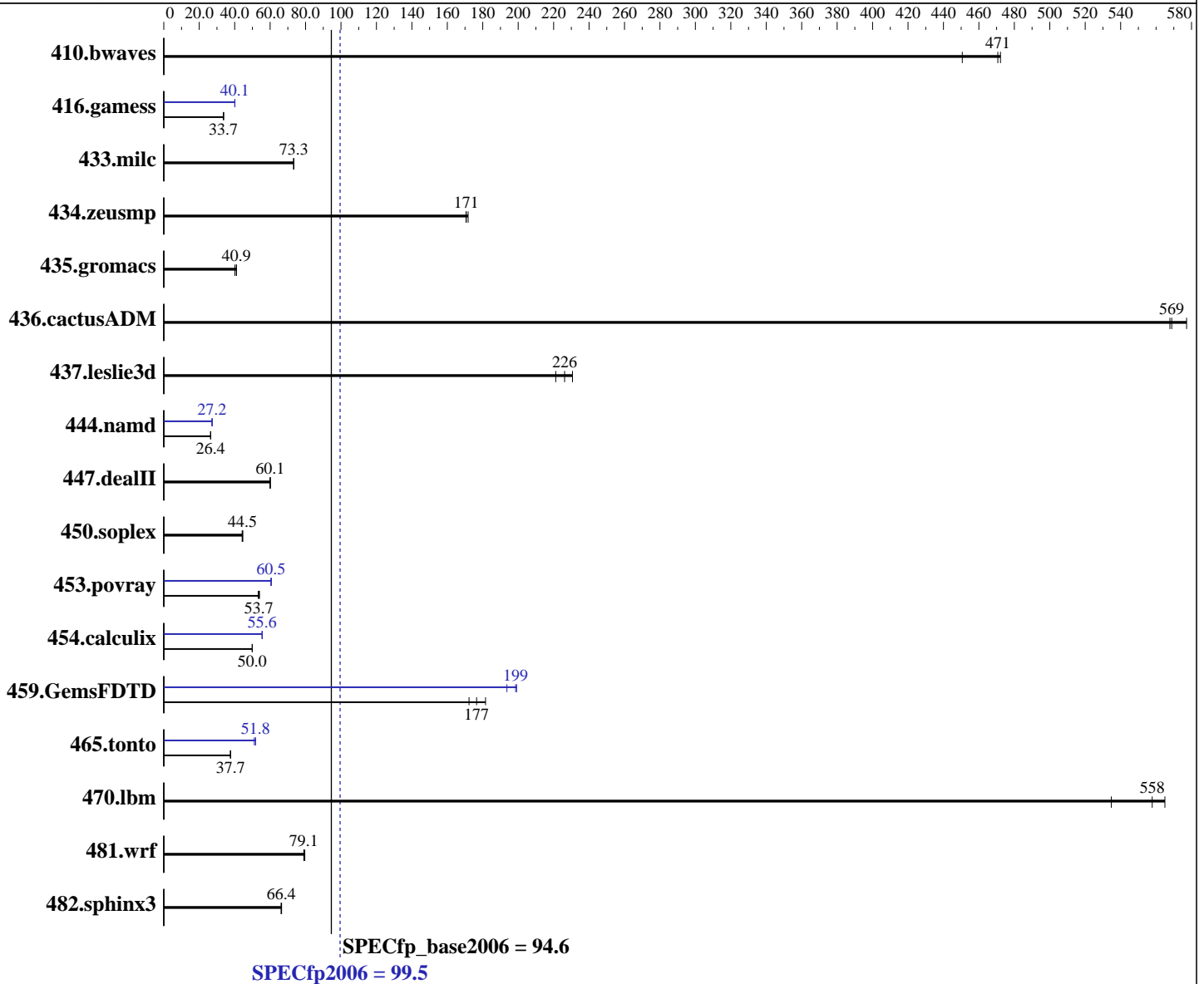
Test sponsor: Huawei

Tested by: Huawei

Test date: Oct-2016

Hardware Availability: Mar-2016

Software Availability: Mar-2016



SPECfp2006 = 99.5

### Hardware

CPU Name: Intel Xeon E5-2620 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)

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L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133T-R)  
 Disk Subsystem: 1 x 480 GB SATA SSD  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: none

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	28.8	472	30.2	451	<u>28.9</u>	<u>471</u>	28.8	472	30.2	451	<u>28.9</u>	<u>471</u>
416.gamess	<u>581</u>	<u>33.7</u>	580	33.8	582	33.6	489	40.0	488	40.1	<u>488</u>	<u>40.1</u>
433.milc	125	73.3	125	73.3	<u>125</u>	<u>73.3</u>	125	73.3	125	73.3	<u>125</u>	<u>73.3</u>
434.zeusmp	53.0	172	53.4	171	<u>53.3</u>	<u>171</u>	53.0	172	53.4	171	<u>53.3</u>	<u>171</u>
435.gromacs	<u>174</u>	<u>40.9</u>	174	41.1	178	40.1	<u>174</u>	<u>40.9</u>	174	41.1	178	40.1
436.cactusADM	<u>21.0</u>	<u>569</u>	20.7	577	21.0	568	<u>21.0</u>	<u>569</u>	20.7	577	21.0	568
437.leslie3d	42.5	221	<u>41.6</u>	<u>226</u>	40.8	231	42.5	221	<u>41.6</u>	<u>226</u>	40.8	231
444.namd	303	26.5	<u>303</u>	<u>26.4</u>	304	26.4	294	27.3	<u>294</u>	<u>27.2</u>	294	27.2
447.dealII	<u>190</u>	<u>60.1</u>	191	60.0	190	60.1	<u>190</u>	<u>60.1</u>	191	60.0	190	60.1
450.soplex	<u>187</u>	<u>44.5</u>	189	44.2	187	44.5	<u>187</u>	<u>44.5</u>	189	44.2	187	44.5
453.povray	<u>99.1</u>	<u>53.7</u>	99.9	53.3	98.5	54.0	<u>87.9</u>	<u>60.5</u>	87.9	60.5	87.6	60.7
454.calculix	166	49.8	<u>165</u>	<u>50.0</u>	165	50.0	148	55.6	<u>149</u>	<u>55.6</u>	149	55.4
459.GemsFDTD	<u>60.1</u>	<u>177</u>	58.4	182	61.6	172	54.8	194	<u>53.4</u>	<u>199</u>	53.3	199
465.tonto	262	37.5	261	37.7	<u>261</u>	<u>37.7</u>	<u>190</u>	<u>51.8</u>	193	51.1	190	51.8
470.lbm	24.3	565	<u>24.6</u>	<u>558</u>	25.7	535	24.3	565	<u>24.6</u>	<u>558</u>	25.7	535
481.wrf	140	79.6	<u>141</u>	<u>79.1</u>	141	79.1	140	79.6	<u>141</u>	<u>79.1</u>	141	79.1
482.sphinx3	293	66.5	<u>294</u>	<u>66.4</u>	294	66.2	293	66.5	<u>294</u>	<u>66.4</u>	294	66.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Power Efficiency Mode to Custom  
 Set Snoop Mode to HS mode  
 Set Patrol Scrub to Disable  
 Set Hyper-Threading to Disable  
 Sysinfo program /spec16/config/sysinfo.rev6914  
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
 running on linux-ljfn Tue Oct 25 19:17:01 2016

This section contains SUT (System Under Test) info as seen by  
Continued on next page



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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2620 v4 @ 2.10GHz
  2 "physical id"s (chips)
  16 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 8
    siblings  : 8
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7
  cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      264064288 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

```

```

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 1
  # This file is deprecated and will be removed in a future service pack or
  # release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP1"
  VERSION_ID="12.1"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux linux-ljfn 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Oct 25 14:12

```

SPEC is set to: /spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal       ext4  394G   14G  380G   4% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to

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## Platform Notes (Continued)

hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.32 09/14/2016

Memory:

16x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz

8x NO DIMM NO DIMM

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

The Huawei CH225 V3 and Huawei CH226 V3

are electronically equivalent.

The results have been measured on a Huawei CH225 V3 model

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

Continued on next page

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Page 4



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Test sponsor: Huawei

Hardware Availability: Mar-2016

Tested by: Huawei

Software Availability: Mar-2016

## Base Portability Flags (Continued)

```

435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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Test sponsor: Huawei

Tested by: Huawei

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Hardware Availability: Mar-2016

Software Availability: Mar-2016

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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Tested by: Huawei

Software Availability: Mar-2016

## Peak Optimization Flags (Continued)

465.tonto (continued):

-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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