



SPEC[®] CFP2006 Result

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Huawei

SPECfp[®]2006 = 107

Huawei CH222 V3 (Intel Xeon E5-2695 v4)

SPECfp_base2006 = 101

CPU2006 license: 3175

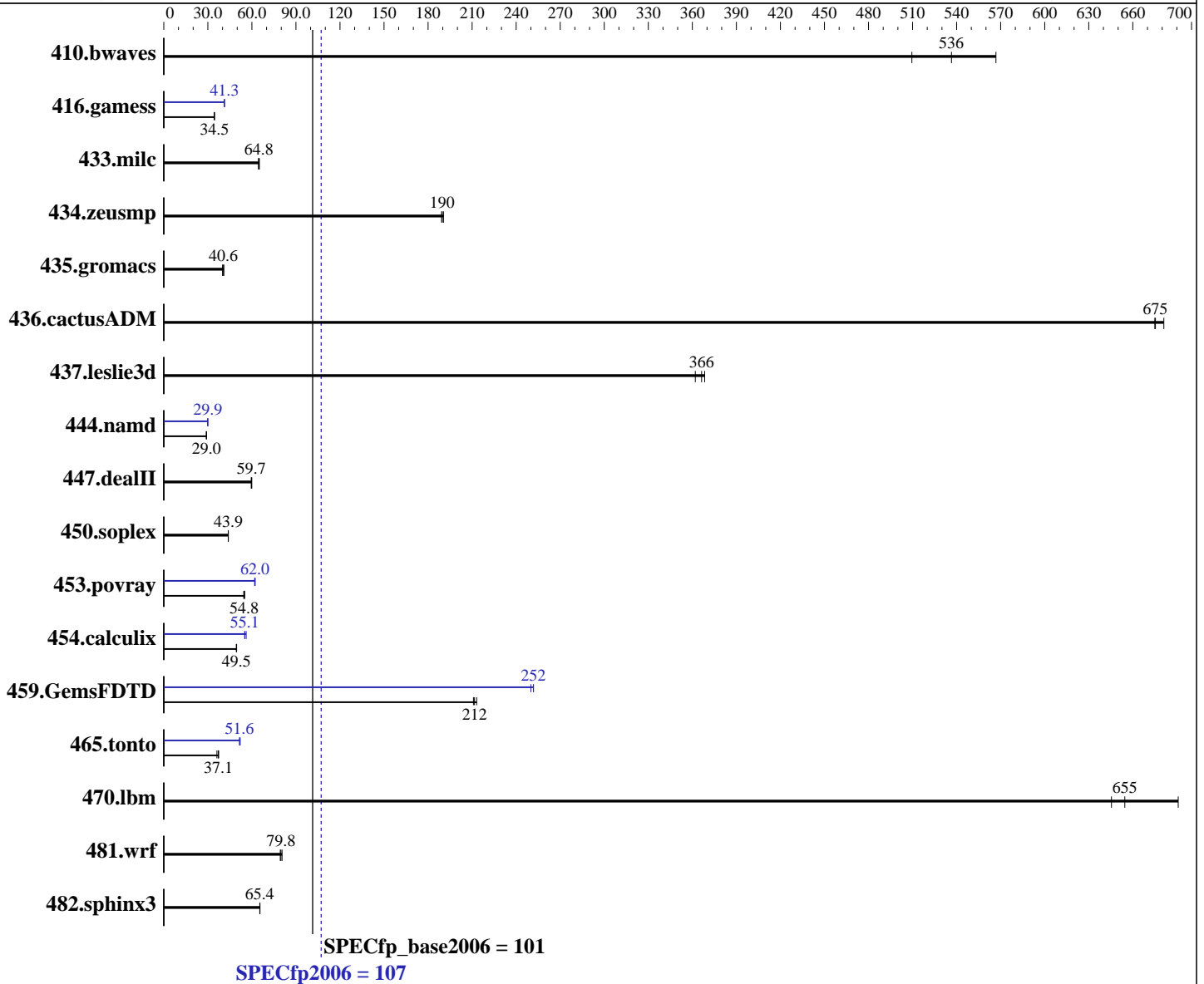
Test sponsor: Huawei

Tested by: Huawei

Test date: Oct-2016

Hardware Availability: Mar-2016

Software Availability: Mar-2016



Hardware

CPU Name: Intel Xeon E5-2695 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
 3.10.0-327.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs

Continued on next page



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L3 Cache: 45 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx8 PC4-2400T-R)
 Disk Subsystem: 1 x 1000GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	26.7	510	<u>25.3</u>	<u>536</u>	24.0	567	26.7	510	<u>25.3</u>	<u>536</u>	24.0	567
416.gamess	<u>568</u>	<u>34.5</u>	567	34.5	570	34.3	474	41.3	<u>474</u>	<u>41.3</u>	475	41.3
433.milc	141	65.1	<u>142</u>	<u>64.8</u>	142	64.4	141	65.1	<u>142</u>	<u>64.8</u>	142	64.4
434.zeusmp	<u>48.0</u>	<u>190</u>	48.1	189	47.8	191	<u>48.0</u>	<u>190</u>	48.1	189	47.8	191
435.gromacs	<u>176</u>	<u>40.6</u>	175	40.9	179	40.0	<u>176</u>	<u>40.6</u>	175	40.9	179	40.0
436.cactusADM	<u>17.7</u>	<u>675</u>	17.7	675	17.5	681	<u>17.7</u>	<u>675</u>	17.7	675	17.5	681
437.leslie3d	26.0	362	25.5	368	<u>25.7</u>	<u>366</u>	26.0	362	25.5	368	<u>25.7</u>	<u>366</u>
444.namd	276	29.1	<u>276</u>	<u>29.0</u>	276	29.0	268	29.9	<u>268</u>	<u>29.9</u>	268	29.9
447.dealII	192	59.6	<u>192</u>	<u>59.7</u>	191	59.9	192	59.6	<u>192</u>	<u>59.7</u>	191	59.9
450.soplex	190	43.9	190	43.9	<u>190</u>	<u>43.9</u>	190	43.9	190	43.9	<u>190</u>	<u>43.9</u>
453.povray	<u>97.1</u>	<u>54.8</u>	97.7	54.4	96.6	55.1	<u>85.8</u>	<u>62.0</u>	86.0	61.9	85.4	62.3
454.calculix	167	49.4	166	49.6	<u>167</u>	<u>49.5</u>	150	55.1	<u>150</u>	<u>55.1</u>	147	56.1
459.GemsFDTD	50.3	211	<u>50.1</u>	<u>212</u>	49.8	213	42.4	250	42.1	252	<u>42.1</u>	<u>252</u>
465.tonto	263	37.4	<u>265</u>	<u>37.1</u>	272	36.2	191	51.6	<u>191</u>	<u>51.6</u>	189	52.0
470.lbm	19.9	691	<u>21.0</u>	<u>655</u>	21.3	645	19.9	691	<u>21.0</u>	<u>655</u>	21.3	645
481.wrf	139	80.6	<u>140</u>	<u>79.8</u>	141	79.3	139	80.6	<u>140</u>	<u>79.8</u>	141	79.3
482.sphinx3	<u>298</u>	<u>65.4</u>	297	65.5	298	65.4	<u>298</u>	<u>65.4</u>	297	65.5	298	65.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set Power Efficiency Mode to Custom
 Set Snoop Mode to HS mode
 Set Patrol Scrub to Disable
 Set Hyper-Threading to Disable
 Sysinfo program /spec16/config/sysinfo.rev6914
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
 running on localhost.localdomain Fri Oct 21 19:46:53 2016

This section contains SUT (System Under Test) info as seen by
Continued on next page



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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2695 v4 @ 2.10GHz
 2 "physical id"s (chips)
 36 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 18
  siblings     : 18
  physical 0   : cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
  physical 1   : cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size     : 46080 KB

```

```

From /proc/meminfo
MemTotal:      263567500 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Oct 21 19:45

```

SPEC is set to: /spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       xfs   440G  9.3G  430G   3% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.32 09/14/2016

Continued on next page



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Platform Notes (Continued)

Memory:
8x NO DIMM NO DIMM
16x Ramaxel RMRA6351MB78HBF2400 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact,1,0"  
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"  
OMP_NUM_THREADS = "36"
```

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

The Huawei CH121 V3 and Huawei CH222 V3 are electronically equivalent.

The results have been measured on a Huawei CH121 V3 model.

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -noFOR_main  
436.cactusADM: -DSPEC_CPU_LP64 -noFOR_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64
```

Continued on next page



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Base Portability Flags (Continued)

```

447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```

Peak Portability Flags

Same as Base Portability Flags



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Software Availability: Mar-2016

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

Continued on next page



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Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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