



# SPEC<sup>®</sup> CINT2006 Result

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## HITACHI

SPECint<sup>®</sup>\_rate2006 = 1960

BladeSymphony BS2500 (Intel Xeon E7-8893 v4)

SPECint\_rate\_base2006 = 1850

CPU2006 license: 35

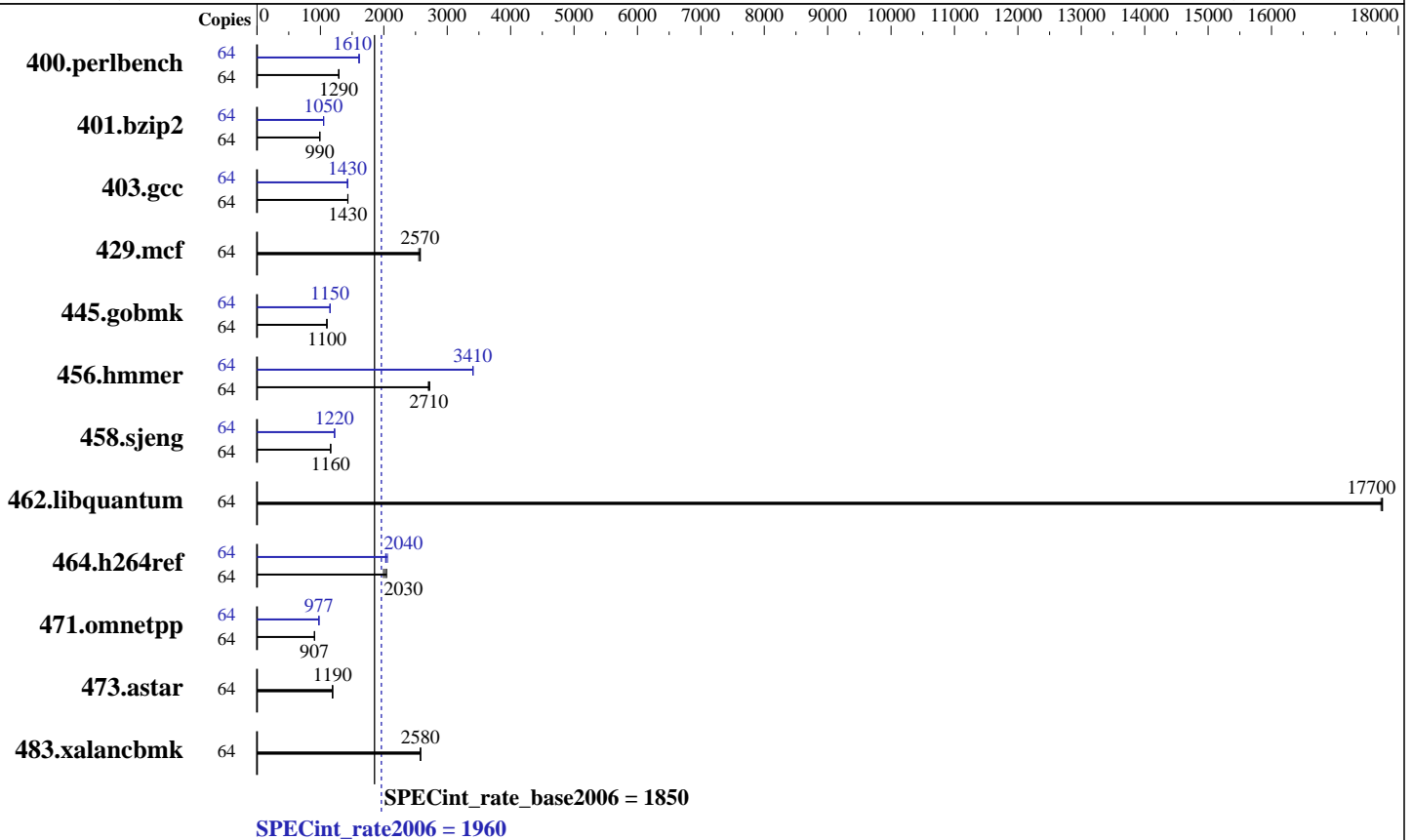
Test date: Sep-2016

Test sponsor: HITACHI

Hardware Availability: Sep-2016

Tested by: HITACHI

Software Availability: Mar-2016



### Hardware

CPU Name: Intel Xeon E7-8893 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
 CPU MHz: 3200  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 8 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,3,4,8 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 60 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 3 TB (192 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)  
 Disk Subsystem: 2 x 600 GB SAS, 15000 RPM, RAID1  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	64	484	1290	<b><u>485</u></b>	<b><u>1290</u></b>	485	1290	64	<b><u>388</u></b>	<b><u>1610</u></b>	389	1610	388	1610
401.bzip2	64	623	991	624	989	<b><u>624</u></b>	<b><u>990</u></b>	64	589	1050	587	1050	<b><u>588</u></b>	<b><u>1050</u></b>
403.gcc	64	359	1430	<b><u>359</u></b>	<b><u>1430</u></b>	360	1430	64	<b><u>361</u></b>	<b><u>1430</u></b>	361	1430	360	1430
429.mcf	64	229	2550	<b><u>227</u></b>	<b><u>2570</u></b>	227	2580	64	229	2550	<b><u>227</u></b>	<b><u>2570</u></b>	227	2580
445.gobmk	64	<b><u>609</u></b>	<b><u>1100</u></b>	610	1100	608	1100	64	<b><u>583</u></b>	<b><u>1150</u></b>	583	1150	583	1150
456.hammer	64	<b><u>220</u></b>	<b><u>2710</u></b>	219	2720	221	2700	64	175	3410	175	3400	<b><u>175</u></b>	<b><u>3410</u></b>
458.sjeng	64	<b><u>667</u></b>	<b><u>1160</u></b>	666	1160	668	1160	64	<b><u>633</u></b>	<b><u>1220</u></b>	633	1220	634	1220
462.libquantum	64	74.8	17700	74.7	17800	<b><u>74.7</u></b>	<b><u>17700</u></b>	64	74.8	17700	74.7	17800	<b><u>74.7</u></b>	<b><u>17700</u></b>
464.h264ref	64	693	2040	710	2000	<b><u>699</u></b>	<b><u>2030</u></b>	64	698	2030	<b><u>696</u></b>	<b><u>2040</u></b>	688	2060
471.omnetpp	64	441	908	441	906	<b><u>441</u></b>	<b><u>907</u></b>	64	409	977	410	976	<b><u>409</u></b>	<b><u>977</u></b>
473.astar	64	378	1190	376	1190	<b><u>377</u></b>	<b><u>1190</u></b>	64	378	1190	376	1190	<b><u>377</u></b>	<b><u>1190</u></b>
483.xalancbmk	64	<b><u>171</u></b>	<b><u>2580</u></b>	171	2580	171	2580	64	<b><u>171</u></b>	<b><u>2580</u></b>	171	2580	171	2580

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Memory Power Management = Automatic  
Active Energy Manager = "Capping Disabled"  
Platform Controlled Type = "Maximum Performance"  
C1 Enhanced Mode = Disable  
Sysinfo program /home/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on rhel7264 Fri Sep 16 09:19:32 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E7-8893 v4 @ 3.20GHz  
8 "physical id"s (chips)  
64 "processors"

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### Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```

cpu cores : 4
siblings  : 8
physical 0: cores 12 13 25 26
physical 1: cores 12 13 25 26
physical 2: cores 12 13 25 26
physical 3: cores 12 13 25 26
physical 4: cores 12 13 25 26
physical 5: cores 12 13 25 26
physical 6: cores 12 13 25 26
physical 7: cores 12 13 25 26
cache size : 61440 KB

```

From /proc/meminfo

```

MemTotal:      3169800748 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/\*release\* /etc/\*version\*

```

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

```

uname -a:

```

Linux rhel7264 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Sep 16 09:14

SPEC is set to: /home/cpu2006

```

Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs  504G  12G  493G   3% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HITACHI 11-04 08/29/2016

Memory:

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### Platform Notes (Continued)

```

76x 0x0000 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
2x 0x0001 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
6x 0x0003 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
2x 0x0004 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
2x 0x0200 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
2x 0x0201 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
2x 0x0603 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
2x 0x0A0D M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
2x 0x5C00 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
96x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz

```

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB memory using RedHat EL 7.2 glibc 2.17

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Hitachi Compute Blade 520X and BladeSymphony BS2500 are electronically equivalent.

The results have been measured on a Hitachi Compute Blade 520X.

### Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

### Base Portability Flags

```

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64

```

Continued on next page

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Page 4



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## Base Portability Flags (Continued)

462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
 471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
 473.astar: -D\_FILE\_OFFSET\_BITS=64  
 483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
 -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
 -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

Continued on next page



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## Peak Portability Flags (Continued)

```

401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmr: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
               -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
               -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
            -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias
            -opt-mem-layout-trans=3

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
            -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
              -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
              -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
              -ansi-alias

```

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## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.6.xml>

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