



SPEC® CINT2006 Result

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NEC Corporation

SPECint®_rate2006 = 206

Express5800/GT110h (Intel Xeon E3-1220 v5)

SPECint_rate_base2006 = 199

CPU2006 license: 9006

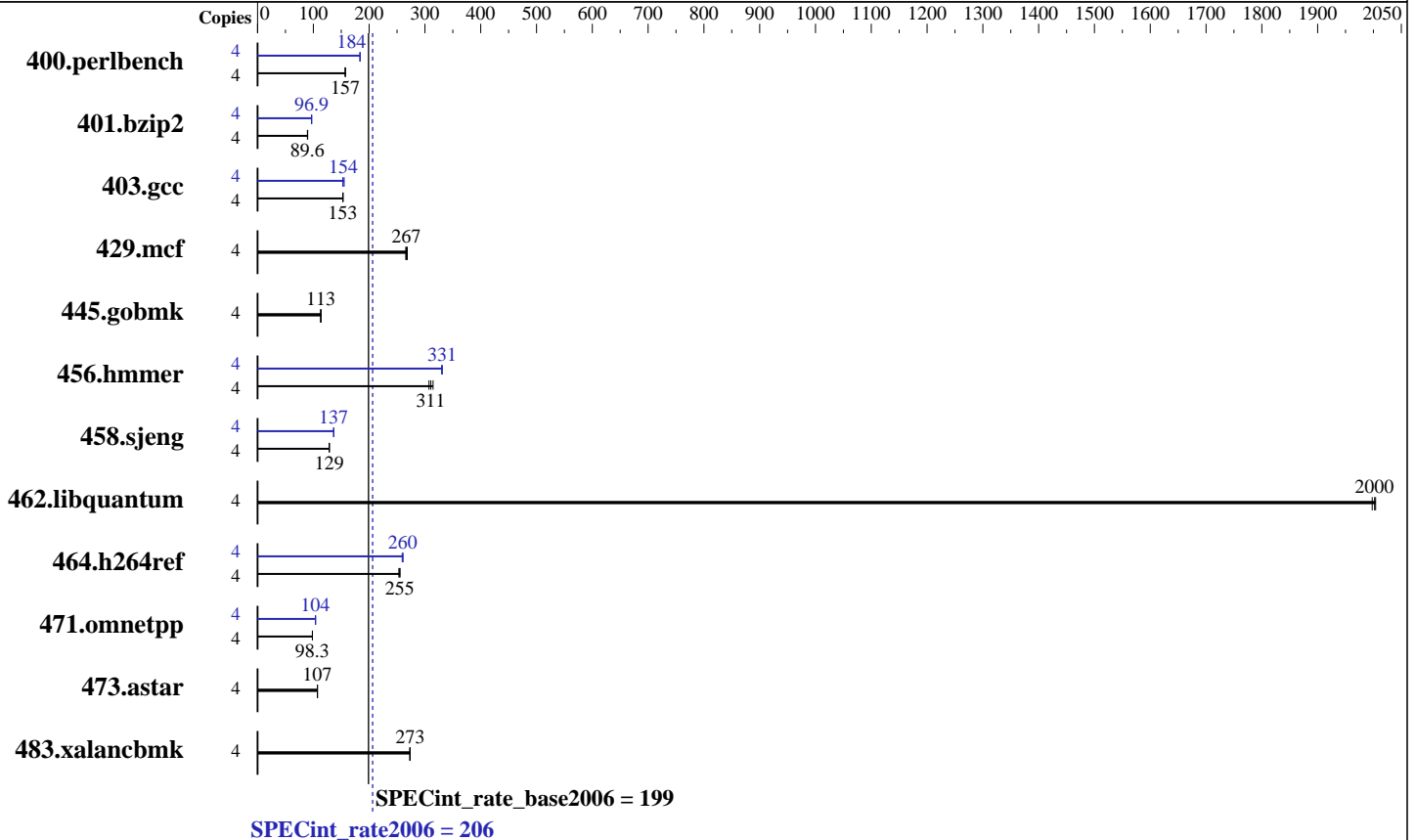
Test date: Jul-2016

Test sponsor: NEC Corporation

Hardware Availability: Jul-2016

Tested by: NEC Corporation

Software Availability: Feb-2016



Hardware

CPU Name: Intel Xeon E3-1220 v5
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC4-2133P-E)
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
 Kernel 3.10.0-327.4.5.el7.x86_64
 Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	249	157	248	157	<u>248</u>	<u>157</u>	4	<u>213</u>	<u>184</u>	213	184	213	184
401.bzip2	4	<u>431</u>	<u>89.6</u>	432	89.4	430	89.8	4	399	96.7	398	97.0	<u>398</u>	<u>96.9</u>
403.gcc	4	<u>211</u>	<u>153</u>	210	153	211	153	4	211	153	<u>210</u>	<u>154</u>	207	155
429.mcf	4	137	266	136	268	<u>137</u>	<u>267</u>	4	137	266	136	268	<u>137</u>	<u>267</u>
445.gobmk	4	368	114	<u>370</u>	<u>113</u>	371	113	4	368	114	<u>370</u>	<u>113</u>	371	113
456.hammer	4	<u>120</u>	<u>311</u>	121	307	119	315	4	113	331	<u>113</u>	<u>331</u>	113	330
458.sjeng	4	377	129	<u>377</u>	<u>129</u>	376	129	4	354	137	355	136	<u>354</u>	<u>137</u>
462.libquantum	4	<u>41.4</u>	<u>2000</u>	41.5	2000	41.4	2000	4	<u>41.4</u>	<u>2000</u>	41.5	2000	41.4	2000
464.h264ref	4	<u>348</u>	<u>255</u>	347	255	350	253	4	<u>340</u>	<u>260</u>	340	261	341	259
471.omnetpp	4	<u>254</u>	<u>98.3</u>	254	98.4	254	98.3	4	241	104	<u>241</u>	<u>104</u>	240	104
473.astar	4	<u>262</u>	<u>107</u>	261	107	262	107	4	<u>262</u>	<u>107</u>	261	107	262	107
483.xalancbmk	4	101	273	<u>101</u>	<u>273</u>	101	273	4	101	273	<u>101</u>	<u>273</u>	101	273

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Power Management Policy: Custom
Energy Performance: Performance

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB memory using RedHat EL 7.2 glibc 2.17
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled



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Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
 401.bzip2: -D_FILE_OFFSET_BITS=64
 403.gcc: -D_FILE_OFFSET_BITS=64
 429.mcf: -D_FILE_OFFSET_BITS=64
 445.gobmk: -D_FILE_OFFSET_BITS=64
 456.hmmer: -D_FILE_OFFSET_BITS=64
 458.sjeng: -D_FILE_OFFSET_BITS=64
 462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
 464.h264ref: -D_FILE_OFFSET_BITS=64
 471.omnetpp: -D_FILE_OFFSET_BITS=64
 473.astar: -D_FILE_OFFSET_BITS=64
 483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

Peak Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
 401.bzip2: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
 403.gcc: `-D_FILE_OFFSET_BITS=64`
 429.mcf: `-D_FILE_OFFSET_BITS=64`
 445.gobmk: `-D_FILE_OFFSET_BITS=64`
 456.hmmer: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
 458.sjeng: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
 462.libquantum: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`
 464.h264ref: `-D_FILE_OFFSET_BITS=64`
 471.omnetpp: `-D_FILE_OFFSET_BITS=64`
 473.astar: `-D_FILE_OFFSET_BITS=64`
 483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)`
`-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)`
`-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)`
`-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)`
`-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch`
`-auto-ilp32 -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div`

429.mcf: `basepeak = yes`

445.gobmk: `basepeak = yes`

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Peak Optimization Flags (Continued)

456.hmmcr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110h-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110h-RevA.xml>



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For other inquiries, please contact webmaster@spec.org.

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