



# SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp®\_rate2006 = 688

Lenovo Flex System x240 M5  
(2.20 GHz, Intel Xeon E5-2630 v4)

SPECfp\_rate\_base2006 = 672

CPU2006 license: 9017

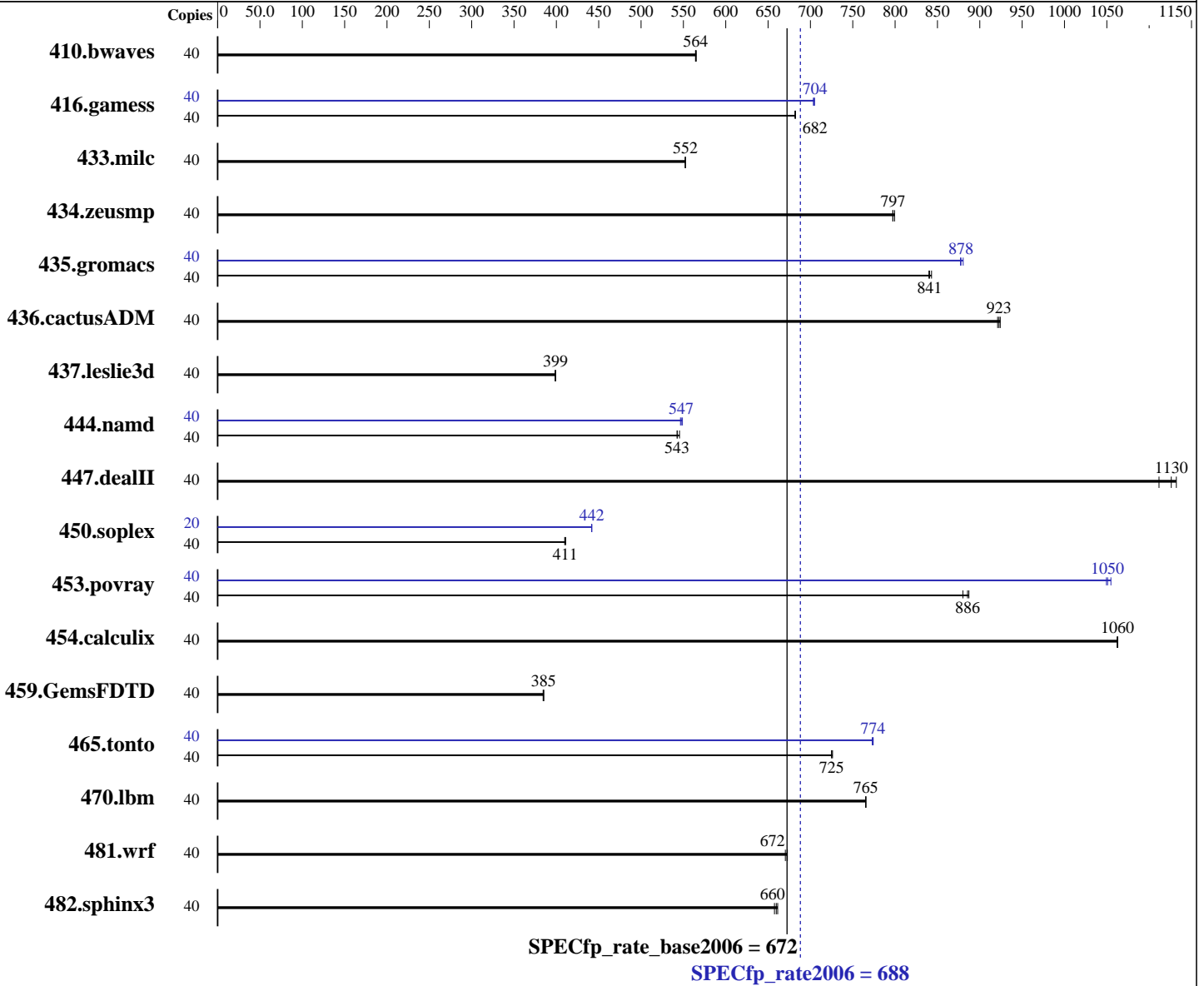
Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E5-2630 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64)  
 Kernel 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp\_rate2006 = **688**

Lenovo Flex System x240 M5  
(2.20 GHz, Intel Xeon E5-2630 v4)

SPECfp\_rate\_base2006 = **672**

CPU2006 license: 9017

Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

L3 Cache: 25 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
Disk Subsystem: 2 x 300 GB 10000 RPM SAS, RAID 0  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	40	962	565	963	564	<b>963</b>	<b>564</b>	40	962	565	963	564	<b>963</b>	<b>564</b>		
416.gamess	40	1149	682	1148	682	<b>1148</b>	<b>682</b>	40	1113	703	1111	705	<b>1112</b>	<b>704</b>		
433.milc	40	665	552	<b>665</b>	<b>552</b>	665	552	40	665	552	<b>665</b>	<b>552</b>	665	552		
434.zeusmp	40	457	797	<b>456</b>	<b>797</b>	455	799	40	457	797	<b>456</b>	<b>797</b>	455	799		
435.gromacs	40	340	840	339	843	<b>340</b>	<b>841</b>	40	324	880	326	877	<b>325</b>	<b>878</b>		
436.cactusADM	40	517	924	<b>518</b>	<b>923</b>	519	921	40	517	924	<b>518</b>	<b>923</b>	519	921		
437.leslie3d	40	943	399	<b>943</b>	<b>399</b>	942	399	40	943	399	<b>943</b>	<b>399</b>	942	399		
444.namd	40	588	545	<b>591</b>	<b>543</b>	591	543	40	585	549	587	547	<b>586</b>	<b>547</b>		
447.dealII	40	404	1130	<b>406</b>	<b>1130</b>	412	1110	40	404	1130	<b>406</b>	<b>1130</b>	412	1110		
450.soplex	40	812	411	814	410	<b>813</b>	<b>411</b>	20	378	442	<b>378</b>	<b>442</b>	378	442		
453.povray	40	<b>240</b>	<b>886</b>	242	880	240	887	40	203	1050	202	1050	<b>202</b>	<b>1050</b>		
454.calculix	40	<b>311</b>	<b>1060</b>	310	1060	311	1060	40	<b>311</b>	<b>1060</b>	310	1060	311	1060		
459.GemsFDTD	40	1102	385	1104	384	<b>1102</b>	<b>385</b>	40	1102	385	1104	384	<b>1102</b>	<b>385</b>		
465.tonto	40	543	725	<b>543</b>	<b>725</b>	542	726	40	509	773	<b>509</b>	<b>774</b>	508	774		
470.lbm	40	718	765	<b>718</b>	<b>765</b>	718	765	40	718	765	<b>718</b>	<b>765</b>	718	765		
481.wrf	40	<b>664</b>	<b>672</b>	664	673	666	670	40	<b>664</b>	<b>672</b>	664	673	666	670		
482.sphinx3	40	1179	661	1186	658	<b>1182</b>	<b>660</b>	40	1179	661	1186	658	<b>1182</b>	<b>660</b>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

**SPECfp\_rate2006 = 688**

Lenovo Flex System x240 M5  
(2.20 GHz, Intel Xeon E5-2630 v4)

**SPECfp\_rate\_base2006 = 672**

**CPU2006 license:** 9017

**Test date:** May-2016

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Mar-2016

**Tested by:** Lenovo Group Limited

**Software Availability:** Dec-2015

## Platform Notes

### BIOS Configuration:

Operating Mode set to "Maximum Performance"  
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on x240m5-cpu2006 Tue May 3 14:12:10 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

### From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E5-2630 v4 @ 2.20GHz
 2 "physical id"s (chips)
 40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores     : 10
  siblings      : 20
  physical 0:   cores 0 1 2 3 4 8 9 10 11 12
  physical 1:   cores 0 1 2 3 4 8 9 10 11 12
cache size     : 25600 KB
```

### From /proc/meminfo

```
MemTotal:      263827048 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

### From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

### uname -a:

```
Linux x240m5-cpu2006 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 May 3 02:21

SPEC is set to: /home/cpu2006-1.2-ic16.0

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp\_rate2006 = 688

Lenovo Flex System x240 M5  
(2.20 GHz, Intel Xeon E5-2630 v4)

SPECfp\_rate\_base2006 = 672

CPU2006 license: 9017

Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

## Platform Notes (Continued)

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	515G	7.1G	508G	2%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO -[C4E123H-2.10]- 03/25/2016

Memory:

16x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz, configured at 2133 MHz  
8x NO DIMM Unknown

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

**SPECfp\_rate2006 = 688**

Lenovo Flex System x240 M5  
(2.20 GHz, Intel Xeon E5-2630 v4)

**SPECfp\_rate\_base2006 = 672**

**CPU2006 license:** 9017

**Test date:** May-2016

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Mar-2016

**Tested by:** Lenovo Group Limited

**Software Availability:** Dec-2015

## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

**SPECfp\_rate2006 = 688**

Lenovo Flex System x240 M5  
(2.20 GHz, Intel Xeon E5-2630 v4)

**SPECfp\_rate\_base2006 = 672**

**CPU2006 license:** 9017

**Test date:** May-2016

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Mar-2016

**Tested by:** Lenovo Group Limited

**Software Availability:** Dec-2015

## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECfp\_rate2006 = 688

Lenovo Flex System x240 M5  
(2.20 GHz, Intel Xeon E5-2630 v4)

SPECfp\_rate\_base2006 = 672

CPU2006 license: 9017

Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

## Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml>



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

Lenovo Flex System x240 M5  
(2.20 GHz, Intel Xeon E5-2630 v4)

SPECfp\_rate2006 = 688

SPECfp\_rate\_base2006 = 672

**CPU2006 license:** 9017

**Test sponsor:** Lenovo Group Limited

**Tested by:** Lenovo Group Limited

**Test date:** May-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Dec-2015

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Tue Jun 28 17:32:10 2016 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 28 June 2016.