



# SPEC® CFP2006 Result

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## Lenovo Group Limited

SPECfp®2006 = **113**

Lenovo System x3550 M5  
(2.20 GHz, Intel Xeon E5-2618L v4)

SPECfp\_base2006 = **106**

CPU2006 license: 9017

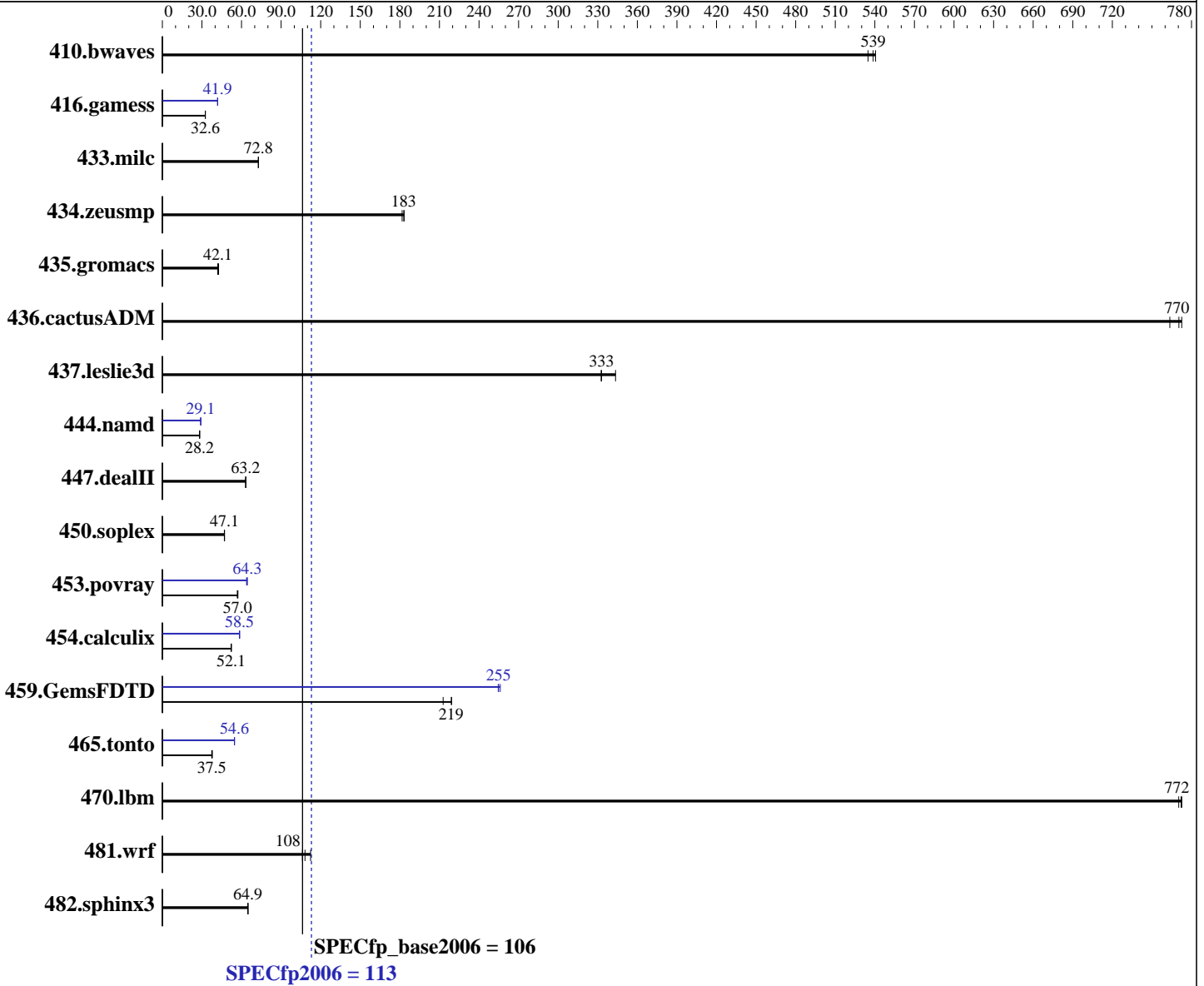
Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015



SPECfp2006 = 113

### Hardware

CPU Name: Intel Xeon E5-2618L v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64)  
 Kernel 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 25 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
Disk Subsystem: 1 x 800 GB SATA SSD  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

| Benchmark     | Base        |             |             |             |             |             | Peak        |             |             |             |             |             |
|---------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|
|               | Seconds     | Ratio       | Seconds     | Ratio       | Seconds     | Ratio       | Seconds     | Ratio       | Seconds     | Ratio       | Seconds     | Ratio       |
| 410.bwaves    | <u>25.2</u> | <u>539</u>  | 25.4        | 535         | 25.1        | 541         | <u>25.2</u> | <u>539</u>  | 25.4        | 535         | 25.1        | 541         |
| 416.gamess    | <u>601</u>  | <u>32.6</u> | 602         | 32.5        | 601         | 32.6        | <u>468</u>  | <u>41.9</u> | 467         | 41.9        | 468         | 41.8        |
| 433.milc      | 126         | 72.9        | <u>126</u>  | <u>72.8</u> | 126         | 72.7        | <u>126</u>  | <u>72.9</u> | <u>126</u>  | <u>72.8</u> | 126         | 72.7        |
| 434.zeusmp    | 49.7        | 183         | <u>49.7</u> | <u>183</u>  | 50.1        | 182         | 49.7        | 183         | <u>49.7</u> | <u>183</u>  | 50.1        | 182         |
| 435.gromacs   | <u>170</u>  | <u>42.1</u> | 170         | 42.0        | 168         | 42.6        | <u>170</u>  | <u>42.1</u> | 170         | 42.0        | 168         | 42.6        |
| 436.cactusADM | <u>15.5</u> | <u>770</u>  | 15.7        | 764         | 15.5        | 773         | <u>15.5</u> | <u>770</u>  | 15.7        | 764         | 15.5        | 773         |
| 437.leslie3d  | <u>28.2</u> | <u>333</u>  | 27.4        | 343         | 28.3        | 333         | <u>28.2</u> | <u>333</u>  | 27.4        | 343         | 28.3        | 333         |
| 444.namd      | 284         | 28.2        | 284         | 28.2        | <u>284</u>  | <u>28.2</u> | 276         | 29.1        | <u>276</u>  | <u>29.1</u> | 275         | 29.1        |
| 447.dealII    | 181         | 63.2        | 181         | 63.2        | <u>181</u>  | <u>63.2</u> | 181         | 63.2        | 181         | 63.2        | <u>181</u>  | <u>63.2</u> |
| 450.soplex    | <u>177</u>  | <u>47.1</u> | 177         | 47.0        | 177         | 47.1        | <u>177</u>  | <u>47.1</u> | 177         | 47.0        | 177         | 47.1        |
| 453.povray    | 93.6        | 56.8        | 92.9        | 57.3        | <u>93.3</u> | <u>57.0</u> | 83.4        | 63.8        | <u>82.8</u> | <u>64.3</u> | 82.7        | 64.3        |
| 454.calculix  | 158         | 52.4        | 158         | 52.1        | <u>158</u>  | <u>52.1</u> | 141         | 58.6        | <u>141</u>  | <u>58.5</u> | 141         | 58.4        |
| 459.GemsFDTD  | <u>48.5</u> | <u>219</u>  | 49.9        | 213         | 48.4        | 219         | 41.4        | 256         | <u>41.6</u> | <u>255</u>  | 41.7        | 255         |
| 465.tonto     | 260         | 37.9        | 263         | 37.4        | <u>262</u>  | <u>37.5</u> | 180         | 54.7        | 180         | 54.6        | <u>180</u>  | <u>54.6</u> |
| 470.lbm       | 17.8        | 770         | 17.8        | 773         | <u>17.8</u> | <u>772</u>  | 17.8        | 770         | 17.8        | 773         | <u>17.8</u> | <u>772</u>  |
| 481.wrf       | <u>103</u>  | <u>108</u>  | 105         | 106         | 99.4        | 112         | <u>103</u>  | <u>108</u>  | 105         | 106         | 99.4        | 112         |
| 482.sphinx3   | 301         | 64.8        | 300         | 64.9        | <u>301</u>  | <u>64.9</u> | 301         | 64.8        | 300         | 64.9        | <u>301</u>  | <u>64.9</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Configuration:

Operating Mode set to Maximum Performance

Intel Hyperthreading set to Disabled

Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on DaAn-02 Fri May 27 18:16:53 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

Continued on next page



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**Hardware Availability:** Mar-2016

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**Software Availability:** Dec-2015

### Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2618L v4 @ 2.20GHz

2 "physical id"s (chips)

20 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 10

siblings : 10

physical 0: cores 0 1 2 3 4 8 9 10 11 12

physical 1: cores 0 1 2 3 4 8 9 10 11 12

cache size : 25600 KB

From /proc/meminfo

MemTotal: 263829888 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 1

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP1"

VERSION\_ID="12.1"

PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP1"

ID="sles"

ANSI\_COLOR="0;32"

CPE\_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:

Linux DaAn-02 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015  
(8d714a0) x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 May 27 18:16

SPEC is set to: /home/cpu2006-1.2-ic16.0

Filesystem Type Size Used Avail Use% Mounted on

/dev/sda4 xfs 688G 28G 661G 5% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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## Platform Notes (Continued)

BIOS LENOVO -[TBE124K-2.10]- 05/10/2016

Memory:

16x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz, configured at 2133 MHz  
8x NO DIMM Unknown

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB  
memory using RedHat EL 7.1

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

447.dealII: -DSPEC\_CPU\_LP64

450.soplex: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags



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Software Availability: Dec-2015

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

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## Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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