



SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint®_rate2006 = 1280

Lenovo Flex System x240 M5
(2.40 GHz, Intel Xeon E5-2680 v4)

SPECint_rate_base2006 = 1230

CPU2006 license: 9017

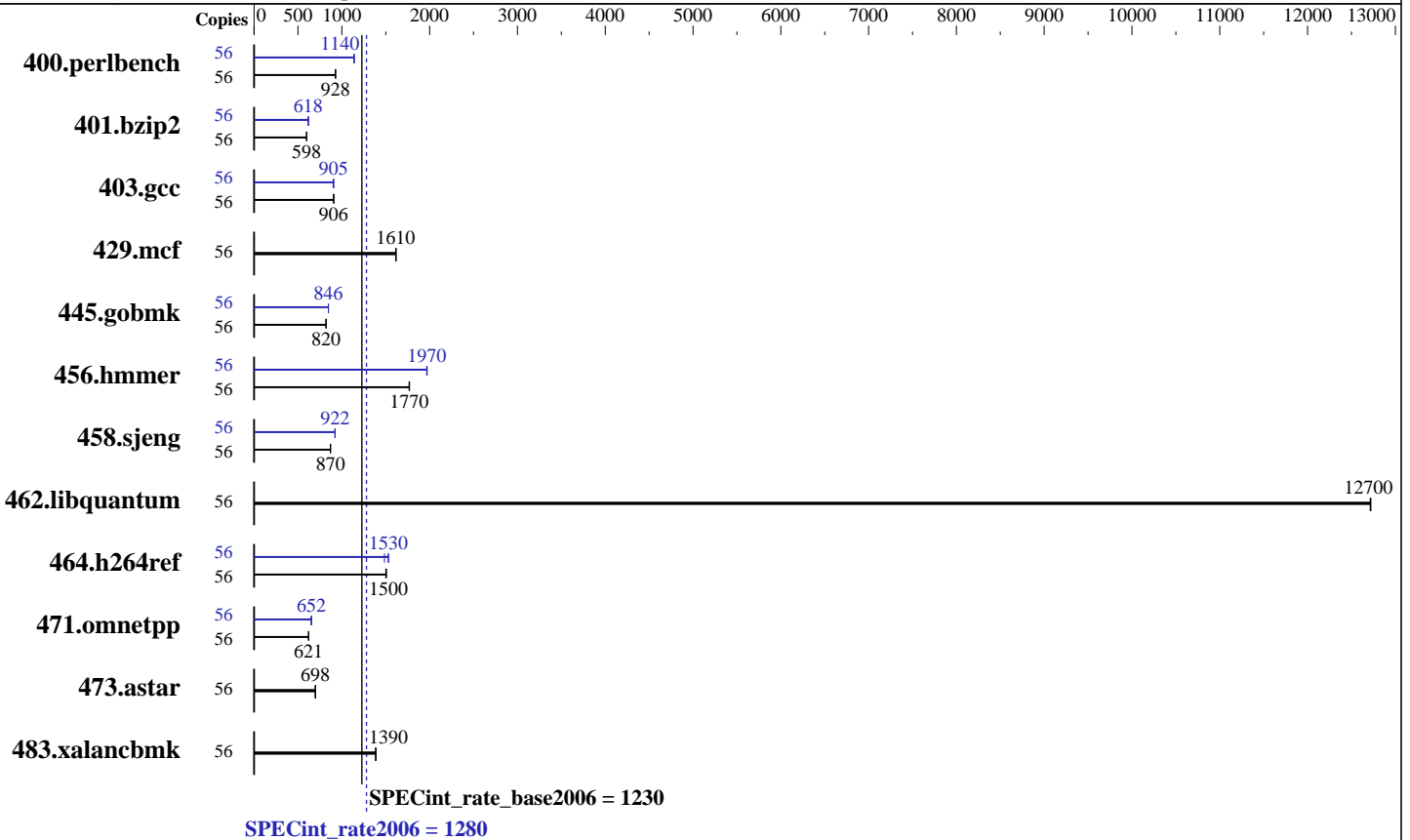
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Apr-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E5-2680 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 35 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
 Disk Subsystem: 2 x 300 GB 10000 RPM SAS, RAID 0
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
 Kernel 3.12.49-11-default
 Compiler: C/C++; Version 16.0.0.101 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 1280

Lenovo Flex System x240 M5
(2.40 GHz, Intel Xeon E5-2680 v4)

SPECint_rate_base2006 = 1230

CPU2006 license: 9017

Test date: Apr-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	56	591	926	589	929	<u>590</u>	<u>928</u>	56	480	1140	<u>480</u>	<u>1140</u>	481	1140
401.bzip2	56	904	598	901	600	<u>903</u>	<u>598</u>	56	873	619	<u>874</u>	<u>618</u>	875	618
403.gcc	56	<u>497</u>	<u>906</u>	498	906	496	908	56	500	901	497	908	<u>498</u>	<u>905</u>
429.mcf	56	<u>316</u>	<u>1610</u>	316	1620	316	1610	56	<u>316</u>	<u>1610</u>	316	1620	316	1610
445.gobmk	56	717	820	<u>717</u>	<u>820</u>	717	819	56	<u>694</u>	<u>846</u>	694	846	693	847
456.hammer	56	<u>295</u>	<u>1770</u>	296	1770	295	1770	56	<u>265</u>	<u>1970</u>	265	1970	266	1970
458.sjeng	56	<u>779</u>	<u>870</u>	779	870	779	870	56	<u>735</u>	<u>922</u>	735	922	735	922
462.libquantum	56	<u>91.2</u>	<u>12700</u>	91.3	12700	91.2	12700	56	<u>91.2</u>	<u>12700</u>	91.3	12700	91.2	12700
464.h264ref	56	<u>825</u>	<u>1500</u>	826	1500	821	1510	56	809	1530	<u>811</u>	<u>1530</u>	834	1490
471.omnetpp	56	<u>563</u>	<u>621</u>	563	622	564	621	56	535	654	<u>536</u>	<u>652</u>	537	652
473.astar	56	<u>563</u>	<u>698</u>	562	699	563	698	56	<u>563</u>	<u>698</u>	562	699	563	698
483.xalancbmk	56	<u>279</u>	<u>1390</u>	279	1380	278	1390	56	<u>279</u>	<u>1390</u>	279	1380	278	1390

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches

Platform Notes

BIOS Configuration:
Operating Mode set to "Maximum Performance"
COD Preference set to Enable
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on x240m5-cpu2006 Thu Apr 14 06:09:23 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2680 v4@ 2.40GHz
Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 1280

Lenovo Flex System x240 M5
(2.40 GHz, Intel Xeon E5-2680 v4)

SPECint_rate_base2006 = 1230

CPU2006 license: 9017

Test date: Apr-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Platform Notes (Continued)

2 "physical id"s (chips)
56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 14
siblings  : 28
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 17920 KB
```

From /proc/meminfo

```
MemTotal:      263824888 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

From /etc/*release* /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
```

os-release:

```
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

uname -a:

```
Linux x240m5-cpu2006 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Apr 14 06:07

SPEC is set to: /home/cpu2006-1.2-ic16.0

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   515G  7.1G  508G   2% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO -[C4E123H-2.10]- 03/25/2016

Memory:

```
16x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz
Continued on next page
```



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 1280

Lenovo Flex System x240 M5
(2.40 GHz, Intel Xeon E5-2680 v4)

SPECint_rate_base2006 = 1230

CPU2006 license: 9017

Test date: Apr-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Platform Notes (Continued)

8x NO DIMM Unknown

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 1280

Lenovo Flex System x240 M5
(2.40 GHz, Intel Xeon E5-2680 v4)

SPECint_rate_base2006 = 1230

CPU2006 license: 9017

Test date: Apr-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Base Optimization Flags (Continued)

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmarthearp

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64

403.gcc: -D_FILE_OFFSET_BITS=64

429.mcf: -D_FILE_OFFSET_BITS=64

445.gobmk: -D_FILE_OFFSET_BITS=64

456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64

458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64

462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

464.h264ref: -D_FILE_OFFSET_BITS=64

471.omnetpp: -D_FILE_OFFSET_BITS=64

473.astar: -D_FILE_OFFSET_BITS=64

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 1280

Lenovo Flex System x240 M5
(2.40 GHz, Intel Xeon E5-2680 v4)

SPECint_rate_base2006 = 1230

CPU2006 license: 9017

Test date: Apr-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Peak Optimization Flags

C benchmarks:

- 400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
- 401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias
- 403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div
- 429.mcf: basepeak = yes
- 445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias
-opt-mem-layout-trans=3
- 456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
- 458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-auto-ilp32
- 462.libquantum: basepeak = yes
- 464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

- 471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap
- 473.astar: basepeak = yes
- 483.xalancbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint_rate2006 = 1280

Lenovo Flex System x240 M5
(2.40 GHz, Intel Xeon E5-2680 v4)

SPECint_rate_base2006 = 1230

CPU2006 license: 9017

Test date: Apr-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue May 3 18:01:17 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 May 2016.