



SPEC® CFP2006 Result

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Lenovo Group Limited

SPECfp®2006 = **115**

Lenovo Flex System x240 M5
(2.40 GHz, Intel Xeon E5-2683 v4)

SPECfp_base2006 = **109**

CPU2006 license: 9017

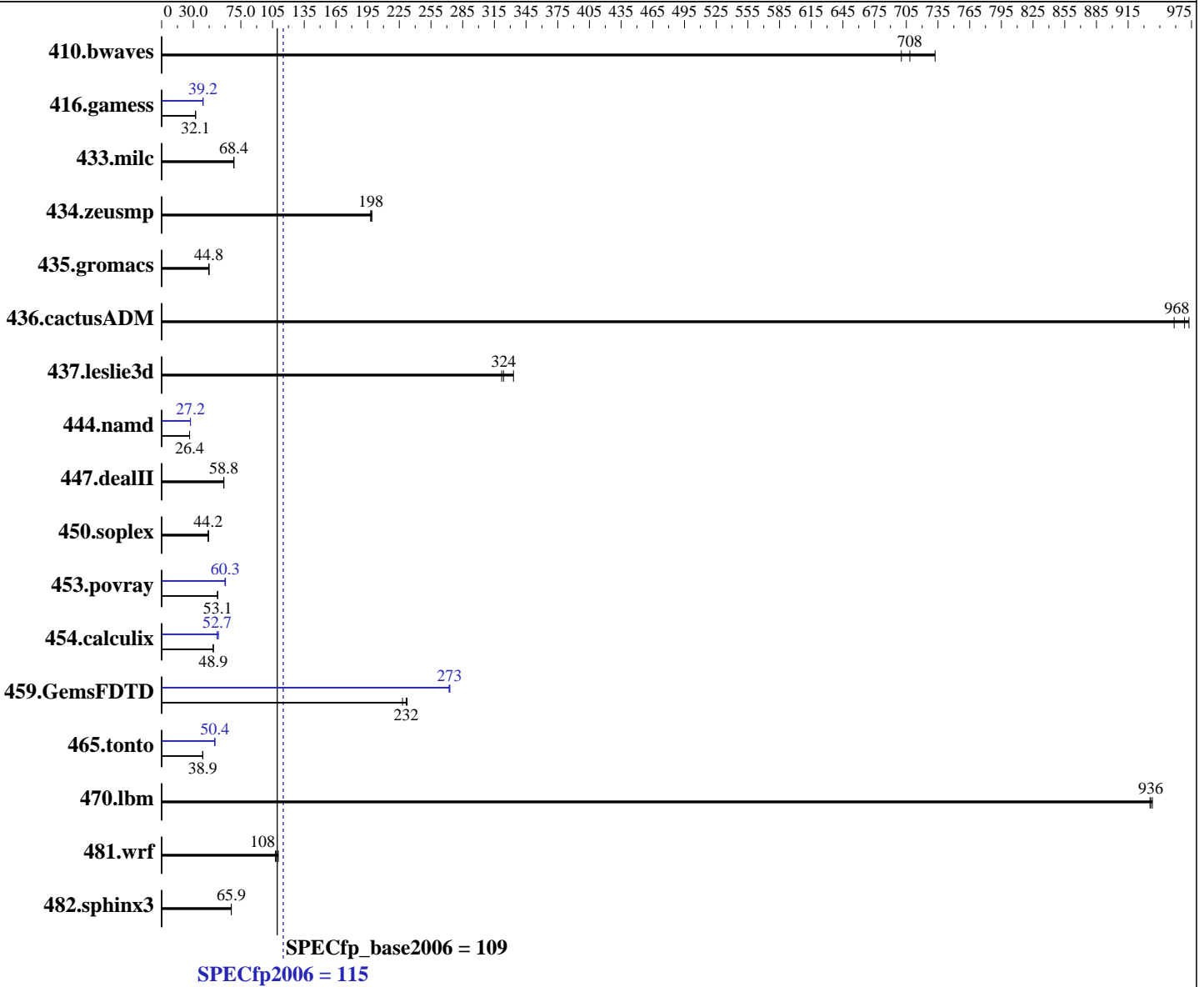
Test date: Apr-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E5-2683 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
 Kernel 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit

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L3 Cache: 40 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 2 x 300 GB 10000 RPM SAS, RAID 0
Other Hardware: None

Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>19.2</u>	<u>708</u>	19.4	700	18.6	732	<u>19.2</u>	<u>708</u>	19.4	700	18.6	732
416.gamess	<u>610</u>	<u>32.1</u>	609	32.2	611	32.0	<u>500</u>	<u>39.2</u>	499	39.2	501	39.1
433.milc	<u>134</u>	<u>68.4</u>	134	68.4	134	68.3	<u>134</u>	<u>68.4</u>	134	68.4	134	68.3
434.zeusmp	45.7	199	<u>45.9</u>	<u>198</u>	46.0	198	45.7	199	<u>45.9</u>	<u>198</u>	46.0	198
435.gromacs	<u>159</u>	<u>44.8</u>	159	44.8	159	44.8	<u>159</u>	<u>44.8</u>	159	44.8	159	44.8
436.cactusADM	<u>12.3</u>	<u>968</u>	12.5	959	12.3	973	<u>12.3</u>	<u>968</u>	12.5	959	12.3	973
437.leslie3d	<u>29.0</u>	<u>324</u>	29.2	322	28.2	333	<u>29.0</u>	<u>324</u>	29.2	322	28.2	333
444.namd	303	26.4	<u>303</u>	<u>26.4</u>	304	26.4	294	27.3	294	27.2	<u>294</u>	<u>27.2</u>
447.dealII	<u>195</u>	<u>58.8</u>	195	58.8	194	59.0	<u>195</u>	<u>58.8</u>	195	58.8	194	59.0
450.soplex	188	44.3	<u>189</u>	<u>44.2</u>	189	44.1	188	44.3	<u>189</u>	<u>44.2</u>	189	44.1
453.povray	101	52.9	100	53.1	<u>100</u>	<u>53.1</u>	<u>88.2</u>	<u>60.3</u>	88.0	60.4	88.8	59.9
454.calculix	169	48.9	<u>169</u>	<u>48.9</u>	169	48.8	153	53.8	<u>157</u>	<u>52.7</u>	157	52.7
459.GemsFDTD	45.7	232	46.5	228	<u>45.8</u>	<u>232</u>	39.0	272	<u>38.9</u>	<u>273</u>	38.9	273
465.tonto	252	39.0	253	38.9	<u>253</u>	<u>38.9</u>	196	50.2	<u>195</u>	<u>50.4</u>	195	50.4
470.lbm	<u>14.7</u>	<u>936</u>	14.6	938	14.7	936	<u>14.7</u>	<u>936</u>	14.6	938	14.7	936
481.wrf	<u>103</u>	<u>108</u>	104	108	101	110	<u>103</u>	<u>108</u>	104	108	101	110
482.sphinx3	<u>296</u>	<u>65.9</u>	296	66.0	296	65.9	<u>296</u>	<u>65.9</u>	296	66.0	296	65.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:
Operating Mode set to Maximum Performance
Intel Hyperthreading set to Disabled
COD Preference set to Disable
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on ip10-245-48-52 Mon Apr 4 23:26:28 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) CPU E5-2683 v4 @ 2.10GHz
 2 "physical id"s (chips)
 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores     : 16
  siblings      : 16
  physical 0:   cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1:   cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size      : 40960 KB

```

From /proc/meminfo

```

MemTotal:      263828392 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

From /etc/*release* /etc/*version*

```

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

uname -a:

```

Linux ip10-245-48-52 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Apr 4 17:55

SPEC is set to: /home/cpu2006-1.2-ic16.0

```

Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda4        xfs       515G      7.0G  508G   2% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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Platform Notes (Continued)

BIOS LENOVO -[C4E123H-2.10]- 03/25/2016

Memory:

16x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz

8x NO DIMM Unknown

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

OMP_NUM_THREADS = "32"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

447.dealII: -DSPEC_CPU_LP64

450.soplex: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:
 -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
 -ansi-alias

C++ benchmarks:
 -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
 -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
 -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
 -ansi-alias

Peak Compiler Invocation

C benchmarks:
 icc -m64

C++ benchmarks:
 icpc -m64

Fortran benchmarks:
 ifort -m64

Benchmarks using both Fortran and C:
 icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags



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Software Availability: Dec-2015

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Default-Platform-Flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Default-Platform-Flags.xml>

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