



SPEC® CFP2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL460c Gen9
(2.60 GHz, Intel Xeon E5-2690 v4)

SPECfp®2006 = 125

SPECfp_base2006 = 120

CPU2006 license: 3

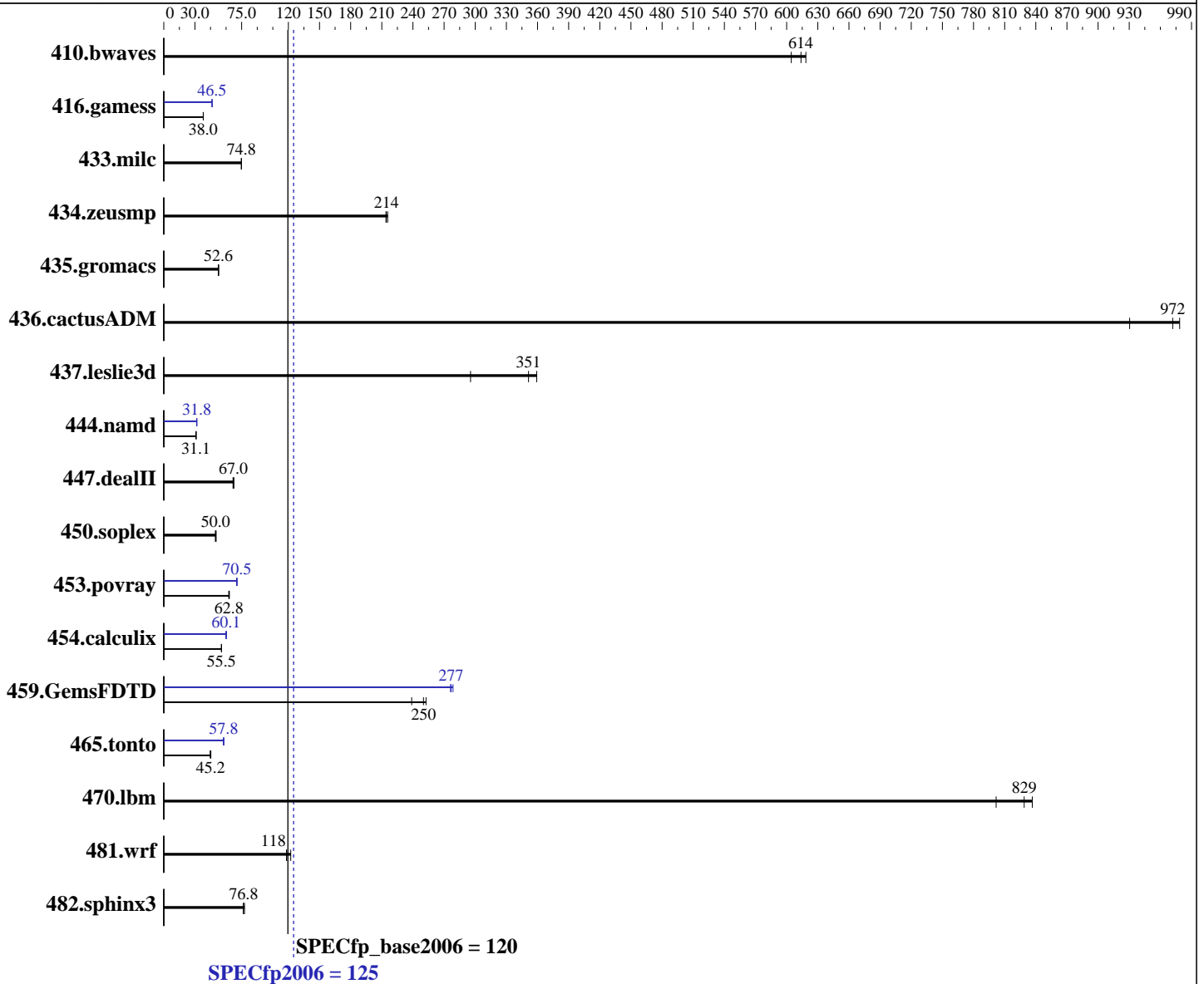
Test sponsor: HPE

Tested by: HPE

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E5-2690 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SuSE Linux Enterprise 12 (x86_64) SP 1
 Kernel 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 5 (multi-user, w/GUI)

Continued on next page



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L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 2 x 500 GB SAS HDD 10 K, RAID 1
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	22.0	619	<u>22.1</u>	<u>614</u>	22.5	604	22.0	619	<u>22.1</u>	<u>614</u>	22.5	604
416.gamess	514	38.1	<u>515</u>	<u>38.0</u>	515	38.0	421	46.5	421	46.5	<u>421</u>	<u>46.5</u>
433.milc	123	74.9	123	74.6	<u>123</u>	<u>74.8</u>	123	74.9	123	74.6	<u>123</u>	<u>74.8</u>
434.zeusmp	42.5	214	<u>42.4</u>	<u>214</u>	42.2	216	42.5	214	<u>42.4</u>	<u>214</u>	42.2	216
435.gromacs	135	52.9	<u>136</u>	<u>52.6</u>	136	52.5	135	52.9	<u>136</u>	<u>52.6</u>	136	52.5
436.cactusADM	12.2	979	12.8	930	<u>12.3</u>	<u>972</u>	12.2	979	12.8	930	<u>12.3</u>	<u>972</u>
437.leslie3d	26.2	359	31.8	295	<u>26.8</u>	<u>351</u>	26.2	359	31.8	295	<u>26.8</u>	<u>351</u>
444.namd	<u>258</u>	<u>31.1</u>	258	31.1	258	31.1	252	31.8	<u>252</u>	<u>31.8</u>	253	31.7
447.dealII	<u>171</u>	<u>67.0</u>	169	67.5	172	66.7	<u>171</u>	<u>67.0</u>	169	67.5	172	66.7
450.soplex	<u>167</u>	<u>50.0</u>	165	50.4	168	49.7	<u>167</u>	<u>50.0</u>	165	50.4	168	49.7
453.povray	84.2	63.2	85.0	62.6	<u>84.7</u>	<u>62.8</u>	75.9	70.1	75.3	70.6	<u>75.5</u>	<u>70.5</u>
454.calculix	149	55.5	148	55.6	<u>149</u>	<u>55.5</u>	138	59.9	<u>137</u>	<u>60.1</u>	137	60.3
459.GemsFDTD	44.4	239	42.0	253	<u>42.4</u>	<u>250</u>	<u>38.3</u>	<u>277</u>	38.1	279	38.4	276
465.tonto	220	44.7	217	45.3	<u>218</u>	<u>45.2</u>	170	57.8	<u>170</u>	<u>57.8</u>	171	57.4
470.lbm	16.4	837	<u>16.6</u>	<u>829</u>	17.1	802	16.4	837	<u>16.6</u>	<u>829</u>	17.1	802
481.wrf	<u>94.4</u>	<u>118</u>	94.5	118	91.3	122	<u>94.4</u>	<u>118</u>	94.5	118	91.3	122
482.sphinx3	<u>254</u>	<u>76.8</u>	251	77.7	255	76.5	<u>254</u>	<u>76.8</u>	251	77.7	255	76.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration:
Intel Hyperthreading Option set to Enabled
Power Profile set to Custom
Power Regulator set to Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C1E State
Minimum Processor Idle Power Package C-State set to No Package State
Collaborative Power Control set to Disabled
QPI Snoop Configuration set to Home Snoop

Continued on next page



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Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh
Sysinfo program /home/cpuv1.3/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1
running on bl460c2-gen9-b Wed Mar 30 16:20:59 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E5-2690 v4@ 2.60GHz
 2 "physical id"s (chips)
 56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores      : 14
  siblings       : 28
  physical 0:    : cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1:    : cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size      : 35840 KB
```

From /proc/meminfo

```
MemTotal:      264325200 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

/usr/bin/lsb_release -d

```
SUSE Linux Enterprise Server 12 SP1
```

From /etc/*release* /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
```

os-release:

```
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

uname -a:

```
Linux bl460c2-gen9-b 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

Continued on next page



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Platform Notes (Continued)

run-level 5 Mar 30 16:17

SPEC is set to: /home/cpuv1.3/cpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	424G	218G	206G	52%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I36 02/22/2016

Memory:

8x UNKNOWN NOT AVAILABLE

8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as:
8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/cpuv1.3/cpu2006/libs/32:/home/cpuv1.3/cpu2006/libs/64:/home/cpuv1.3/cpu2006/sh"

OMP_NUM_THREADS = "28"

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB memory using RedHat EL 7.2

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

Continued on next page

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Page 5



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Peak Compiler Invocation (Continued)

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

Continued on next page

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Page 6



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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.xml>

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