



SPEC® CFP2006 Result

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Lenovo Group Limited

SPECfp®_rate2006 = 2880

Lenovo System x3950 X6
(Intel Xeon E7-8891 v3, 2.80 GHz)

SPECfp_rate_base2006 = 3060

CPU2006 license: 9017

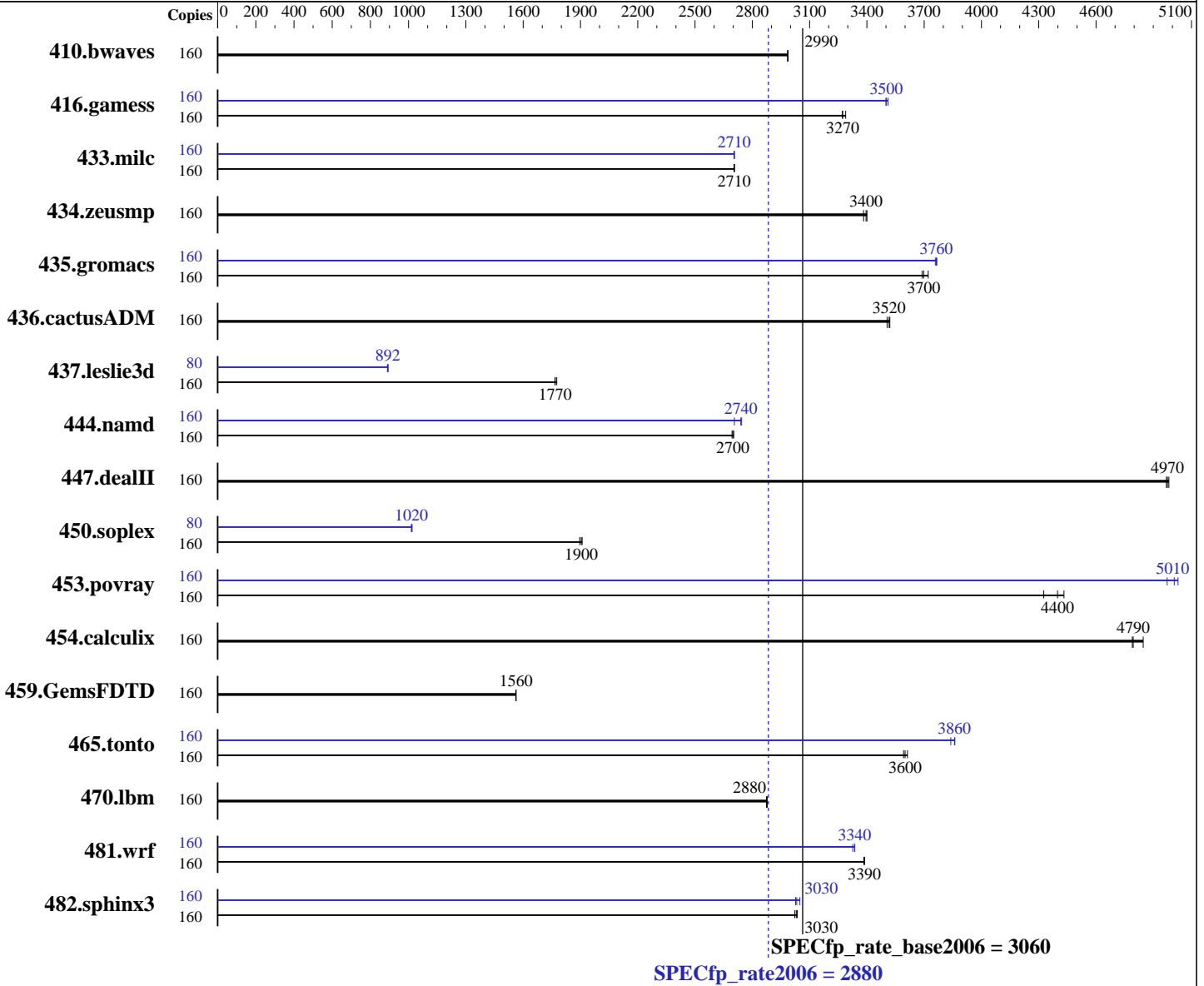
Test date: Jan-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Jul-2015

Tested by: Lenovo Group Limited

Software Availability: Oct-2014



Hardware

CPU Name: Intel Xeon E7-8891 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 80 cores, 8 chips, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 4, 6, 8 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64)
 3.12.28-4-default
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache: 45 MB I+D on chip per chip
Other Cache: None
Memory: 2 TB (128 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem: 1 x 400 GB SSD
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|-------------|-------------|-------------|------------|-------------|--------|-------------|-------------|-------------|-------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 160 | 728 | 2990 | 728 | 2980 | 728 | 2990 | 160 | 728 | 2990 | 728 | 2980 | 728 | 2990 |
| 416.gamess | 160 | 952 | 3290 | 958 | 3270 | 957 | 3270 | 160 | 895 | 3500 | 895 | 3500 | 892 | 3510 |
| 433.milc | 160 | 543 | 2710 | 543 | 2710 | 543 | 2710 | 160 | 543 | 2700 | 543 | 2710 | 543 | 2710 |
| 434.zeusmp | 160 | 429 | 3400 | 430 | 3380 | 428 | 3400 | 160 | 429 | 3400 | 430 | 3380 | 428 | 3400 |
| 435.gromacs | 160 | 307 | 3720 | 310 | 3690 | 309 | 3700 | 160 | 303 | 3770 | 304 | 3760 | 304 | 3760 |
| 436.cactusADM | 160 | 545 | 3510 | 544 | 3520 | 543 | 3520 | 160 | 545 | 3510 | 544 | 3520 | 543 | 3520 |
| 437.leslie3d | 160 | 851 | 1770 | 851 | 1770 | 847 | 1780 | 80 | 843 | 892 | 843 | 892 | 846 | 889 |
| 444.namd | 160 | 476 | 2690 | 475 | 2700 | 476 | 2700 | 160 | 474 | 2710 | 468 | 2740 | 468 | 2740 |
| 447.dealII | 160 | 367 | 4980 | 368 | 4970 | 368 | 4970 | 160 | 367 | 4980 | 368 | 4970 | 368 | 4970 |
| 450.soplex | 160 | 699 | 1910 | 704 | 1900 | 701 | 1900 | 80 | 657 | 1020 | 655 | 1020 | 657 | 1010 |
| 453.povray | 160 | 197 | 4330 | 192 | 4430 | 194 | 4400 | 160 | 170 | 5010 | 171 | 4970 | 169 | 5030 |
| 454.calculix | 160 | 276 | 4790 | 275 | 4790 | 272 | 4850 | 160 | 276 | 4790 | 275 | 4790 | 272 | 4850 |
| 459.GemsFDTD | 160 | 1087 | 1560 | 1086 | 1560 | 1087 | 1560 | 160 | 1087 | 1560 | 1086 | 1560 | 1087 | 1560 |
| 465.tonto | 160 | 436 | 3610 | 438 | 3590 | 437 | 3600 | 160 | 408 | 3860 | 408 | 3860 | 410 | 3840 |
| 470.lbm | 160 | 764 | 2880 | 765 | 2870 | 764 | 2880 | 160 | 764 | 2880 | 765 | 2870 | 764 | 2880 |
| 481.wrf | 160 | 528 | 3380 | 528 | 3390 | 528 | 3390 | 160 | 536 | 3340 | 537 | 3330 | 536 | 3340 |
| 482.sphinx3 | 160 | 1032 | 3020 | 1029 | 3030 | 1027 | 3040 | 160 | 1023 | 3050 | 1028 | 3030 | 1030 | 3030 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Operating Mode set to Custom in BIOS
Cstates disabled in BIOS
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6914
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Software Availability: Oct-2014

Platform Notes (Continued)

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on linux-lea3 Tue Oct 13 21:37:47 2015

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```

model name : Intel(R) Xeon(R) CPU E7-8891 v3 @ 2.80GHz
 8 "physical id"s (chips)
160 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 10
  siblings  : 20
  physical 0: cores 0 1 2 4 6 8 17 19 20 23
  physical 1: cores 0 1 2 4 6 8 17 19 20 23
  physical 2: cores 0 1 2 4 6 8 17 19 20 23
  physical 3: cores 0 1 2 4 6 8 17 19 20 23
  physical 4: cores 0 1 2 4 6 8 17 19 20 23
  physical 5: cores 0 1 2 4 6 8 17 19 20 23
  physical 6: cores 0 1 2 4 6 8 17 19 20 23
  physical 7: cores 0 1 2 4 6 8 17 19 20 23
cache size : 46080 KB

```

From /proc/meminfo

```

MemTotal:      2117742048 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

/usr/bin/lsb_release -d

SUSE Linux Enterprise Server 12

From /etc/*release* /etc/*version*

```

SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 0
  # This file is deprecated and will be removed in a future service pack or
  # release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12"
  VERSION_ID="12"
  PRETTY_NAME="SUSE Linux Enterprise Server 12"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12"

```

uname -a:

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Platform Notes (Continued)

Linux linux-lea3 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 13 11:22 last=5

SPEC is set to: /cpu2006.1.2

| Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
|------------|------|------|------|-------|------|------------|
| /dev/sda3 | xfs | 371G | 8.0G | 363G | 3% | / |

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[A9E125JUS-2.00]- 06/18/2015

Memory:

126x Hynix HMA42GR7MFR4N-TF 16 GB 2 rank 2133 MHz, configured at 1600 MHz
2x Hynix HMA42GR7MFR4N-TFTD 16 GB 2 rank 2133 MHz, configured at 1600 MHz
64x NO DIMM Unknown

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:

icc -m64

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Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.deallI: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2)
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
 -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
 -unroll2

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-CC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-CC.xml>

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