



SPEC[®] CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint[®]2006 = **55.5**

Lenovo System x3500 M5
(Intel Xeon E5-2630L v3, 1.80 GHz)

SPECint_base2006 = **52.9**

CPU2006 license: 9017

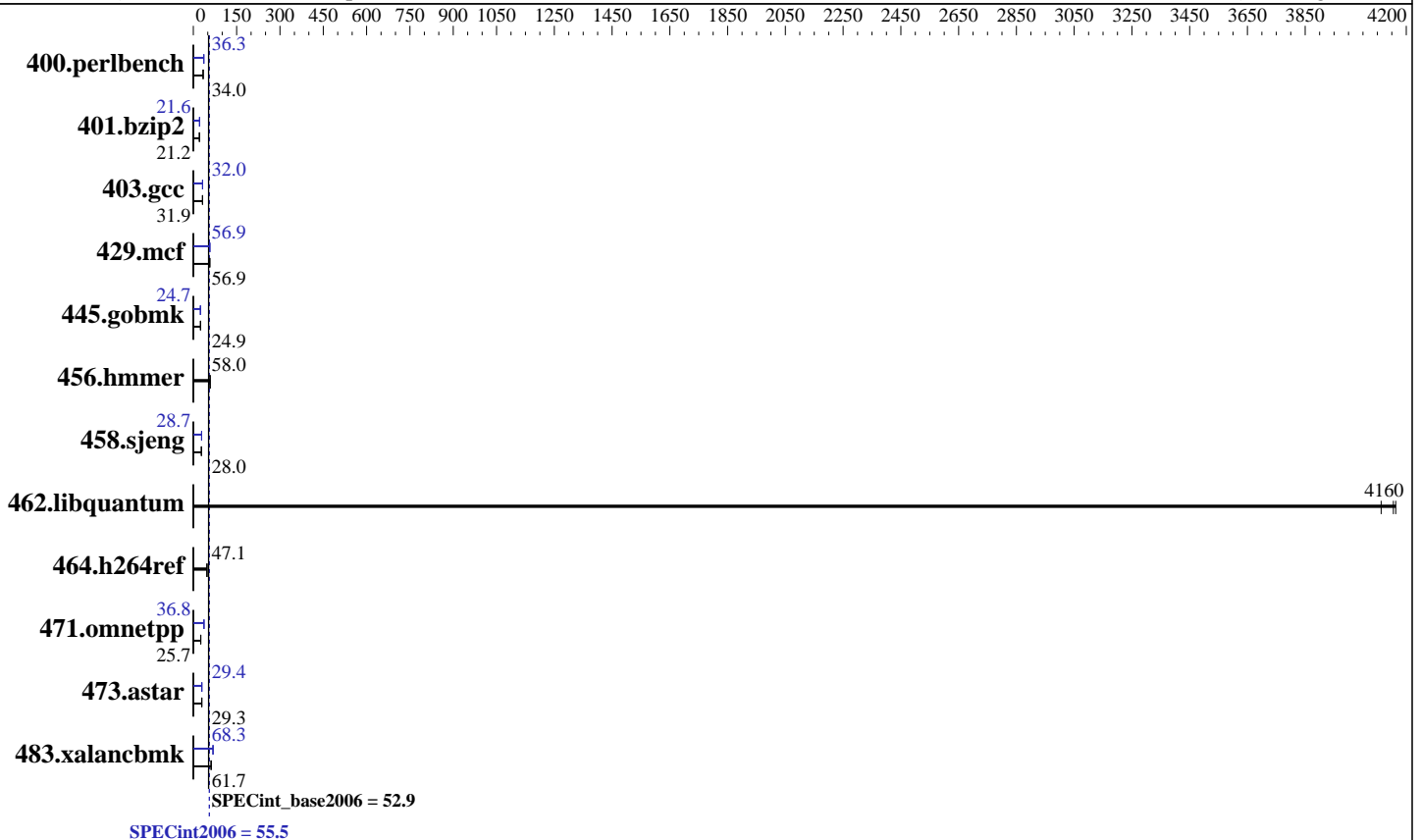
Test date: Dec-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2015

Tested by: Lenovo Group Limited

Software Availability: Aug-2015



Hardware

CPU Name: Intel Xeon E5-2630L v3
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
 Disk Subsystem: 1 x 960 GB SATA SSD
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
 3.10.0-123.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint2006 = **55.5**

Lenovo System x3500 M5
(Intel Xeon E5-2630L v3, 1.80 GHz)

SPECint_base2006 = **52.9**

CPU2006 license: 9017

Test date: Dec-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2015

Tested by: Lenovo Group Limited

Software Availability: Aug-2015

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	287	34.0	288	34.0	<u>288</u>	<u>34.0</u>	<u>269</u>	<u>36.3</u>	269	36.3	269	36.3
401.bzip2	<u>456</u>	<u>21.2</u>	455	21.2	458	21.1	447	21.6	446	21.6	<u>446</u>	<u>21.6</u>
403.gcc	252	31.9	253	31.9	<u>253</u>	<u>31.9</u>	251	32.1	252	32.0	<u>251</u>	<u>32.0</u>
429.mcf	161	56.7	160	57.1	<u>160</u>	<u>56.9</u>	158	57.7	<u>160</u>	<u>56.9</u>	161	56.8
445.gobmk	421	24.9	<u>421</u>	<u>24.9</u>	421	24.9	425	24.7	426	24.6	<u>425</u>	<u>24.7</u>
456.hammer	161	58.0	<u>161</u>	<u>58.0</u>	161	58.0	161	58.0	<u>161</u>	<u>58.0</u>	161	58.0
458.sjeng	432	28.0	431	28.0	<u>432</u>	<u>28.0</u>	422	28.7	422	28.7	<u>422</u>	<u>28.7</u>
462.libquantum	<u>4.99</u>	<u>4160</u>	5.04	4110	4.98	4160	<u>4.99</u>	<u>4160</u>	5.04	4110	4.98	4160
464.h264ref	<u>470</u>	<u>47.1</u>	467	47.4	470	47.1	<u>470</u>	<u>47.1</u>	467	47.4	470	47.1
471.omnetpp	244	25.6	241	25.9	<u>243</u>	<u>25.7</u>	169	36.9	170	36.7	<u>170</u>	<u>36.8</u>
473.astar	240	29.3	239	29.3	<u>239</u>	<u>29.3</u>	<u>238</u>	<u>29.4</u>	238	29.5	239	29.4
483.xalancbmk	112	61.7	112	61.8	<u>112</u>	<u>61.7</u>	101	68.3	<u>101</u>	<u>68.3</u>	101	68.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS setting:

Operating Mode set to "Maximum Performance"

Hyper-threading set to "Disable"

Snoop mode set to "Early Snoop"

Sysinfo program /home/SPEC_ic16/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1

running on x3500M5 Thu Dec 3 16:53:58 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2630L v3 @ 1.80GHz

2 "physical id"s (chips)

16 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

<http://www.spec.org/>

Page 2



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint2006 = 55.5

Lenovo System x3500 M5
(Intel Xeon E5-2630L v3, 1.80 GHz)

SPECint_base2006 = 52.9

CPU2006 license: 9017

Test date: Dec-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2015

Tested by: Lenovo Group Limited

Software Availability: Aug-2015

Platform Notes (Continued)

```

cpu cores : 8
siblings  : 8
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB

```

From /proc/meminfo

```

MemTotal:      263457684 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/*release* /etc/*version*

```

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

```

uname -a:

```

Linux x3500M5 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64
x86_64 x86_64 GNU/Linux

```

SPEC is set to: /home/SPEC_ic16

```

Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs  927G  147G  780G  16% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[TAE105J-1.10]- 04/20/2015

Memory:

```

6x Hynix HMA42GR7MFR4N-TF 16 GB 2 rank 2133 MHz, configured at 1866 MHz
10x Hynix HMA42GR7MFR4N-TFT1 16 GB 2 rank 2133 MHz, configured at 1866 MHz
8x NO DIMM Unknown

```

(End of data from sysinfo program)



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint2006 = 55.5

Lenovo System x3500 M5
(Intel Xeon E5-2630L v3, 1.80 GHz)

SPECint_base2006 = 52.9

CPU2006 license: 9017

Test date: Dec-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2015

Tested by: Lenovo Group Limited

Software Availability: Aug-2015

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/SPEC_ic16/libs/32:/home/SPEC_ic16/libs/64:/home/SPEC_ic16/sh"

OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

471.omnetpp: -DSPEC_CPU_LP64

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32

-Wl,-z,muldefs -L/sh -lsmarthheap64



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint2006 = 55.5

Lenovo System x3500 M5
(Intel Xeon E5-2630L v3, 1.80 GHz)

SPECint_base2006 = 52.9

CPU2006 license: 9017

Test date: Dec-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2015

Tested by: Lenovo Group Limited

Software Availability: Aug-2015

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -D_FILE_OFFSET_BITS=64

456.hmmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

471.omnetpp: -D_FILE_OFFSET_BITS=64

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint2006 = 55.5

Lenovo System x3500 M5
(Intel Xeon E5-2630L v3, 1.80 GHz)

SPECint_base2006 = 52.9

CPU2006 license: 9017

Test date: Dec-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2015

Tested by: Lenovo Group Limited

Software Availability: Aug-2015

Peak Optimization Flags (Continued)

401.bzip2 (continued):

-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc

-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel

-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)

-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)

-par-num-threads=1(pass 1) -prof-use(pass 2)

-opt-ra-region-strategy=block

-ansi-alias

-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-D.20150923.html>



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint2006 = 55.5

Lenovo System x3500 M5
(Intel Xeon E5-2630L v3, 1.80 GHz)

SPECint_base2006 = 52.9

CPU2006 license: 9017

Test date: Dec-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2015

Tested by: Lenovo Group Limited

Software Availability: Aug-2015

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-D.20150923.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Dec 30 19:57:25 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 29 December 2015.