



# SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

## NEC Corporation

**SPECint®2006 = 59.0**

Express5800/R120f-1E (Intel Xeon E5-2630 v3)

**SPECint\_base2006 = 56.4**

CPU2006 license: 9006

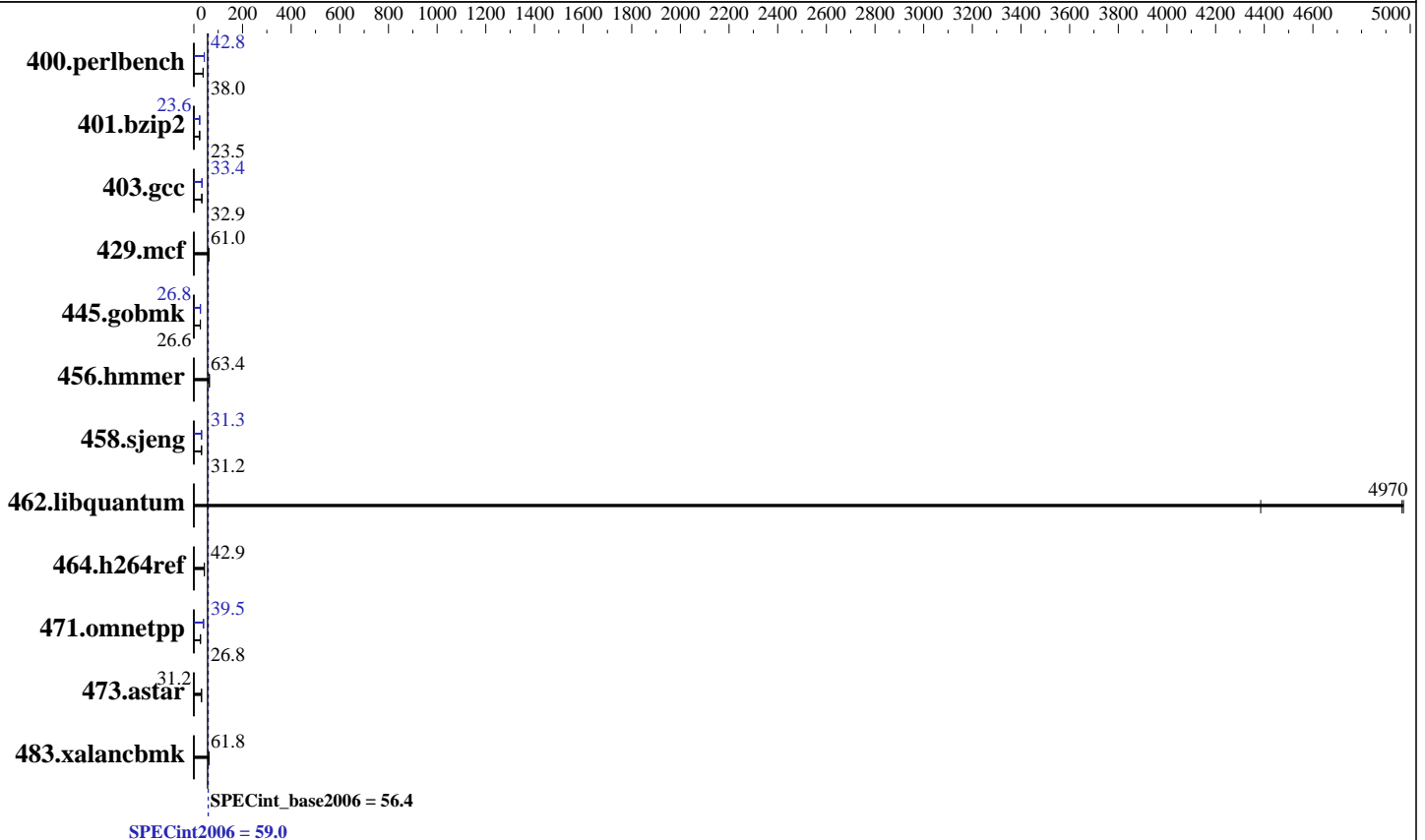
Test date: May-2015

Test sponsor: NEC Corporation

Hardware Availability: Jan-2015

Tested by: NEC Corporation

Software Availability: Jul-2014



### Hardware

CPU Name: Intel Xeon E5-2630 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)  
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 Kernel 2.6.32-431.20.3.el6.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

## NEC Corporation

SPECint2006 = **59.0**

Express5800/R120f-1E (Intel Xeon E5-2630 v3)

SPECint\_base2006 = **56.4**

CPU2006 license: 9006

Test date: May-2015

Test sponsor: NEC Corporation

Hardware Availability: Jan-2015

Tested by: NEC Corporation

Software Availability: Jul-2014

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	257	38.0	256	38.1	<u>257</u>	<u>38.0</u>	228	42.8	228	42.9	<u>228</u>	<u>42.8</u>
401.bzip2	<u>411</u>	<u>23.5</u>	413	23.4	411	23.5	<u>409</u>	<u>23.6</u>	408	23.6	409	23.6
403.gcc	<u>245</u>	<u>32.9</u>	245	32.9	246	32.8	241	33.5	242	33.2	<u>241</u>	<u>33.4</u>
429.mcf	149	61.2	150	60.7	<u>150</u>	<u>61.0</u>	149	61.2	150	60.7	<u>150</u>	<u>61.0</u>
445.gobmk	<u>395</u>	<u>26.6</u>	395	26.6	395	26.6	391	26.8	<u>391</u>	<u>26.8</u>	391	26.8
456.hammer	147	63.4	<u>147</u>	<u>63.4</u>	147	63.4	147	63.4	<u>147</u>	<u>63.4</u>	147	63.4
458.sjeng	388	31.2	387	31.3	<u>387</u>	<u>31.2</u>	386	31.4	386	31.3	<u>386</u>	<u>31.3</u>
462.libquantum	4.72	4390	<u>4.17</u>	<u>4970</u>	4.17	4970	4.72	4390	<u>4.17</u>	<u>4970</u>	4.17	4970
464.h264ref	518	42.8	514	43.0	<u>515</u>	<u>42.9</u>	518	42.8	514	43.0	<u>515</u>	<u>42.9</u>
471.omnetpp	233	26.8	230	27.1	<u>233</u>	<u>26.8</u>	158	39.5	159	39.4	<u>158</u>	<u>39.5</u>
473.astar	225	31.1	225	31.2	<u>225</u>	<u>31.2</u>	225	31.1	225	31.2	<u>225</u>	<u>31.2</u>
483.xalancbmk	111	62.1	<u>112</u>	<u>61.8</u>	112	61.7	111	62.1	<u>112</u>	<u>61.8</u>	112	61.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Power Management Policy: Custom  
Energy Performance: Performance  
Patrol Scrub: Disabled  
Hyper-Threading: Disabled

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "16"

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 2



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

NEC Corporation

SPECint2006 = 59.0

Express5800/R120f-1E (Intel Xeon E5-2630 v3)

SPECint\_base2006 = 56.4

CPU2006 license: 9006

Test date: May-2015

Test sponsor: NEC Corporation

Hardware Availability: Jan-2015

Tested by: NEC Corporation

Software Availability: Jul-2014

## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64  
400.perlbench: icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32  
445.gobmk: icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

NEC Corporation

SPECint2006 = 59.0

Express5800/R120f-1E (Intel Xeon E5-2630 v3)

SPECint\_base2006 = 56.4

CPU2006 license: 9006

Test date: May-2015

Test sponsor: NEC Corporation

Hardware Availability: Jan-2015

Tested by: NEC Corporation

Software Availability: Jul-2014

## Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
 -opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-alloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4

462.libquantum: basepeak = yes

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

NEC Corporation

SPECint2006 = 59.0

Express5800/R120f-1E (Intel Xeon E5-2630 v3)

SPECint\_base2006 = 56.4

CPU2006 license: 9006

Test date: May-2015

Test sponsor: NEC Corporation

Hardware Availability: Jan-2015

Tested by: NEC Corporation

Software Availability: Jul-2014

## Peak Optimization Flags (Continued)

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Tue Jun 2 13:47:09 2015 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 2 June 2015.