



SPEC[®] CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECfp[®]_rate2006 = 571

PRIMERGY BX2560 M1, Intel Xeon E5-2630 v3, 2.4 GHz

SPECfp_rate_base2006 = 556

CPU2006 license: 19

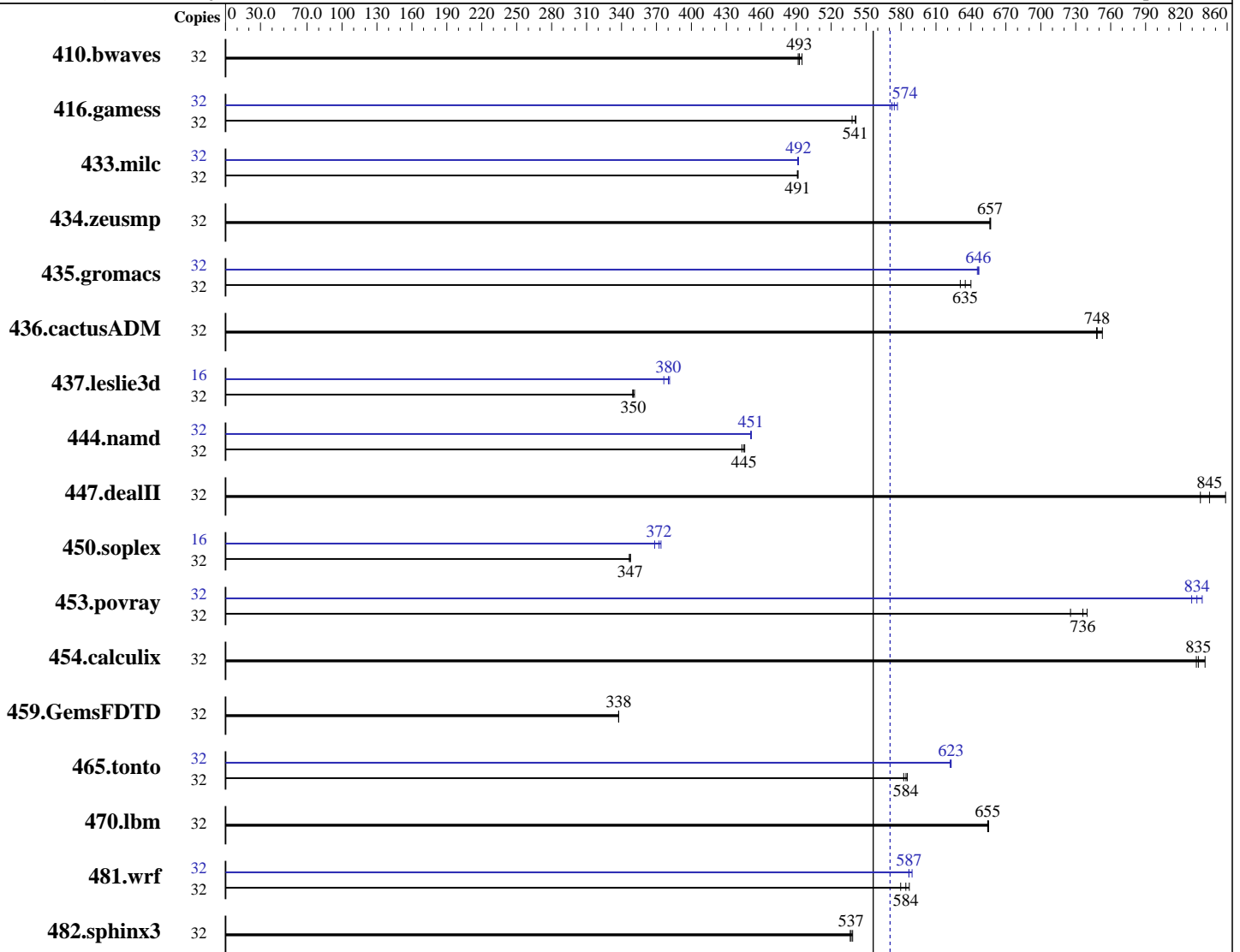
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014



SPECfp_rate_base2006 = 556

SPECfp_rate2006 = 571

Hardware

CPU Name: Intel Xeon E5-2630 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
 Kernel 3.10.0-123.8.1.el7.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: xfs

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECfp_rate2006 = **571**

PRIMERGY BX2560 M1, Intel Xeon E5-2630 v3, 2.4 GHz

SPECfp_rate_base2006 = **556**

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014

L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	32	884	492	879	495	883	493	32	884	492	879	495	883	493		
416.gamess	32	1165	538	1159	541	1158	541	32	1086	577	1095	572	1091	574		
433.milc	32	598	491	597	492	598	491	32	598	491	597	492	597	492		
434.zeusmp	32	443	657	444	656	444	657	32	443	657	444	656	444	657		
435.gromacs	32	357	640	362	631	360	635	32	353	646	354	646	353	647		
436.cactusADM	32	511	748	511	748	508	753	32	511	748	511	748	508	753		
437.leslie3d	32	857	351	859	350	860	350	16	394	381	396	380	400	376		
444.namd	32	577	445	579	443	576	446	32	569	451	569	451	568	452		
447.dealII	32	437	837	433	845	426	859	32	437	837	433	845	426	859		
450.soplex	32	768	347	770	347	768	348	16	357	374	358	372	362	368		
453.povray	32	230	740	231	736	235	726	32	203	839	205	830	204	834		
454.calculix	32	314	841	316	835	317	834	32	314	841	316	835	317	834		
459.GemsFDTD	32	1006	338	1006	338	1006	338	32	1006	338	1006	338	1006	338		
465.tonto	32	538	585	541	582	539	584	32	506	623	505	623	506	622		
470.lbm	32	672	655	672	654	671	655	32	672	655	672	654	671	655		
481.wrf	32	612	584	609	587	617	580	32	609	587	606	589	609	587		
482.sphinx3	32	1163	536	1161	537	1158	538	32	1163	536	1161	537	1158	538		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Energy Performance = Performance
Utilization Profile = Unbalanced

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECfp_rate2006 = 571

PRIMERGY BX2560 M1, Intel Xeon E5-2630 v3, 2.4 GHz

SPECfp_rate_base2006 = 556

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Dec-2014
Hardware Availability: Sep-2014
Software Availability: Sep-2014

Platform Notes (Continued)

QPI snoop mode: Home Snoop
COD Enable = Disabled, Early Snoop = Disabled
CPU C1E Support = Disabled

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
memory using RedHat EL 7.0
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECfp_rate2006 = 571

PRIMERGY BX2560 M1, Intel Xeon E5-2630 v3, 2.4 GHz

SPECfp_rate_base2006 = 556

CPU2006 license: 19

Test date: Dec-2014

Test sponsor: Fujitsu

Hardware Availability: Sep-2014

Tested by: Fujitsu

Software Availability: Sep-2014

Base Portability Flags (Continued)

459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECfp_rate2006 = 571

PRIMERGY BX2560 M1, Intel Xeon E5-2630 v3, 2.4 GHz

SPECfp_rate_base2006 = 556

CPU2006 license: 19

Test date: Dec-2014

Test sponsor: Fujitsu

Hardware Availability: Sep-2014

Tested by: Fujitsu

Software Availability: Sep-2014

Peak Portability Flags (Continued)

```

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
         -auto-ilp32

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
         -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -opt-malloc-options=3

```

```

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
           -ansi-alias

```

Fortran benchmarks:

410.bwaves: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECfp_rate2006 = 571

PRIMERGY BX2560 M1, Intel Xeon E5-2630 v3, 2.4 GHz

SPECfp_rate_base2006 = 556

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014

Peak Optimization Flags (Continued)

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Tue May 19 18:14:55 2015 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 19 May 2015.