



SPEC® CFP2006 Result

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Huawei

SPECfp®_rate2006 = 763

Huawei RH2288 V3 (Intel Xeon E5-2658A v3)

SPECfp_rate_base2006 = 743

CPU2006 license: 3175

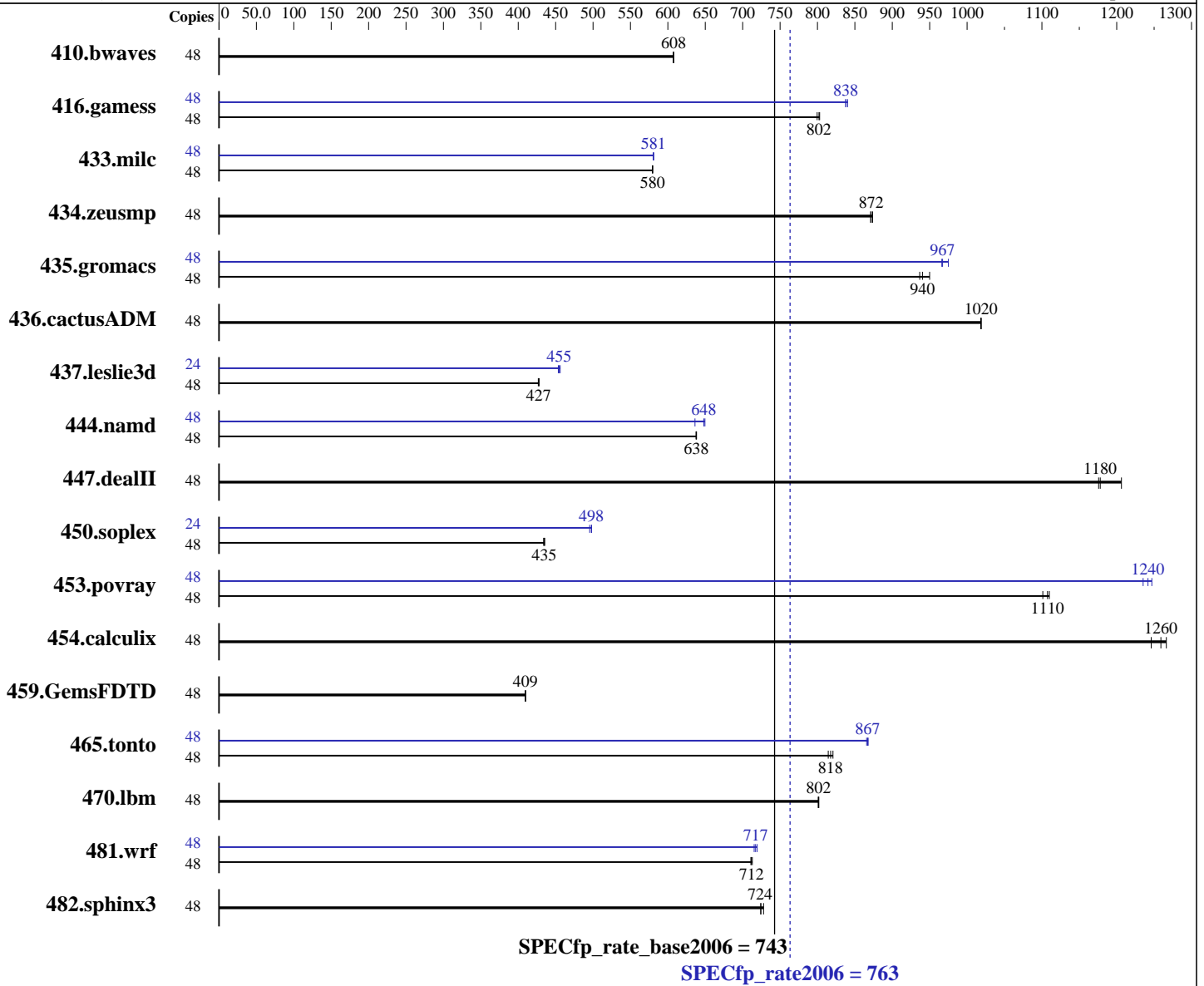
Test sponsor: Huawei

Tested by: Huawei

Test date: Mar-2015

Hardware Availability: Mar-2015

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2658A v3
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
 3.10.0-123.el7.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: xfs

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L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|--------------------|-------------------|--------------------|-------------------|--------------------|--------------------|--------|--------------------|-------------------|--------------------|--------------------|--------------------|--------------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 48 | 1074 | 608 | <u>1074</u> | <u>608</u> | 1074 | 607 | 48 | 1074 | 608 | <u>1074</u> | <u>608</u> | 1074 | 607 |
| 416.gamess | 48 | 1175 | 800 | 1171 | 803 | <u>1172</u> | <u>802</u> | 48 | 1122 | 838 | <u>1122</u> | <u>838</u> | 1118 | 840 |
| 433.milc | 48 | <u>760</u> | <u>580</u> | 760 | 580 | 761 | 579 | 48 | 759 | 580 | <u>759</u> | <u>581</u> | 758 | 581 |
| 434.zeusmp | 48 | 500 | 874 | 501 | 871 | <u>501</u> | <u>872</u> | 48 | 500 | 874 | 501 | 871 | <u>501</u> | <u>872</u> |
| 435.gromacs | 48 | 361 | 950 | 366 | 937 | <u>364</u> | <u>940</u> | 48 | 352 | 975 | 355 | 966 | <u>354</u> | <u>967</u> |
| 436.cactusADM | 48 | 563 | 1020 | 563 | 1020 | <u>563</u> | <u>1020</u> | 48 | 563 | 1020 | 563 | 1020 | <u>563</u> | <u>1020</u> |
| 437.leslie3d | 48 | 1055 | 428 | <u>1056</u> | <u>427</u> | 1057 | 427 | 24 | 497 | 454 | <u>496</u> | <u>455</u> | 495 | 456 |
| 444.namd | 48 | 603 | 638 | <u>603</u> | <u>638</u> | 604 | 638 | 48 | 605 | 636 | <u>594</u> | <u>648</u> | 593 | 650 |
| 447.dealII | 48 | 455 | 1210 | 467 | 1180 | <u>466</u> | <u>1180</u> | 48 | 455 | 1210 | 467 | 1180 | <u>466</u> | <u>1180</u> |
| 450.soplex | 48 | 919 | 435 | 923 | 434 | <u>921</u> | <u>435</u> | 24 | <u>402</u> | <u>498</u> | 404 | 495 | 402 | 498 |
| 453.povray | 48 | 232 | 1100 | 230 | 1110 | <u>231</u> | <u>1110</u> | 48 | 207 | 1240 | <u>206</u> | <u>1240</u> | 205 | 1250 |
| 454.calculix | 48 | 313 | 1270 | 318 | 1250 | <u>315</u> | <u>1260</u> | 48 | 313 | 1270 | 318 | 1250 | <u>315</u> | <u>1260</u> |
| 459.GemsFDTD | 48 | <u>1244</u> | <u>409</u> | 1243 | 410 | 1244 | 409 | 48 | <u>1244</u> | <u>409</u> | 1243 | 410 | 1244 | 409 |
| 465.tonto | 48 | 580 | 814 | 576 | 821 | <u>578</u> | <u>818</u> | 48 | 546 | 866 | <u>545</u> | <u>867</u> | 544 | 868 |
| 470.lbm | 48 | <u>823</u> | <u>802</u> | 823 | 801 | 823 | 802 | 48 | <u>823</u> | <u>802</u> | 823 | 801 | 823 | 802 |
| 481.wrf | 48 | <u>753</u> | <u>712</u> | 752 | 713 | 754 | 711 | 48 | <u>748</u> | <u>717</u> | 745 | 719 | 749 | 715 |
| 482.sphinx3 | 48 | 1285 | 728 | 1292 | 724 | <u>1292</u> | <u>724</u> | 48 | 1285 | 728 | 1292 | 724 | <u>1292</u> | <u>724</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to COD

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Platform Notes (Continued)

```

Set Patrol Scrub to Disable
Baseboard Management Controller used to adjust the fan speed to 100%
Sysinfo program /spec/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Wed Mar 25 03:49:20 2015

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2658A v3 @ 2.20GHz
 2 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 6
  siblings     : 12
  physical 0:  cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1:  cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size     : 15360 KB

```

```

From /proc/meminfo
MemTotal:      263575160 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Mar 22 06:19

```

SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       xfs   445G  209G  236G  47% /

```

Additional information from dmidecode:

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Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 1.23 11/19/2014

Memory:

8x Micron 36ASF2G72PZ-2G1A2 16 GB 1 rank 2133 MHz

8x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

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info@spec.org

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Software Availability: Sep-2014

Base Portability Flags (Continued)

```

434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

```

Fortran benchmarks:

```

ifort -m64

```

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2)
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
 -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2)
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
 -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2)
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)

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Peak Optimization Flags (Continued)

450.soplex (continued):

-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2)

-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4

-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2

-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2)

-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)

-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml>



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For other inquiries, please contact webmaster@spec.org.

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