SPEC® CFP2006 Result

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Lenovo Group Limited

$SPECfp^®\_rate2006 =$ 152

Lenovo System x3250 M5
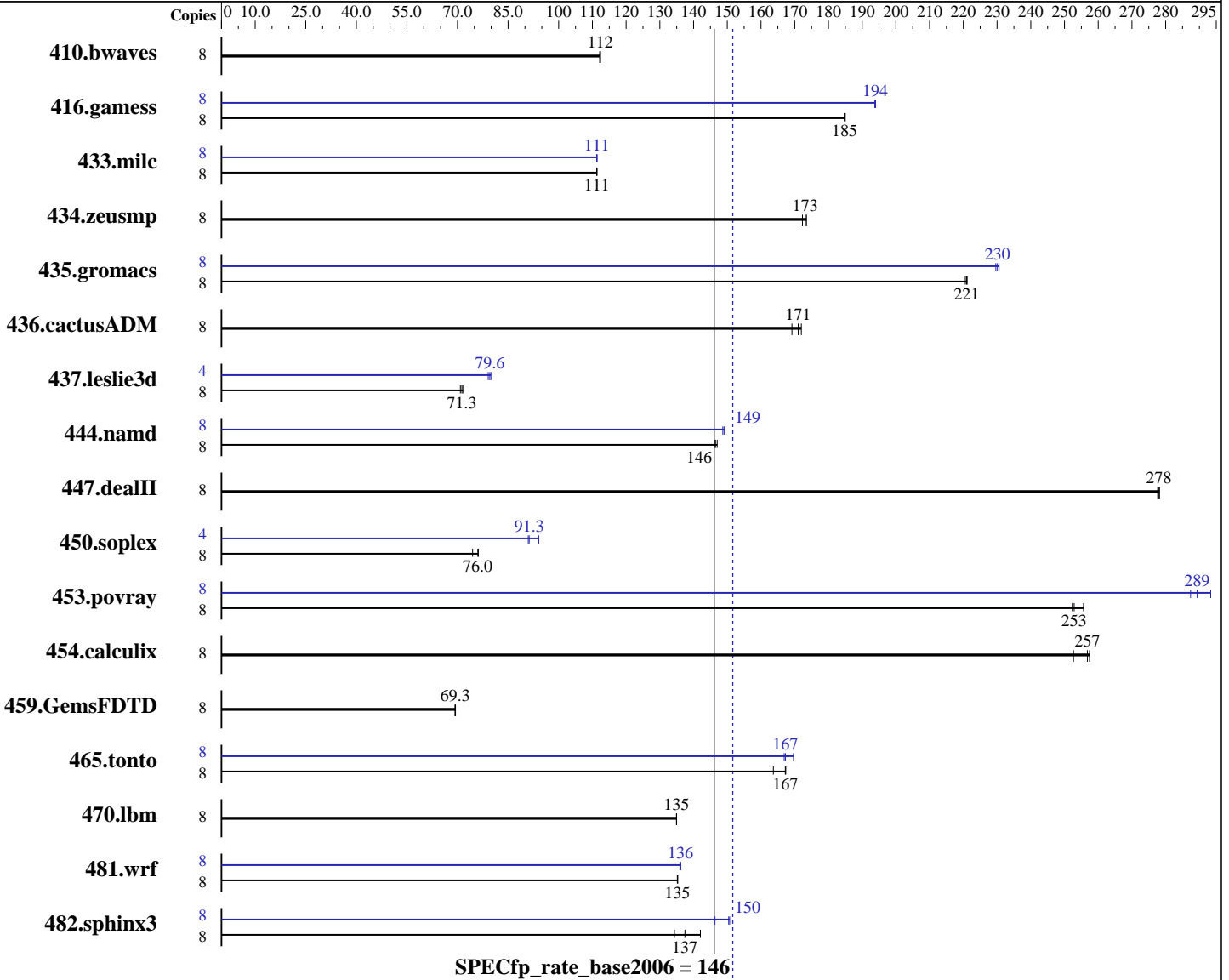(Intel Xeon E3-1231 v3, 3.40 GHz)

$SPECfp\_rate\_base2006 =$ 146

CPU2006 license: 9017	**Test date:**	Dec-2014
Test sponsor: Lenovo Group Limited	**Hardware Availability:**	Jul-2014
Tested by: Lenovo Group Limited	**Software Availability:**	Nov-2013

Benchmark	Copies	Result
410.bwaves	8	112
416.gamess	8 / 8	194 / 185
433.milc	8 / 8	111 / 111
434.zeusmp	8	173
435.gromacs	8 / 8	230 / 221
436.cactusADM	8	171
437.leslie3d	4 / 8	79.6 / 71.3
444.namd	8 / 8	149 / 146
447.dealII	8	278
450.soplex	4 / 8	91.3 / 76.0
453.povray	8 / 8	289 / 253
454.calculix	8	257
459.GemsFDTD	8	69.3
465.tonto	8 / 8	167 / 167
470.lbm	8	135
481.wrf	8 / 8	136 / 135
482.sphinx3	8 / 8	150 / 137

SPECfp_rate_base2006 = 146

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Hardware

CPU Name:	Intel Xeon E3-1231 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.80 GHz
CPU MHz:	3400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

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L3 Cache:	8 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	16 GB (4 x 4 GB 2Rx8 PC3-12800U-13)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x 500 GB SATA, 7200 RPM	Other Software:	None
Other Hardware:	None		

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	969	112	**968**	**112**	968	112	8	969	112	**968**	**112**	968	112
416.gamess	8	847	185	848	185	**848**	**185**	8	808	194	808	194	**808**	**194**
433.milc	8	660	111	**660**	**111**	660	111	8	**660**	**111**	659	111	660	111
434.zeusmp	8	423	172	**420**	**173**	420	173	8	423	172	**420**	**173**	420	173
435.gromacs	8	258	221	**259**	**221**	259	221	8	249	230	**248**	**230**	248	231
436.cactusADM	8	565	169	556	172	**559**	**171**	8	565	169	556	172	**559**	**171**
437.leslie3d	8	**1055**	**71.3**	1050	71.6	1061	70.8	4	470	79.9	475	79.1	**473**	**79.6**
444.namd	8	438	146	**438**	**146**	436	147	8	432	149	**430**	**149**	430	149
447.dealII	8	330	278	**329**	**278**	329	278	8	330	278	**329**	**278**	329	278
450.soplex	8	896	74.5	**878**	**76.0**	876	76.2	4	367	91.0	**366**	**91.3**	354	94.1
453.povray	8	**168**	**253**	169	252	166	256	8	**147**	**289**	145	293	148	287
454.calculix	8	261	253	**257**	**257**	256	257	8	261	253	**257**	**257**	256	257
459.GemsFDTD	8	1223	69.4	1225	69.3	**1224**	**69.3**	8	1223	69.4	1225	69.3	**1224**	**69.3**
465.tonto	8	470	167	**471**	**167**	481	164	8	**471**	**167**	472	167	464	170
470.lbm	8	815	135	**814**	**135**	814	135	8	815	135	**814**	**135**	814	135
481.wrf	8	660	135	661	135	**661**	**135**	8	656	136	**656**	**136**	657	136
482.sphinx3	8	**1134**	**137**	1161	134	1098	142	8	1035	151	1065	146	**1037**	**150**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The taskset mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate taskset commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

    Sysinfo program /home/SPEC_ic14/config/sysinfo.rev6818
    $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
    running on localhost.localdomain Wed Dec 17 08:36:35 2014

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Hardware Availability: Jul-2014
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Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E3-1231 v3 @ 3.40GHz
      1 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 8
      physical 0: cores 0 1 2 3
   cache size : 8192 KB

From /proc/meminfo
   MemTotal:       16165980 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
   EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 16 23:19

SPEC is set to: /home/SPEC_ic14
   Filesystem                   Type  Size  Used Avail Use% Mounted on
   /dev/mapper/VolGroup-lv_home ext4  860G   12G  805G   2% /home

Additional information from dmidecode:
  BIOS IBM -[JUE115CUS-1.06]- 11/11/2014
  Memory:
   4x Hynix/Hyundai HMT351U7EFR8A-PB 4 GB 1600 MHz 2 rank

(End of data from sysinfo program)
```

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General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPEC_ic14/libs/32:/home/SPEC_ic14/libs/64:/home/SPEC_ic14/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
       icc   -m64

C++ benchmarks:
       icpc   -m64

Fortran benchmarks:
       ifort -m64

Benchmarks using both Fortran and C:
       icc   -m64 ifort -m64

Base Portability Flags

```
        410.bwaves: -DSPEC_CPU_LP64
        416.gamess: -DSPEC_CPU_LP64
          433.milc: -DSPEC_CPU_LP64
        434.zeusmp: -DSPEC_CPU_LP64
       435.gromacs: -DSPEC_CPU_LP64 -nofor_main
      436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
       437.leslie3d: -DSPEC_CPU_LP64
         444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
        450.soplex: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
       454.calculix: -DSPEC_CPU_LP64 -nofor_main
      459.GemsFDTD: -DSPEC_CPU_LP64
         465.tonto: -DSPEC_CPU_LP64
          470.lbm: -DSPEC_CPU_LP64
          481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
       482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
       -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
       -ansi-alias -opt-mem-layout-trans=3

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Base Optimization Flags (Continued)

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc    -m64
```

```
482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```

```
450.soplex: icpc -m32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

```
      410.bwaves: -DSPEC_CPU_LP64
      416.gamess: -DSPEC_CPU_LP64
        433.milc: -DSPEC_CPU_LP64
      434.zeusmp: -DSPEC_CPU_LP64
     435.gromacs: -DSPEC_CPU_LP64 -nofor_main
   436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
     437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
       447.dealII: -DSPEC_CPU_LP64
      453.povray: -DSPEC_CPU_LP64
     454.calculix: -DSPEC_CPU_LP64 -nofor_main
    459.GemsFDTD: -DSPEC_CPU_LP64
```

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Hardware Availability: Jul-2014
Software Availability: Nov-2013

Peak Portability Flags (Continued)

```
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2)
          -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
          -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
             -unroll2
```

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2)
          -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
          -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2)
            -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
            -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2)
            -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
            -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
```

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Peak Optimization Flags (Continued)

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-A.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-A.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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