



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Huawei

SPECint®2006 = 37.4

Huawei RH1288 V2 (Intel Xeon E5-2650L v2)

SPECint\_base2006 = 34.8

CPU2006 license: 3175

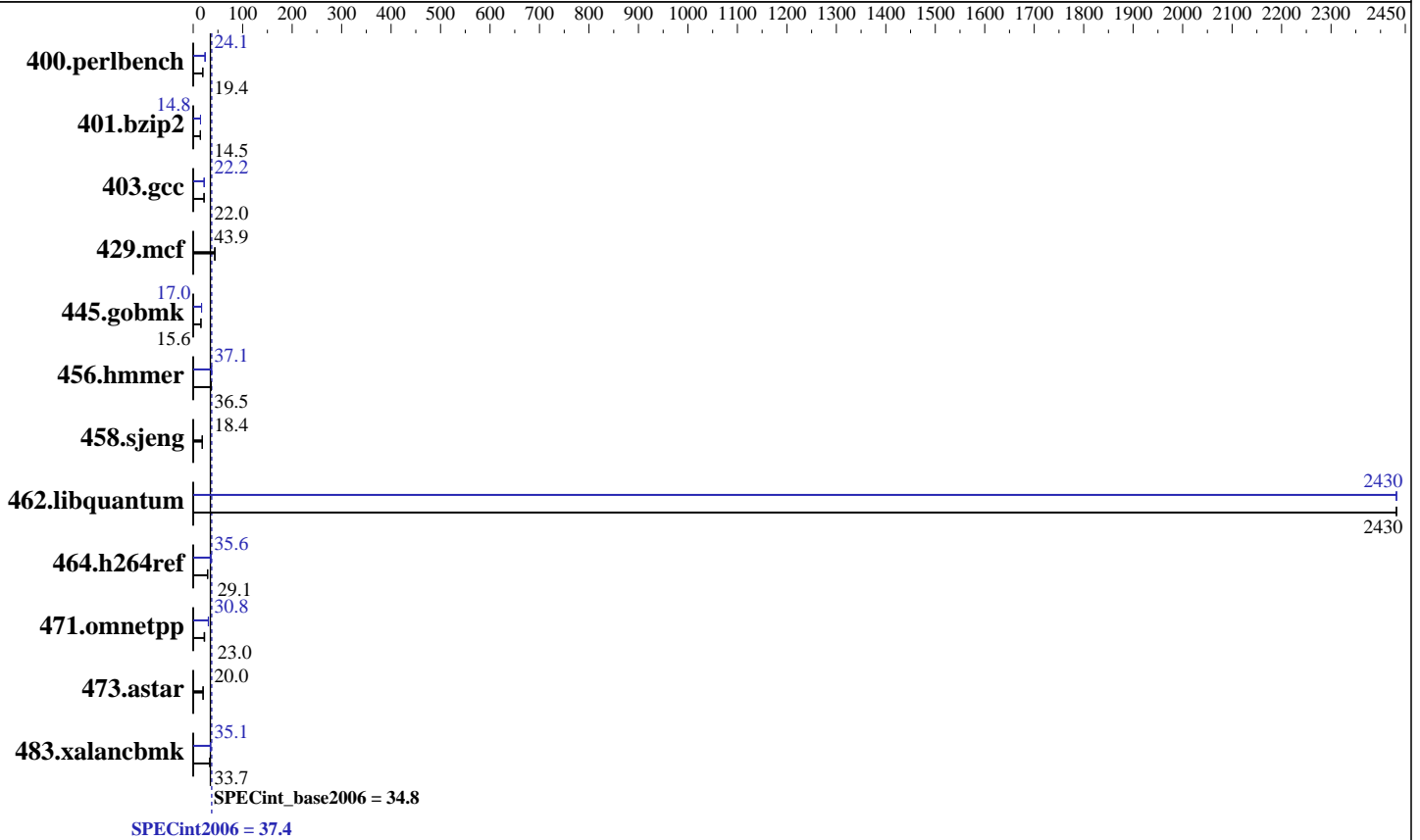
Test sponsor: Huawei

Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Sep-2013

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2650L v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.10 GHz  
 CPU MHz: 1700  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx8 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 300 GB SAS, 10K RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Huawei

SPECint2006 = 37.4

Huawei RH1288 V2 (Intel Xeon E5-2650L v2)

SPECint\_base2006 = 34.8

CPU2006 license: 3175  
Test sponsor: Huawei  
Tested by: Huawei

Test date: Sep-2014  
Hardware Availability: Sep-2013  
Software Availability: Sep-2013

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	505	19.3	504	19.4	<b>504</b>	<b>19.4</b>	405	24.1	<b>405</b>	<b>24.1</b>	405	24.1
401.bzip2	<b>664</b>	<b>14.5</b>	664	14.5	664	14.5	651	14.8	651	14.8	<b>651</b>	<b>14.8</b>
403.gcc	<b>367</b>	<b>22.0</b>	367	22.0	365	22.1	362	22.2	362	22.2	<b>362</b>	<b>22.2</b>
429.mcf	207	44.1	<b>208</b>	<b>43.9</b>	211	43.3	207	44.1	<b>208</b>	<b>43.9</b>	211	43.3
445.gobmk	671	15.6	670	15.6	<b>671</b>	<b>15.6</b>	<b>619</b>	<b>17.0</b>	619	16.9	619	17.0
456.hammer	255	36.6	<b>256</b>	<b>36.5</b>	257	36.4	251	37.1	<b>252</b>	<b>37.1</b>	252	37.0
458.sjeng	659	18.4	660	18.3	<b>659</b>	<b>18.4</b>	659	18.4	660	18.3	<b>659</b>	<b>18.4</b>
462.libquantum	8.52	2430	<b>8.52</b>	<b>2430</b>	8.52	2430	8.52	2430	<b>8.52</b>	<b>2430</b>	8.52	2430
464.h264ref	758	29.2	<b>759</b>	<b>29.1</b>	760	29.1	622	35.6	621	35.6	<b>621</b>	<b>35.6</b>
471.omnetpp	285	22.0	<b>271</b>	<b>23.0</b>	271	23.1	<b>203</b>	<b>30.8</b>	205	30.5	202	30.9
473.astar	351	20.0	350	20.1	<b>351</b>	<b>20.0</b>	351	20.0	350	20.1	<b>351</b>	<b>20.0</b>
483.xalancbmk	204	33.8	<b>205</b>	<b>33.7</b>	205	33.6	196	35.2	197	35.0	<b>196</b>	<b>35.1</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Custom  
Baseboard Management Controller used to adjust the fan speed to 100%  
Set Hyper-Threading to Disabled  
Sysinfo program /spec/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on wbspeccpu Fri Sep 12 12:07:41 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650L v2 @ 1.70GHz
 2 "physical id"s (chips)
 20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 10
siblings : 10
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
```

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Huawei**

**SPECint2006 = 37.4**

**Huawei RH1288 V2 (Intel Xeon E5-2650L v2)**

**SPECint\_base2006 = 34.8**

**CPU2006 license:** 3175  
**Test sponsor:** Huawei  
**Tested by:** Huawei

**Test date:** Sep-2014  
**Hardware Availability:** Sep-2013  
**Software Availability:** Sep-2013

## Platform Notes (Continued)

cache size : 25600 KB

From /proc/meminfo

MemTotal: 132103760 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d

Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/\*release\* /etc/\*version\*

redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:

Linux wbspeccpu 2.6.32-431.el6.x86\_64 #1 SMP Sun Nov 10 22:19:54 EST 2013  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Sep 2 10:15

SPEC is set to: /spec

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdal	ext4	270G	148G	109G	58%	/

Additional information from dmidecode:

Memory:  
16x Hynix HMT41GR7MFR8C-PB 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,0,1"  
LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64"  
OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory using RHEL 6.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint2006 = 37.4

Huawei RH1288 V2 (Intel Xeon E5-2650L v2)

SPECint\_base2006 = 34.8

CPU2006 license: 3175  
Test sponsor: Huawei  
Tested by: Huawei

Test date: Sep-2014  
Hardware Availability: Sep-2013  
Software Availability: Sep-2013

## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64  
400.perlbench: icc -m32  
445.gobmk: icc -m32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint2006 = 37.4

Huawei RH1288 V2 (Intel Xeon E5-2650L v2)

SPECint\_base2006 = 34.8

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Sep-2013

Software Availability: Sep-2013

## Peak Compiler Invocation (Continued)

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
 -opt-prefetch -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -ansi-alias

458.sjeng: basepeak = yes

462.libquantum: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch  
 -auto-p32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint2006 = 37.4

Huawei RH1288 V2 (Intel Xeon E5-2650L v2)

SPECint\_base2006 = 34.8

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Sep-2013

Software Availability: Sep-2013

## Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Dec 10 11:54:04 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 10 December 2014.