



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x3650 M4 BD
(Intel Xeon E5-2620 v2, 2.10 GHz)

SPECfp®_rate2006 = 386

SPECfp_rate_base2006 = 378

CPU2006 license: 11

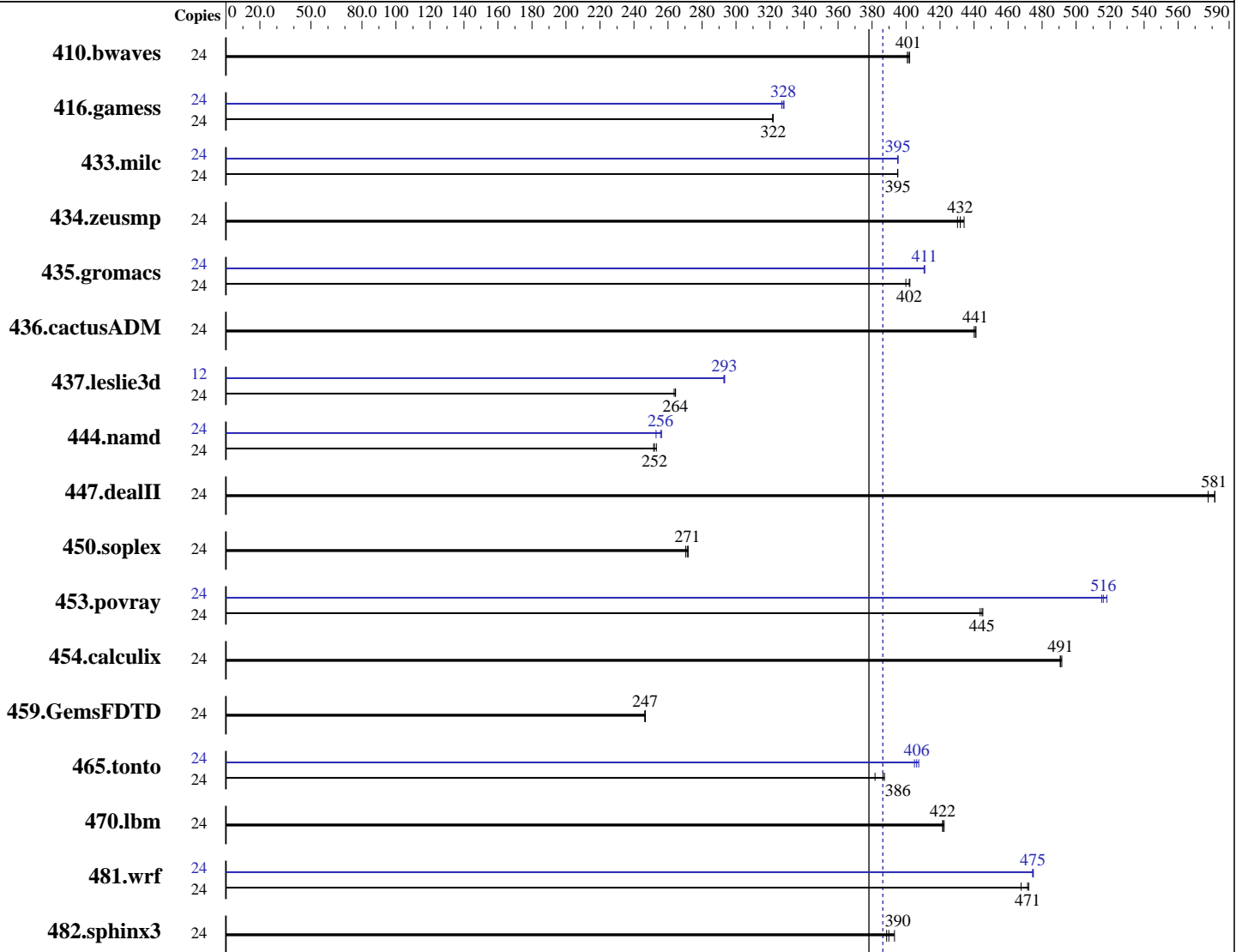
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Aug-2014

Hardware Availability: Jan-2014

Software Availability: Sep-2013



SPECfp_rate_base2006 = 378

SPECfp_rate2006 = 386

Hardware

CPU Name: Intel Xeon E5-2620 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.60 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 2.6.32-358.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x3650 M4 BD
(Intel Xeon E5-2620 v2, 2.10 GHz)

SPECfp_rate2006 = 386

SPECfp_rate_base2006 = 378

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Aug-2014
Hardware Availability: Jan-2014
Software Availability: Sep-2013

L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC, running at 1600 MHz)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	24	814	401	813	401	811	402	24	814	401	813	401	811	402		
416.gamess	24	1460	322	1462	321	1460	322	24	1438	327	1432	328	1433	328		
433.milc	24	558	395	558	395	558	395	24	558	395	557	395	558	395		
434.zeusmp	24	508	430	506	432	503	434	24	508	430	506	432	503	434		
435.gromacs	24	429	400	426	402	426	402	24	417	411	417	411	417	411		
436.cactusADM	24	650	441	652	440	651	441	24	650	441	652	440	651	441		
437.leslie3d	24	853	264	853	264	856	264	12	385	293	385	293	385	293		
444.namd	24	760	253	764	252	765	252	24	751	256	761	253	753	256		
447.dealII	24	475	578	472	581	472	582	24	475	578	472	581	472	582		
450.soplex	24	740	270	736	272	738	271	24	740	270	736	272	738	271		
453.povray	24	287	445	288	444	287	445	24	247	516	248	515	246	518		
454.calculix	24	404	491	403	491	403	492	24	404	491	403	491	403	492		
459.GemsFDTD	24	1034	246	1032	247	1033	247	24	1034	246	1032	247	1033	247		
465.tonto	24	610	387	611	386	619	382	24	581	406	583	405	580	407		
470.ibm	24	782	422	781	422	782	422	24	782	422	781	422	782	422		
481.wrf	24	568	472	569	471	573	468	24	565	474	565	475	564	475		
482.sphinx3	24	1190	393	1204	389	1200	390	24	1190	393	1204	389	1200	390		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Zone reclaim mode enabled with:
echo 1 > /proc/sys/vm/zone_reclaim_mode
Intel Idle Driver disabled with the following Linux kernel parameter in /etc/grub.conf:
intel_idle.max_cstate=0



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x3650 M4 BD
(Intel Xeon E5-2620 v2, 2.10 GHz)

SPECfp_rate2006 = 386

SPECfp_rate_base2006 = 378

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Aug-2014
Hardware Availability: Jan-2014
Software Availability: Sep-2013

Platform Notes

BIOS setting:
Operating Mode set to Maximum Performance
Sysinfo program /home/SPECcpu-20140116-ic14.0/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on x3650M4BD Wed Aug 20 07:21:34 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2620 v2 @ 2.10GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores     : 6
  siblings      : 12
  physical 0:   cores 0 1 2 3 4 5
  physical 1:   cores 0 1 2 3 4 5
cache size     : 15360 KB
```

```
From /proc/meminfo
MemTotal:      264613204 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux x3650M4BD 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Aug 19 17:56
```

```
SPEC is set to: /home/SPECcpu-20140116-ic14.0
Filesystem      Type      Size Used Avail Use% Mounted on
/dev/mapper/vg_x3650m4bd-lv_home
  ext4          404G    67G  317G  18% /home
```

```
Additional information from dmidecode:
BIOS IBM      -[YOE103BUS-1.10]- 02/14/2014
Memory:
 16x Samsung M393B2G70QH0-CMA 16 GB 1600 MHz 2 rank
```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x3650 M4 BD
(Intel Xeon E5-2620 v2, 2.10 GHz)

SPECfp_rate2006 = 386

SPECfp_rate_base2006 = 378

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Aug-2014
Hardware Availability: Jan-2014
Software Availability: Sep-2013

Platform Notes (Continued)

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/SPECcpu-20140116-ic14.0/libs/32:/home/SPECcpu-20140116-ic14.0/libs/64:/home/SPECcpu-20140116-ic14.0/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x3650 M4 BD
(Intel Xeon E5-2620 v2, 2.10 GHz)

SPECfp_rate2006 = 386

SPECfp_rate_base2006 = 378

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Aug-2014
Hardware Availability: Jan-2014
Software Availability: Sep-2013

Base Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x3650 M4 BD
(Intel Xeon E5-2620 v2, 2.10 GHz)

SPECfp_rate2006 = 386

SPECfp_rate_base2006 = 378

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Aug-2014
Hardware Availability: Jan-2014
Software Availability: Sep-2013

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x3650 M4 BD
(Intel Xeon E5-2620 v2, 2.10 GHz)

SPECfp_rate2006 = 386

SPECfp_rate_base2006 = 378

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Aug-2014

Hardware Availability: Jan-2014

Software Availability: Sep-2013

Peak Optimization Flags (Continued)

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-B.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Sep 10 16:12:58 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 9 September 2014.