



# SPEC® CINT2006 Result

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## Huawei

**SPECint®2006 = 52.4**

### Huawei CH222 (Intel Xeon E5-2658 v2)

**SPECint\_base2006 = 48.0**

CPU2006 license: 3175

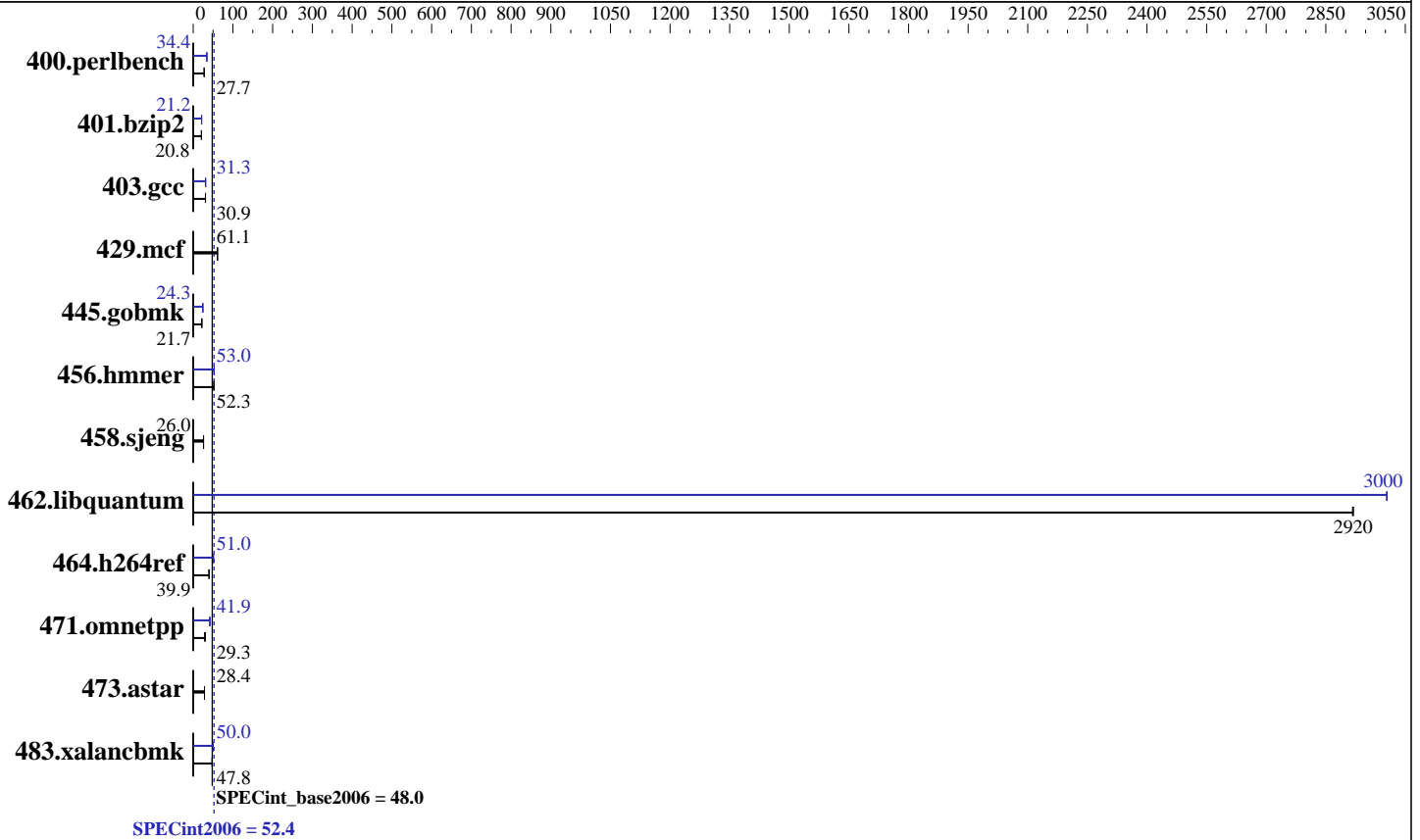
Test date: Jul-2014

Test sponsor: Huawei

Hardware Availability: Sep-2013

Tested by: Huawei

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2658 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 X 300 GB SAS 10000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	352	27.7	353	27.7	<b><u>352</u></b>	<b><u>27.7</u></b>	<b><u>284</u></b>	<b><u>34.4</u></b>	284	34.4	284	34.4
401.bzip2	465	20.8	<b><u>465</u></b>	<b><u>20.8</u></b>	465	20.7	456	21.2	<b><u>456</u></b>	<b><u>21.2</u></b>	455	21.2
403.gcc	<b><u>260</u></b>	<b><u>30.9</u></b>	261	30.9	260	30.9	258	31.2	<b><u>258</u></b>	<b><u>31.3</u></b>	257	31.3
429.mcf	150	60.7	149	61.4	<b><u>149</u></b>	<b><u>61.1</u></b>	150	60.7	149	61.4	<b><u>149</u></b>	<b><u>61.1</u></b>
445.gobmk	483	21.7	<b><u>484</u></b>	<b><u>21.7</u></b>	484	21.7	<b><u>432</u></b>	<b><u>24.3</u></b>	432	24.3	433	24.3
456.hammer	<b><u>179</u></b>	<b><u>52.3</u></b>	178	52.3	179	52.1	<b><u>176</u></b>	<b><u>53.0</u></b>	178	52.4	176	53.1
458.sjeng	<b><u>465</u></b>	<b><u>26.0</u></b>	465	26.0	465	26.0	<b><u>465</u></b>	<b><u>26.0</u></b>	465	26.0	465	26.0
462.libquantum	7.10	2920	<b><u>7.10</u></b>	<b><u>2920</u></b>	7.10	2920	6.90	3000	6.90	3000	<b><u>6.90</u></b>	<b><u>3000</u></b>
464.h264ref	555	39.9	<b><u>555</u></b>	<b><u>39.9</u></b>	556	39.8	<b><u>434</u></b>	<b><u>51.0</u></b>	434	51.0	434	51.0
471.omnetpp	215	29.0	<b><u>213</u></b>	<b><u>29.3</u></b>	203	30.8	149	41.8	<b><u>149</u></b>	<b><u>41.9</u></b>	149	42.0
473.astar	247	28.4	<b><u>247</u></b>	<b><u>28.4</u></b>	248	28.4	247	28.4	<b><u>247</u></b>	<b><u>28.4</u></b>	248	28.4
483.xalancbmk	<b><u>144</u></b>	<b><u>47.8</u></b>	144	47.8	144	47.8	138	50.0	<b><u>138</u></b>	<b><u>50.0</u></b>	138	50.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set HT to Disable  
Sysinfo program /spec/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on RHEL Thu Jul 17 22:24:56 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2658 v2 @ 2.40GHz  
2 "physical id"s (chips)  
20 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 10  
siblings : 10  
physical 0: cores 0 1 2 3 4 8 9 10 11 12  
physical 1: cores 0 1 2 3 4 8 9 10 11 12  
cache size : 25600 KB

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## Platform Notes (Continued)

From /proc/meminfo

MemTotal: 264478184 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d

Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/\*release\* /etc/\*version\*

redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:

Linux RHEL 2.6.32-431.el6.x86\_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86\_64  
x86\_64 x86\_64 GNU/Linux

run-level 3 Jul 17 10:15

SPEC is set to: /spec

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	ext4	265G	20G	232G	8%	/

Additional information from dmidecode:

Memory:

2x Hynix HMT42GR7AFR4C-RD 16 GB 1867 MHz 2 rank

8x Samsung M393B2G70DB0-CMA 16 GB 1867 MHz 2 rank

6x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,0,1"

LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64"

OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory  
using RHEL 6.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64  
400.perlbench: icc -m32  
445.gobmk: icc -m32

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## Peak Compiler Invocation (Continued)

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
 -opt-prefetch -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -ansi-alias

458.sjeng: basepeak = yes

462.libquantum: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch  
 -auto-p32

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## Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-ra-region-strategy=block -ansi-alias  
 -Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
 -Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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