



# SPEC® CINT2006 Result

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## HITACHI

**SPECint®2006 = 53.5**

Compute Blade 520X (Intel Xeon E7-4860 v2)

**SPECint\_base2006 = 49.7**

CPU2006 license: 35

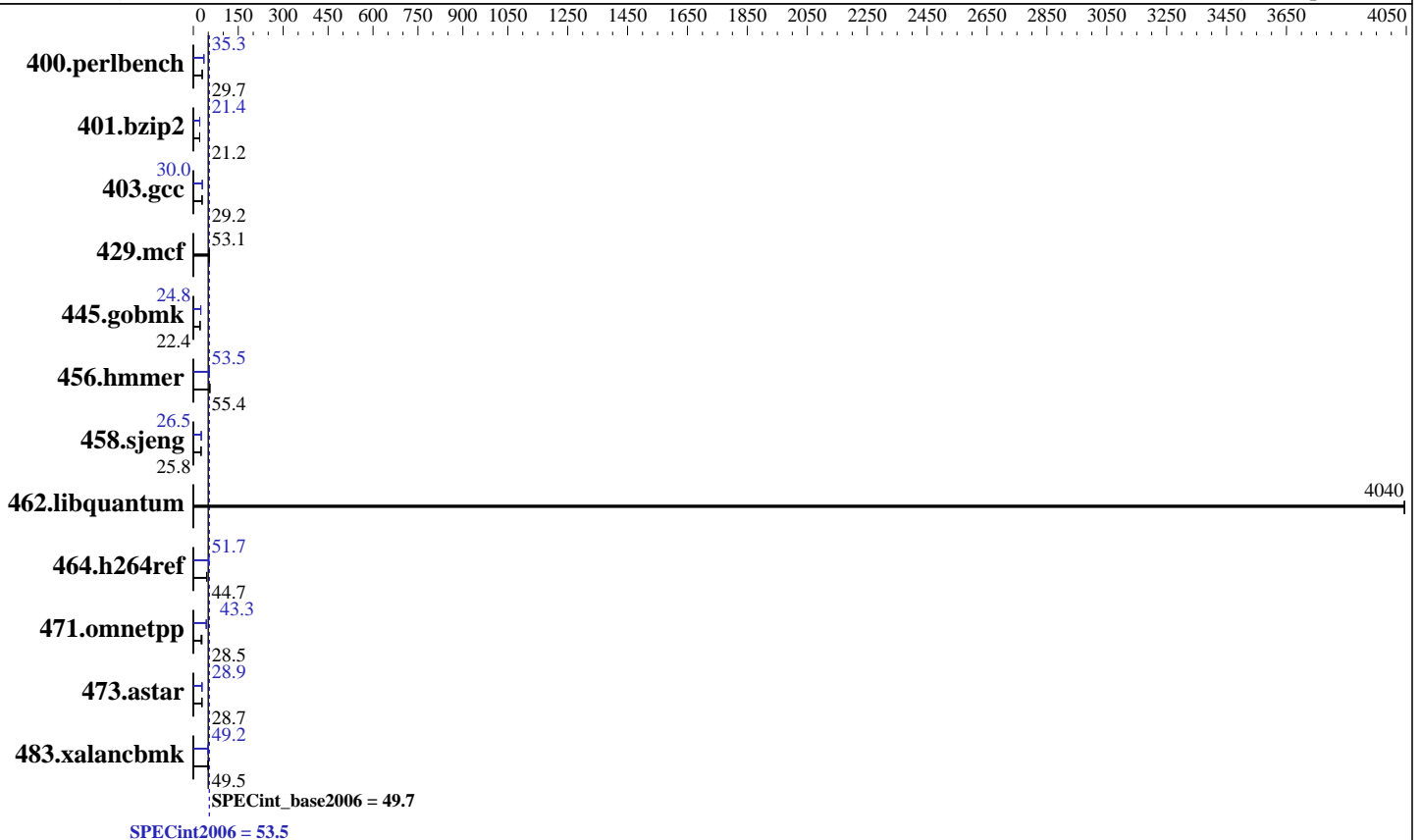
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Apr-2014

Hardware Availability: Apr-2014

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E7-4860 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 1 TB (64 x 16 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz)  
 Disk Subsystem: 2 x 300 GB SAS, 15000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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### Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	329	29.7	<b>329</b>	<b>29.7</b>	329	29.7	277	35.3	<b>277</b>	<b>35.3</b>	277	35.2
401.bzip2	<b>456</b>	<b>21.2</b>	456	21.2	456	21.2	450	21.4	<b>450</b>	<b>21.4</b>	450	21.4
403.gcc	<b>275</b>	<b>29.2</b>	275	29.2	276	29.2	<b>268</b>	<b>30.0</b>	268	30.0	268	30.0
429.mcf	172	52.9	<b>172</b>	<b>53.1</b>	172	53.2	172	52.9	<b>172</b>	<b>53.1</b>	172	53.2
445.gobmk	468	22.4	468	22.4	<b>468</b>	<b>22.4</b>	422	24.8	<b>422</b>	<b>24.8</b>	422	24.9
456.hammer	168	55.4	<b>168</b>	<b>55.4</b>	172	54.4	<b>174</b>	<b>53.5</b>	174	53.6	175	53.2
458.sjeng	<b>469</b>	<b>25.8</b>	469	25.8	469	25.8	457	26.5	456	26.6	<b>456</b>	<b>26.5</b>
462.libquantum	5.12	4040	5.12	4040	<b>5.12</b>	<b>4040</b>	5.12	4040	5.12	4040	<b>5.12</b>	<b>4040</b>
464.h264ref	496	44.6	494	44.8	<b>495</b>	<b>44.7</b>	430	51.5	428	51.7	<b>428</b>	<b>51.7</b>
471.omnetpp	243	25.7	<b>219</b>	<b>28.5</b>	219	28.6	<b>144</b>	<b>43.3</b>	144	43.4	144	43.3
473.astar	243	28.9	245	28.7	<b>244</b>	<b>28.7</b>	242	29.0	<b>243</b>	<b>28.9</b>	243	28.9
483.xalancbmk	139	49.6	139	49.5	<b>139</b>	<b>49.5</b>	140	49.2	<b>140</b>	<b>49.2</b>	140	49.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

Sysinfo program /home/cpu2006/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on RHEL6.5x8664 Fri Apr 11 20:21:12 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4860 v2 @ 2.60GHz
4 "physical id"s (chips)
96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 12
siblings : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
```

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### Platform Notes (Continued)

```
physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB
```

```
From /proc/meminfo
MemTotal:      1058309204 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux RHEL6.5x8664 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Apr 11 11:53
```

```
SPEC is set to: /home/cpu2006
Filesystem                Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_rhel6-lv_home ext4      221G   5.4G  205G   3% /home
```

Additional information from dmidecode:

```
BIOS HITACHI 06-02 04/04/2014
Memory:
32x NO DIMM Unknown
1x Samsung 393B2G7 BH0 YH9 16 GB 1333 MHz 2 rank
3x Samsung M393B2G7 BH0 YK0 16 GB 1333 MHz 2 rank
13x Samsung M393B2G70BH0-YK0 16 GB 1333 MHz 2 rank
47x Samsung M393B2G70QH0-YK0 16 GB 1333 MHz 2 rank
```

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "48"
```

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 445.gobmk: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 471.omnetpp: -DSPEC\_CPU\_LP64  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs  
-L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-ansi-alias`

401.bzip2: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch  
-ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias`

456.hmmer: `-xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias`

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## Peak Optimization Flags (Continued)

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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