SPEC® CINT2006 Result

Dell Inc.

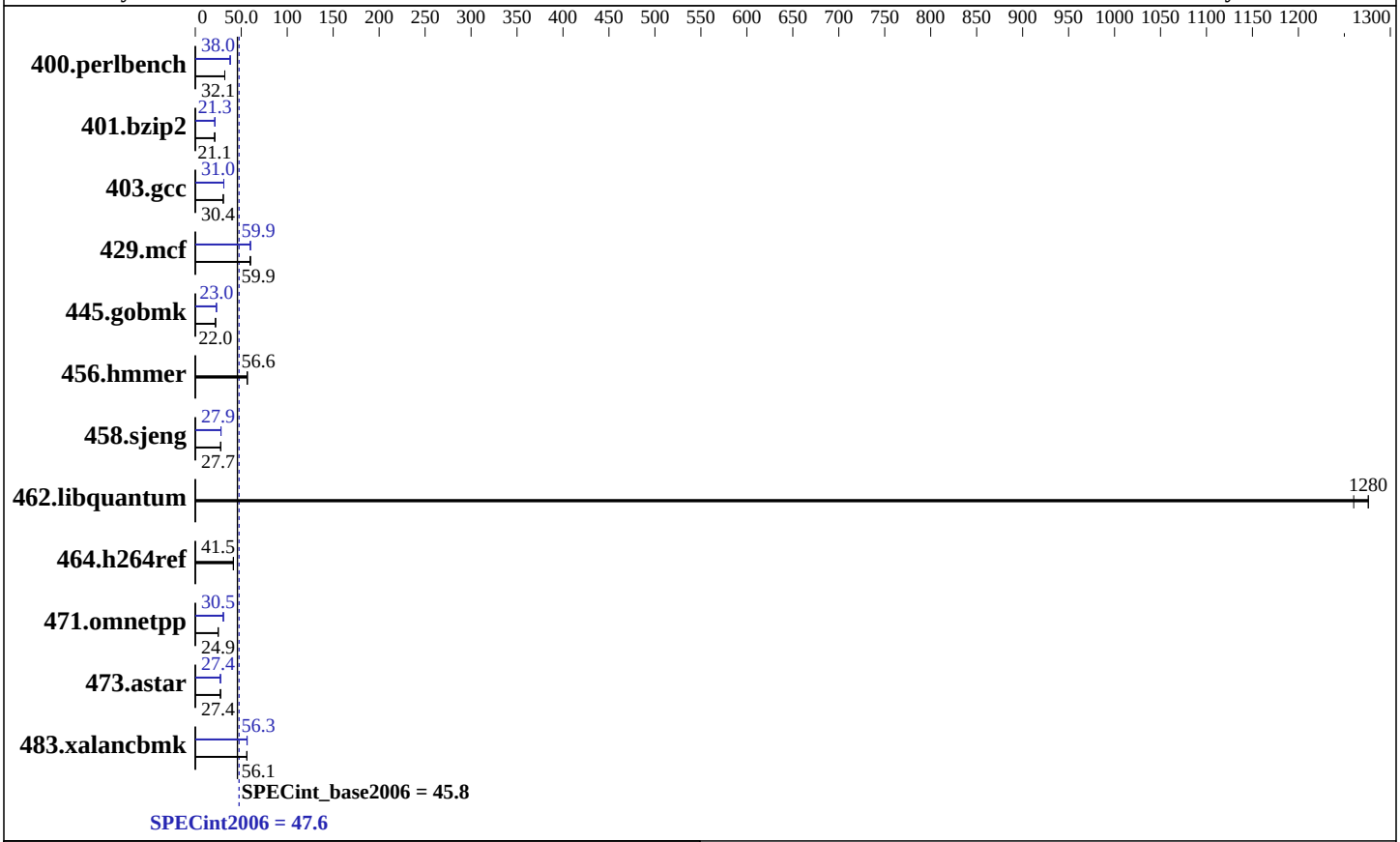
PowerEdge R220 (Intel Xeon E3-1230L v3, 1.80 GHz)

SPECint®2006 = 47.6

SPECint_base2006 = 45.8

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Mar-2014
Hardware Availability: Mar-2014
Software Availability: Feb-2014



Hardware

CPU Name:	Intel Xeon E3-1230L v3
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (4 x 8 GB 2Rx8 PC3L-12800E-11, ECC)
Disk Subsystem:	2 x 300 GB 10000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) 3.0.76-0.11-default
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	305	32.0	**305**	**32.1**	304	32.1	258	37.9	**257**	**38.0**	257	38.1
401.bzip2	**457**	**21.1**	457	21.1	457	21.1	**453**	**21.3**	453	21.3	453	21.3
403.gcc	264	30.4	**265**	**30.4**	265	30.3	**260**	**31.0**	259	31.0	260	31.0
429.mcf	153	59.5	**152**	**59.9**	152	60.2	**152**	**59.9**	152	60.0	152	59.9
445.gobmk	477	22.0	477	22.0	**477**	**22.0**	456	23.0	457	23.0	**457**	**23.0**
456.hmmer	165	56.6	165	56.4	**165**	**56.6**	165	56.6	165	56.4	**165**	**56.6**
458.sjeng	437	27.7	**438**	**27.7**	438	27.7	**433**	**27.9**	433	27.9	433	27.9
462.libquantum	16.4	1260	**16.2**	**1280**	16.2	1280	16.4	1260	**16.2**	**1280**	16.2	1280
464.h264ref	**534**	**41.5**	535	41.4	534	41.5	**534**	**41.5**	535	41.4	534	41.5
471.omnetpp	251	24.9	251	24.9	**251**	**24.9**	**205**	**30.5**	205	30.5	205	30.5
473.astar	**256**	**27.4**	256	27.4	257	27.4	256	27.4	257	27.3	**257**	**27.4**
483.xalancbmk	**123**	**56.1**	123	56.2	123	56.1	122	56.5	**123**	**56.3**	123	56.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Virtualization Technology disabled
Execute Disable disabled
System Profile set to Performance
Logical Processor disabled
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on linux Mon Mar 17 09:26:36 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E3-1230L v3 @ 1.80GHz
      1 "physical id"s (chips)
      4 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
```

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Platform Notes (Continued)

```
        cpu cores : 4
        siblings  : 4
        physical 0: cores 0 1 2 3
     cache size : 8192 KB

  From /proc/meminfo
     MemTotal:       32809896 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     SUSE Linux Enterprise Server 11 (x86_64)

  From /etc/*release* /etc/*version*
     SuSE-release:
        SUSE Linux Enterprise Server 11 (x86_64)
        VERSION = 11
        PATCHLEVEL = 3

  uname -a:
     Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
     x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Mar 17 09:25 last=S

  SPEC is set to: /root/cpu2006-1.2
     Filesystem     Type  Size  Used Avail Use% Mounted on
     /dev/sda2      ext2  542G  7.8G  533G   2% /

  Additional information from dmidecode:
    BIOS Dell Inc. 1.0.2 03/02/2014
    Memory:
     4x Samsung M391B1G73BH0-YK0 8 GB 1600 MHz

  (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
 OMP_NUM_THREADS = "4"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RedHat EL 6.4
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/transparent_hugepage/enabled
  runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc  -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m64
```

Continued on next page

Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-opt-prefetch -ansi-alias`

401.bzip2: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div -prof-use`(pass 2) `-auto-ilp32`
`-opt-prefetch -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc`
`-opt-malloc-options=3 -auto-ilp32`

429.mcf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel`
`-opt-prefetch -auto-p32`

445.gobmk: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2)
`-ansi-alias`

456.hmmer: `basepeak = yes`

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Peak Optimization Flags (Continued)

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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