



# SPEC® CINT2006 Result

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## IBM Corporation

IBM System x3500 M4  
(Intel Xeon E5-2609 v2, 2.50 GHz)

**SPECint®2006 = 39.4**

**SPECint\_base2006 = 38.0**

CPU2006 license: 11

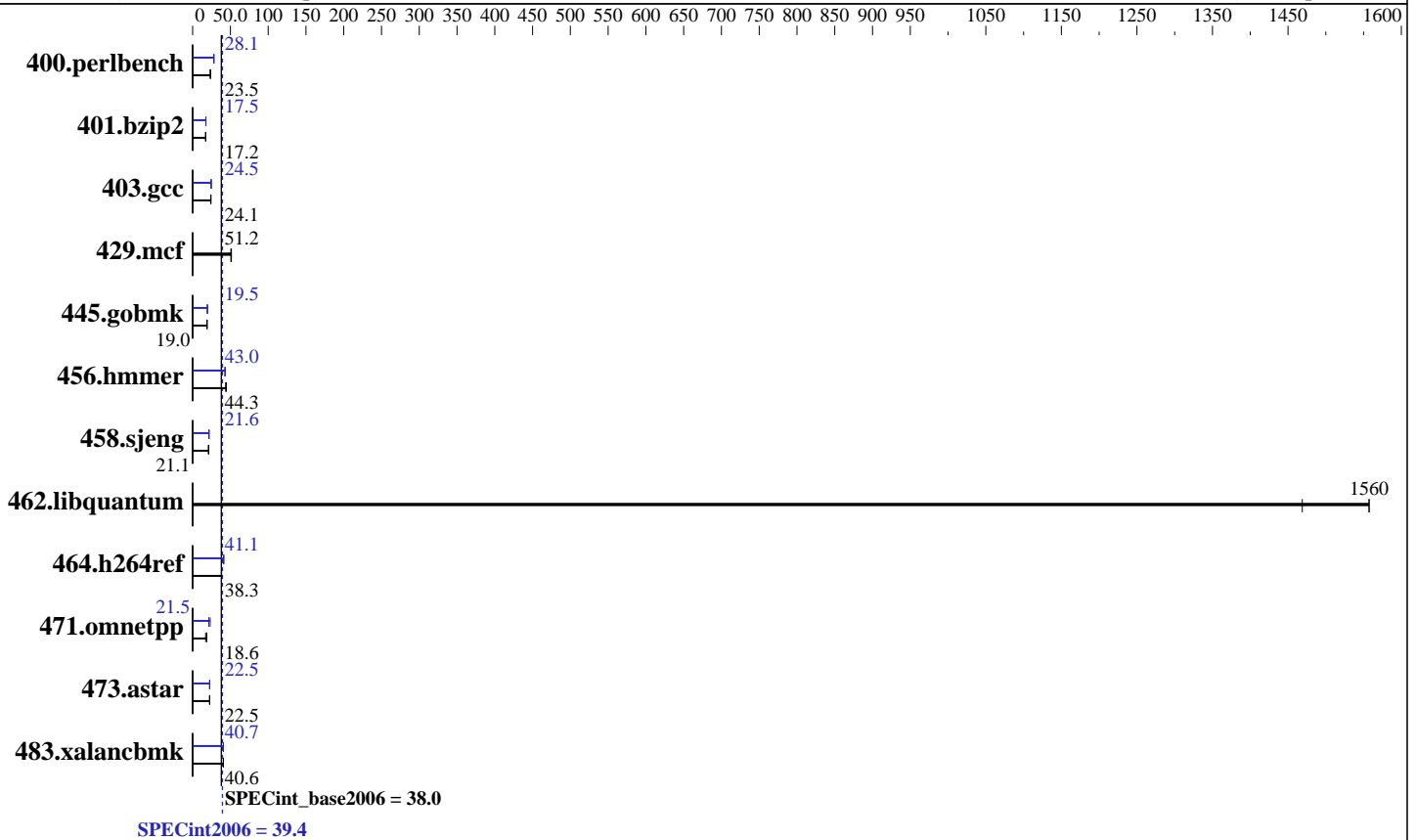
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Mar-2014

Hardware Availability: Dec-2013

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2609 v2  
 CPU Characteristics: 2500  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 10 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC, running at 1333 MHz)  
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	417	23.4	415	23.6	<b>416</b>	<b>23.5</b>	<b>348</b>	<b>28.1</b>	348	28.1	347	28.1
401.bzip2	560	17.2	<b>559</b>	<b>17.2</b>	558	17.3	<b>550</b>	<b>17.5</b>	550	17.5	550	17.5
403.gcc	<b>335</b>	<b>24.1</b>	333	24.2	335	24.0	329	24.4	<b>328</b>	<b>24.5</b>	328	24.6
429.mcf	<b>178</b>	<b>51.2</b>	180	50.6	178	51.2	<b>178</b>	<b>51.2</b>	180	50.6	178	51.2
445.gobmk	551	19.0	551	19.1	<b>551</b>	<b>19.0</b>	<b>537</b>	<b>19.5</b>	537	19.5	537	19.5
456.hammer	213	43.9	<b>211</b>	<b>44.3</b>	210	44.3	217	43.1	218	42.8	<b>217</b>	<b>43.0</b>
458.sjeng	<b>574</b>	<b>21.1</b>	574	21.1	574	21.1	560	21.6	<b>560</b>	<b>21.6</b>	559	21.6
462.libquantum	14.1	1470	13.3	1560	<b>13.3</b>	<b>1560</b>	14.1	1470	13.3	1560	<b>13.3</b>	<b>1560</b>
464.h264ref	579	38.2	<b>579</b>	<b>38.3</b>	578	38.3	538	41.1	<b>538</b>	<b>41.1</b>	539	41.1
471.omnetpp	<b>336</b>	<b>18.6</b>	335	18.6	364	17.2	268	23.3	<b>291</b>	<b>21.5</b>	292	21.4
473.astar	313	22.5	312	22.5	<b>312</b>	<b>22.5</b>	313	22.4	310	22.7	<b>312</b>	<b>22.5</b>
483.xalancbmk	170	40.6	169	40.8	<b>170</b>	<b>40.6</b>	<b>169</b>	<b>40.7</b>	170	40.7	169	40.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Zone reclaim mode enabled with:  
echo 1 > /proc/sys/vm/zone\_reclaim\_mode

## Platform Notes

BIOS setting:  
Operating Mode set to Maximum Performance  
Sysinfo program /home/SPECcpu-new/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on x3500M4 Wed Mar 12 00:24:32 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2609 v2 @ 2.50GHz  
2 "physical id"s (chips)  
8 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

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## Platform Notes (Continued)

```
cpu cores : 4
siblings  : 4
physical 0: cores 0 1 2 3
physical 1: cores 0 1 2 3
cache size : 10240 KB
```

```
From /proc/meminfo
MemTotal:      264657388 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux x3500M4 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 10 14:32
```

```
SPEC is set to: /home/SPECcpu-new
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/vg_intelcrb-lv_home
ext4 863G 35G 784G 5% /home
```

```
Additional information from dmidecode:
BIOS IBM -[TKE133FUS-1.50]- 07/26/2013
Memory:
8x Not Specified Not Specified
16x Samsung M393B2G70QH0-CMA 16 GB 1333 MHz 2 rank
```

(End of data from sysinfo program)  
"Not Specified" memory information from dmidecode indicates unused DIMM slots.

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/SPECcpu-new/libs/32:/home/SPECcpu-new/libs/64:/home/SPECcpu-new/sh"  
OMP\_NUM\_THREADS = "8"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>



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## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
  
C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs  
-L/sh -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-ansi-alias`

401.bzip2: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch  
-ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias`

456.hmmer: `-xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias`

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## Peak Optimization Flags (Continued)

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 22:58:27 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 16 April 2014.