



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®2006 = **55.2**

ProLiant DL360e Gen8
(2.40 GHz, Intel Xeon E5-2470 v2)

SPECint_base2006 = **51.0**

CPU2006 license: 3

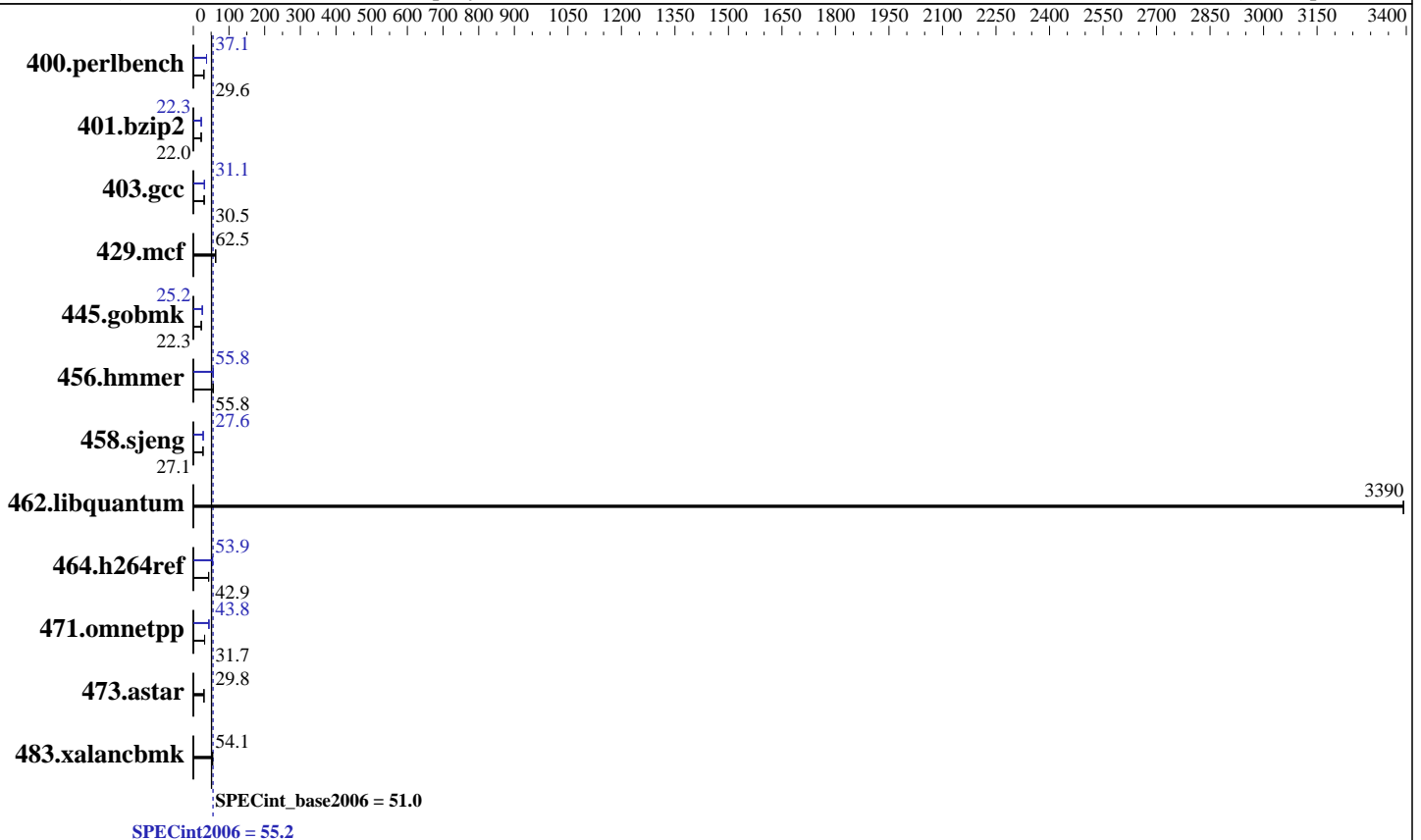
Test date: Dec-2013

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2470 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 300 GB 15 K SAS, RAID 1
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP3
 Kernel 3.0.76-0.11-default
 Compiler: C/C++; Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	331	29.5	329	29.7	<u>330</u>	<u>29.6</u>	<u>263</u>	<u>37.1</u>	263	37.1	263	37.1
401.bzip2	438	22.0	438	22.0	<u>438</u>	<u>22.0</u>	434	22.3	434	22.3	<u>434</u>	<u>22.3</u>
403.gcc	<u>264</u>	<u>30.5</u>	263	30.6	266	30.3	259	31.1	<u>259</u>	<u>31.1</u>	259	31.1
429.mcf	146	62.4	146	62.6	<u>146</u>	<u>62.5</u>	146	62.4	146	62.6	<u>146</u>	<u>62.5</u>
445.gobmk	470	22.3	<u>470</u>	<u>22.3</u>	471	22.3	416	25.2	416	25.2	<u>416</u>	<u>25.2</u>
456.hammer	167	55.8	168	55.5	<u>167</u>	<u>55.8</u>	<u>167</u>	<u>55.8</u>	167	55.8	167	55.7
458.sjeng	446	27.1	<u>446</u>	<u>27.1</u>	447	27.1	438	27.6	438	27.6	<u>438</u>	<u>27.6</u>
462.libquantum	6.11	3390	6.11	3390	<u>6.11</u>	<u>3390</u>	6.11	3390	6.11	3390	<u>6.11</u>	<u>3390</u>
464.h264ref	514	43.1	517	42.8	<u>516</u>	<u>42.9</u>	410	53.9	411	53.9	<u>410</u>	<u>53.9</u>
471.omnetpp	198	31.6	197	31.8	<u>197</u>	<u>31.7</u>	145	43.2	142	44.0	<u>143</u>	<u>43.8</u>
473.astar	235	29.8	<u>236</u>	<u>29.8</u>	236	29.7	235	29.8	<u>236</u>	<u>29.8</u>	236	29.7
483.xalancbmk	127	54.3	130	53.0	<u>127</u>	<u>54.1</u>	127	54.3	130	53.0	<u>127</u>	<u>54.1</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches

Platform Notes

BIOS Configuration:
Intel Hyperthreading Options was set to Disabled
HP Power Profile set to Maximum Performance
System Locality Information Table Gen8 was set to Enabled
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate was set to 1x
Sysinfo program /home/cpu2006/config/sysinfo.rev6819
\$Rev: 6818 \$ \$Date:: 2012-07-17 # \$ 6e76384576c25d0a81352b1f6b6d4262
running on dl360egen8rwen Sun Dec 15 16:19:56 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2470 v2 @ 2.40GHz
2 "physical id"s (chips)
20 "processors"

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Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 10
siblings  : 10
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

```
From /proc/meminfo
MemTotal:      98903452 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

```
uname -a:
Linux dl360egen8rwen 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
(ccab990) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 15 16:15 last=S
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext3  549G  8.6G  539G   2% /
```

```
Additional information from dmidecode:
BIOS HP P73 11/12/2013
Memory:
12x HP 689911-071 8 GB 1600 MHz
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "20"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

471.omnetpp: `icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias`

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Peak Optimization Flags (Continued)

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.xml>

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