



# SPEC® CFP2006 Result

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## Huawei

SPECfp®2006 = 76.8

Huawei RH2288H V2 (Intel Xeon E5-2640)

SPECfp\_base2006 = 73.7

CPU2006 license: 3175

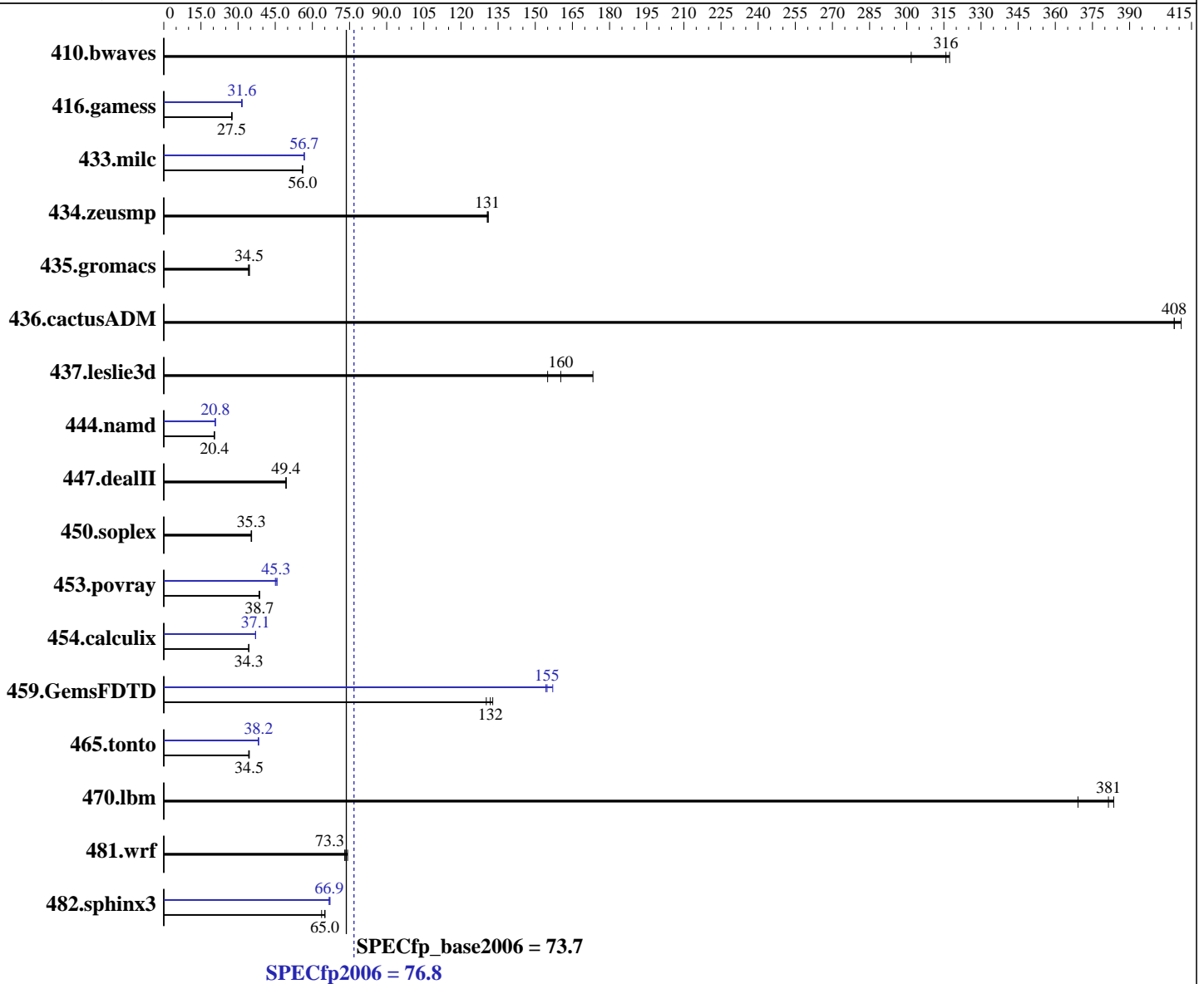
Test date: May-2012

Test sponsor: Huawei

Hardware Availability: Mar-2012

Tested by: Huawei

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E5-2640  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
 Disk Subsystem: 1 x 300 GB SAS, 10K RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	45.0	302	42.8	317	<b>43.0</b>	<b>316</b>	45.0	302	42.8	317	<b>43.0</b>	<b>316</b>
416.gamess	715	27.4	<b>713</b>	<b>27.5</b>	710	27.6	620	31.6	624	31.4	<b>620</b>	<b>31.6</b>
433.milc	164	56.0	<b>164</b>	<b>56.0</b>	164	56.0	<b>162</b>	<b>56.7</b>	162	56.7	162	56.7
434.zeusmp	69.4	131	69.6	131	<b>69.6</b>	<b>131</b>	69.4	131	69.6	131	<b>69.6</b>	<b>131</b>
435.gromacs	207	34.6	<b>207</b>	<b>34.5</b>	209	34.2	207	34.6	<b>207</b>	<b>34.5</b>	209	34.2
436.cactusADM	<b>29.3</b>	<b>408</b>	29.1	411	29.3	408	<b>29.3</b>	<b>408</b>	29.1	411	29.3	408
437.leslie3d	54.2	173	60.6	155	<b>58.6</b>	<b>160</b>	54.2	173	60.6	155	<b>58.6</b>	<b>160</b>
444.namd	<b>392</b>	<b>20.4</b>	393	20.4	392	20.4	385	20.8	386	20.8	<b>386</b>	<b>20.8</b>
447.dealII	232	49.3	231	49.4	<b>232</b>	<b>49.4</b>	232	49.3	231	49.4	<b>232</b>	<b>49.4</b>
450.soplex	235	35.5	236	35.3	<b>236</b>	<b>35.3</b>	235	35.5	236	35.3	<b>236</b>	<b>35.3</b>
453.povray	138	38.4	<b>138</b>	<b>38.7</b>	137	38.7	116	45.8	118	45.1	<b>117</b>	<b>45.3</b>
454.calculix	<b>240</b>	<b>34.3</b>	240	34.4	241	34.3	223	37.1	<b>223</b>	<b>37.1</b>	223	37.1
459.GemsFDTD	79.9	133	81.5	130	<b>80.5</b>	<b>132</b>	<b>68.6</b>	<b>155</b>	67.6	157	68.8	154
465.tonto	287	34.3	285	34.5	<b>285</b>	<b>34.5</b>	<b>257</b>	<b>38.2</b>	258	38.1	257	38.3
470.lbm	35.8	384	<b>36.0</b>	<b>381</b>	37.2	369	35.8	384	<b>36.0</b>	<b>381</b>	37.2	369
481.wrf	153	73.1	<b>152</b>	<b>73.3</b>	150	74.2	153	73.1	<b>152</b>	<b>73.3</b>	150	74.2
482.sphinx3	299	65.1	306	63.7	<b>300</b>	<b>65.0</b>	<b>292</b>	<b>66.9</b>	290	67.1	292	66.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
 Transparent Huge Pages enabled with:  
 echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
 Select only test related files when installing the operating system

## Platform Notes

BIOS configuration:  
 Intel Hyper-Threading set to Disabled  
 Set Power Efficiency Mode to Performance  
 Sysinfo program /spec/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on RH2288-rebuild Fri May 18 08:59:05 2012

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E5-2640 0 @ 2.50GHz
 2 "physical id"s (chips)
 12 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores      : 6
siblings       : 6
physical 0:    : cores 0 1 2 3 4 5
physical 1:    : cores 0 1 2 3 4 5
cache size     : 15360 KB
```

From /proc/meminfo

```
MemTotal:      132124016 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux RH2288-rebuild 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 May 18 02:17

SPEC is set to: /spec

```
Filesystem      Type      Size Used Avail Use% Mounted on
/dev/sdal        ext4      289G  24G  251G   9% /
```

Additional information from dmidecode:

```
Memory:
16x Samsung M393B1K70DH0-CK0 8 GB 1600 MHz 2 rank
```

## General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "12"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

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## General Notes (Continued)

The Huawei RH2288H V2 and the Huawei RH2288 V2 models are electronically equivalent. The results have been measured on a Huawei RH2288 V2 model

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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## Base Optimization Flags (Continued)

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel`

C++ benchmarks:

444.namd: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32`

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## Peak Optimization Flags (Continued)

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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