



# SPEC® CINT2006 Result

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## Cisco Systems

Cisco UCS C220 M3 (Intel Xeon E5-2690 v2, 3.00 GHz)

**SPECint®2006 = 58.1**

**SPECint\_base2006 = 54.7**

**CPU2006 license:** 9019

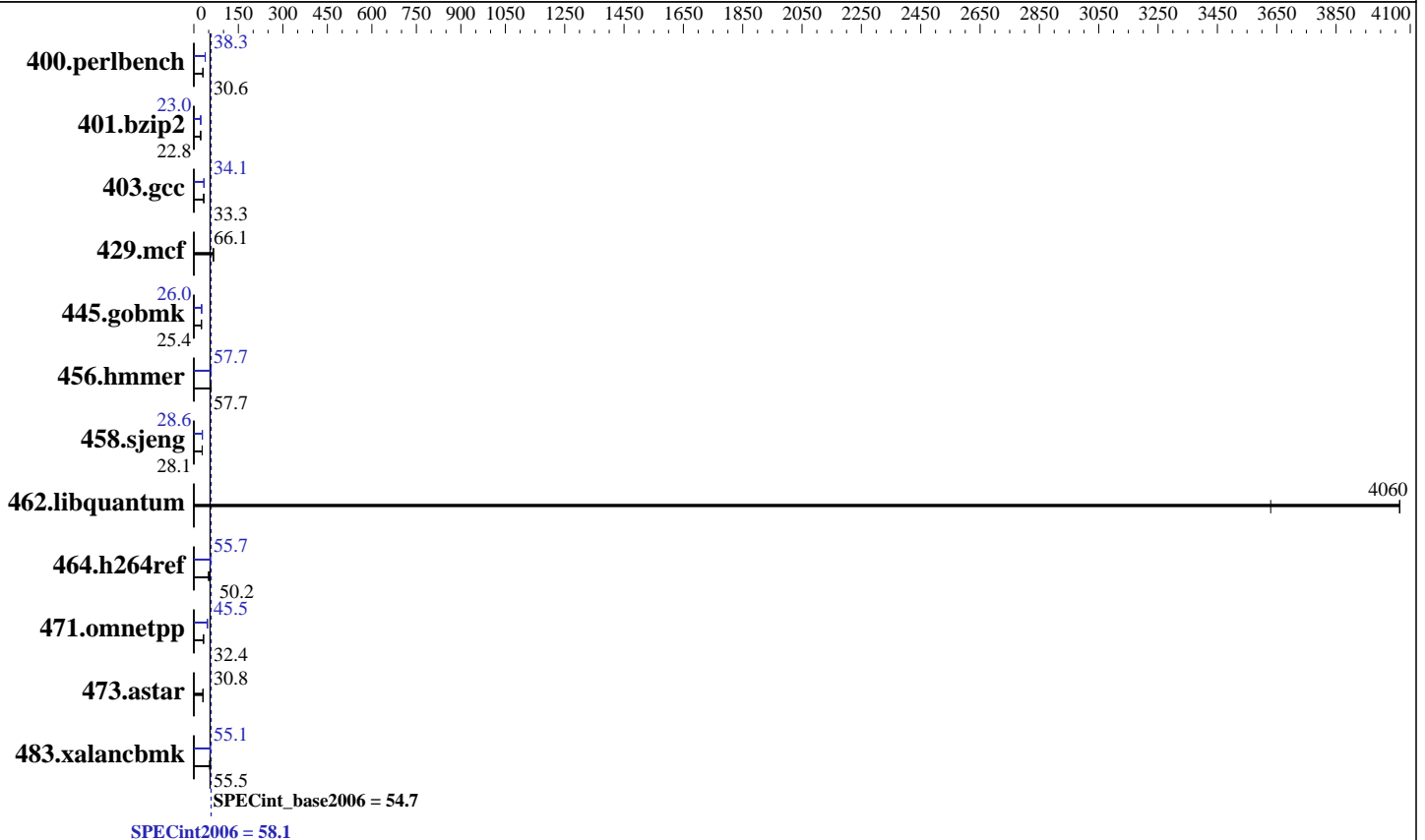
**Test sponsor:** Cisco Systems

**Tested by:** Cisco Systems

**Test date:** Nov-2013

**Hardware Availability:** Sep-2013

**Software Availability:** Sep-2013



### Hardware

**CPU Name:** Intel Xeon E5-2690 v2  
**CPU Characteristics:** Intel Turbo Boost Technology up to 3.60 GHz  
**CPU MHz:** 3000  
**FPU:** Integrated  
**CPU(s) enabled:** 20 cores, 2 chips, 10 cores/chip  
**CPU(s) orderable:** 1,2 chip  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 256 KB I+D on chip per core  
**L3 Cache:** 25 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 128 GB (16 x 8 GB 2Rx4 PC3-14900R-13, ECC)  
**Disk Subsystem:** 1 X 300 GB 15000 RPM SAS  
**Other Hardware:** None

### Software

**Operating System:** Red Hat Enterprise Linux Server release 6.4 (Santiago)  
2.6.32-358.el6.x86\_64  
**Compiler:** C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
**Auto Parallel:** Yes  
**File System:** ext4  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32/64-bit  
**Peak Pointers:** 32/64-bit  
**Other Software:** Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>319</u></b>	<b><u>30.6</u></b>	319	30.6	319	30.6	255	38.3	255	38.3	<b><u>255</u></b>	<b><u>38.3</u></b>
401.bzip2	422	22.9	<b><u>424</u></b>	<b><u>22.8</u></b>	424	22.8	418	23.1	<b><u>419</u></b>	<b><u>23.0</u></b>	419	23.0
403.gcc	244	33.0	241	33.4	<b><u>241</u></b>	<b><u>33.3</u></b>	<b><u>236</u></b>	<b><u>34.1</u></b>	236	34.0	236	34.1
429.mcf	138	66.2	140	65.4	<b><u>138</u></b>	<b><u>66.1</u></b>	138	66.2	140	65.4	<b><u>138</u></b>	<b><u>66.1</u></b>
445.gobmk	413	25.4	410	25.6	<b><u>413</u></b>	<b><u>25.4</u></b>	403	26.0	403	26.0	<b><u>403</u></b>	<b><u>26.0</u></b>
456.hammer	162	57.5	162	57.8	<b><u>162</u></b>	<b><u>57.7</u></b>	162	57.8	162	57.7	<b><u>162</u></b>	<b><u>57.7</u></b>
458.sjeng	<b><u>431</u></b>	<b><u>28.1</u></b>	431	28.1	431	28.1	423	28.6	424	28.5	<b><u>423</u></b>	<b><u>28.6</u></b>
462.libquantum	5.71	3630	5.10	4070	<b><u>5.10</u></b>	<b><u>4060</u></b>	5.71	3630	5.10	4070	<b><u>5.10</u></b>	<b><u>4060</u></b>
464.h264ref	447	49.5	<b><u>441</u></b>	<b><u>50.2</u></b>	436	50.8	398	55.7	<b><u>398</u></b>	<b><u>55.7</u></b>	398	55.6
471.omnetpp	195	32.0	190	32.9	<b><u>193</u></b>	<b><u>32.4</u></b>	<b><u>137</u></b>	<b><u>45.5</u></b>	137	45.7	138	45.3
473.astar	<b><u>228</u></b>	<b><u>30.8</u></b>	228	30.7	227	31.0	<b><u>228</u></b>	<b><u>30.8</u></b>	228	30.7	227	31.0
483.xalancbmk	124	55.6	130	52.9	<b><u>124</u></b>	<b><u>55.5</u></b>	124	55.6	125	55.0	<b><u>125</u></b>	<b><u>55.1</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

### BIOS Settings:

```

Intel HT Technology = Disabled
CPU performance set to HPC
Power Technology set to Custom
CPU Power State C6 set to Enabled
CPU Power State C1 Enhanced set to Disabled
Energy Performance policy set to Performance
Memory RAS configuration set to Maximum Performance
DRAM Clock Throttling Set to Performance
LV DDR Mode set to Performance-mode
DRAM Refresh Rate Set to 1x
Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on SL1-IVB Sun Nov 10 02:53:52 2013

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2690 v2 @ 3.00GHz
2 "physical id"s (chips)
20 "processors"

```

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### Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 10
siblings  : 10
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

From /proc/meminfo

```
MemTotal:      132125860 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

/usr/bin/lsb\_release -d

```
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux SL1-IVB 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 9 21:41

SPEC is set to: /opt/cpu2006-1.2

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sdb1        ext4      275G  270G   0 100% /
```

Additional information from dmidecode:

```
BIOS Cisco Systems, Inc. C220M3.1.5.2.27.071120132232 07/11/2013
Memory:
16x 0xAD00 HMT31GR7EFR4C-RD 8 GB 1866 MHz 2 rank
```

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh"
OMP_NUM_THREADS = "20"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```



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## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias

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## Peak Optimization Flags (Continued)

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2.20130717.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2.20130717.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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