



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint®2006 = **56.8**

PRIMERGY RX200 S8, Intel Xeon E5-2650 v2, 2.60 GHz

SPECint_base2006 = **52.6**

CPU2006 license: 19

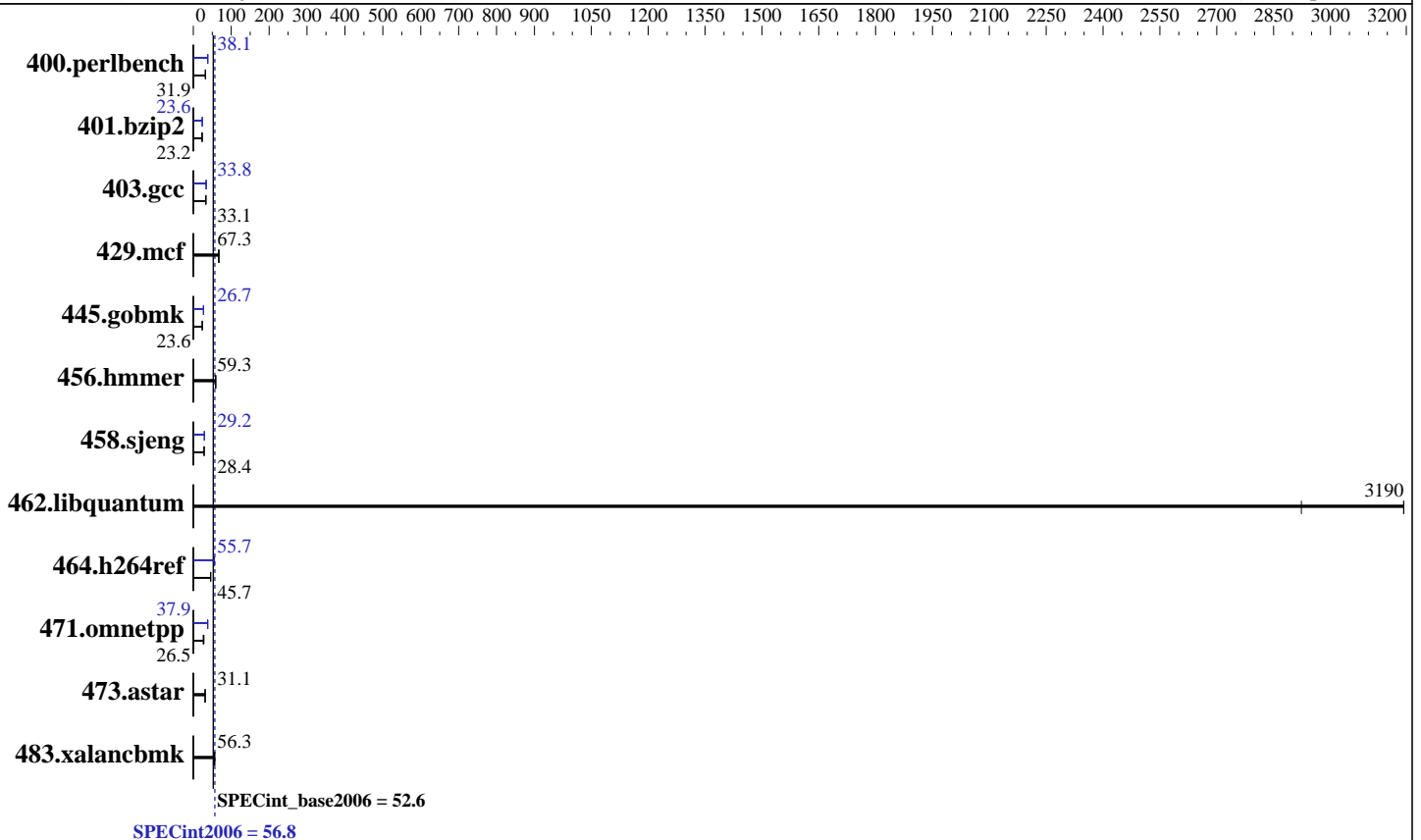
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2013

Hardware Availability: Oct-2013

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2650 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)
 Disk Subsystem: 1 x SATA, 1000 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 2.6.32-358.11.1.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = **56.8**

PRIMERGY RX200 S8, Intel Xeon E5-2650 v2, 2.60 GHz

SPECint_base2006 = **52.6**

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Sep-2013
Hardware Availability: Oct-2013
Software Availability: Sep-2013

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	306	31.9	307	31.8	306	31.9	257	38.1	257	38.0	256	38.1
401.bzip2	416	23.2	416	23.2	416	23.2	409	23.6	409	23.6	410	23.6
403.gcc	243	33.1	243	33.1	243	33.1	238	33.8	238	33.9	238	33.8
429.mcf	136	66.9	135	67.3	135	67.6	136	66.9	135	67.3	135	67.6
445.gobmk	443	23.7	444	23.6	444	23.6	393	26.7	392	26.7	392	26.7
456.hammer	157	59.3	157	59.3	160	58.2	157	59.3	157	59.3	160	58.2
458.sjeng	426	28.4	426	28.4	426	28.4	415	29.2	414	29.2	414	29.2
462.libquantum	7.09	2920	6.49	3190	6.49	3190	7.09	2920	6.49	3190	6.49	3190
464.h264ref	483	45.8	485	45.6	485	45.7	397	55.8	397	55.7	397	55.7
471.omnetpp	236	26.5	236	26.5	223	28.0	165	37.9	165	37.9	165	38.0
473.astar	227	31.0	225	31.1	225	31.2	227	31.0	225	31.1	225	31.2
483.xalancbmk	123	56.3	123	56.0	122	56.4	123	56.3	123	56.0	122	56.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Energy Performance = Performance
Utilization Profile = Unbalanced

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"
OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 56.8

PRIMERGY RX200 S8, Intel Xeon E5-2650 v2, 2.60 GHz

SPECint_base2006 = 52.6

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2013

Hardware Availability: Oct-2013

Software Availability: Sep-2013

General Notes (Continued)

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
-L/sh -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 56.8

PRIMERGY RX200 S8, Intel Xeon E5-2650 v2, 2.60 GHz

SPECint_base2006 = 52.6

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Sep-2013
Hardware Availability: Oct-2013
Software Availability: Sep-2013

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
-ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 56.8

PRIMERGY RX200 S8, Intel Xeon E5-2650 v2, 2.60 GHz

SPECint_base2006 = 52.6

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2013

Hardware Availability: Oct-2013

Software Availability: Sep-2013

Peak Optimization Flags (Continued)

456.hmmr: basepeak = yes

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 17:08:44 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 9 October 2013.