SPEC® CFP2006 Result
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Supermicro

SuperServer 7046A-HR+F (X8DAH+-F, Intel Xeon X5698)

$SPECfp^{®}2006 =$ 65.8
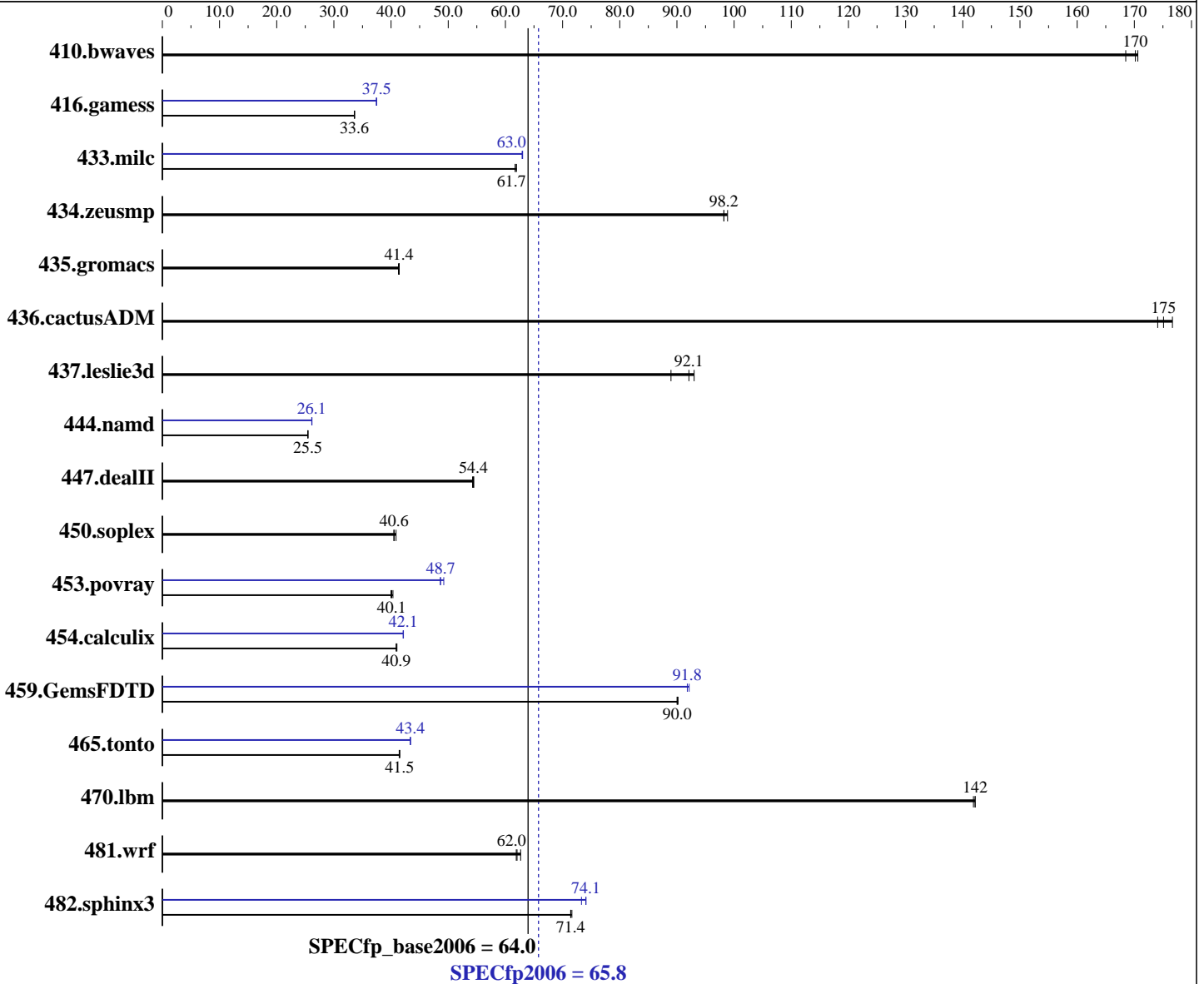
$SPECfp\_base2006 =$ 64.0

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Sep-2012
Hardware Availability: Mar-2011
Software Availability: Jun-2012

Benchmark	Value
410.bwaves	170
416.gamess	37.5 / 33.6
433.milc	63.0 / 61.7
434.zeusmp	98.2
435.gromacs	41.4
436.cactusADM	175
437.leslie3d	92.1
444.namd	26.1 / 25.5
447.dealII	54.4
450.soplex	40.6
453.povray	48.7 / 40.1
454.calculix	42.1 / 40.9
459.GemsFDTD	91.8 / 90.0
465.tonto	43.4 / 41.5
470.lbm	142
481.wrf	62.0
482.sphinx3	74.1 / 71.4

SPECfp_base2006 = 64.0
SPECfp2006 = 65.8

Hardware
CPU Name: Intel Xeon X5698
CPU Characteristics:
CPU MHz: 4400
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
Continued on next page

Software
Operating System: Red Hat Enterprise Linux Server Release 6.3, Kernel 2.6.32-279.el6.x86_64
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: ext4
System State: Run level 3 (multi-user)
Continued on next page

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L3 Cache:	12 MB I+D on chip per chip	Base Pointers:	64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)	Other Software:	None
Disk Subsystem:	1 x 2048 GB SATA II, 7200 RPM		
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	79.7	171	**79.9**	**170**	80.7	168	79.7	171	**79.9**	**170**	80.7	168
416.gamess	**583**	**33.6**	582	33.6	583	33.6	523	37.4	523	37.5	**523**	**37.5**
433.milc	149	61.7	**149**	**61.7**	148	61.9	146	62.9	146	63.0	**146**	**63.0**
434.zeusmp	**92.7**	**98.2**	92.7	98.2	92.1	98.9	**92.7**	**98.2**	92.7	98.2	92.1	98.9
435.gromacs	173	41.3	**172**	**41.4**	172	41.4	173	41.3	**172**	**41.4**	172	41.4
436.cactusADM	**68.2**	**175**	68.6	174	67.6	177	**68.2**	**175**	68.6	174	67.6	177
437.leslie3d	101	93.0	106	89.0	**102**	**92.1**	101	93.0	106	89.0	**102**	**92.1**
444.namd	**315**	**25.5**	315	25.5	315	25.4	**307**	**26.1**	306	26.2	307	26.1
447.dealII	210	54.5	**210**	**54.4**	211	54.3	210	54.5	**210**	**54.4**	211	54.3
450.soplex	206	40.5	204	40.9	**206**	**40.6**	206	40.5	204	40.9	**206**	**40.6**
453.povray	133	40.0	**133**	**40.1**	132	40.3	109	48.6	**109**	**48.7**	108	49.2
454.calculix	**202**	**40.9**	202	40.9	201	41.0	**196**	**42.1**	196	42.2	196	42.1
459.GemsFDTD	118	90.0	118	90.2	**118**	**90.0**	**116**	**91.8**	116	91.8	115	92.1
465.tonto	237	41.5	238	41.4	**237**	**41.5**	**227**	**43.4**	227	43.4	227	43.4
470.lbm	96.9	142	**96.7**	**142**	96.7	142	96.9	142	**96.7**	**142**	96.7	142
481.wrf	180	61.9	**180**	**62.0**	178	62.7	180	61.9	**180**	**62.0**	178	62.7
482.sphinx3	273	71.4	272	71.6	**273**	**71.4**	**263**	**74.1**	263	74.1	266	73.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

    Sysinfo program /usr/cpu2006/Docs/sysinfo
    $Rev: 6775 $ $Date:: 2011-08-16 #$ 8787f7622badcf24e01c368b1db4377c
    running on localhost.localdomain Sat Sep 8 15:40:28 2012

    This section contains SUT (System Under Test) info as seen by
    some common utilities. To remove or add to this section, see:
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo

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Platform Notes (Continued)

```
    model name : Intel(R) Xeon(R) CPU X5698 @ 4.40GHz
       2 "physical id"s (chips)
       8 "processors"
    cores, siblings (Caution: counting these is hw and system dependent.  The
    following excerpts from /proc/cpuinfo might not be reliable.  Use with
    caution.)
       cpu cores : 2
       siblings  : 4
       physical 0: cores 0 10
       physical 1: cores 0 10
    cache size : 12288 KB

  From /proc/meminfo
    MemTotal:       99261184 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.3 (Santiago)

  From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
    Linux localhost.localdomain 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36
    EDT 2012 x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Sep 7 15:31

  SPEC is set to: /usr/cpu2006
    Filesystem      Type    Size  Used Avail Use% Mounted on
    /dev/sda4       ext4    1.4T   30G  1.3T   3% /

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"
OMP_NUM_THREADS = "4"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

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Base Compiler Invocation

C benchmarks:
```
icc    -m64
```

C++ benchmarks:
```
icpc   -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Base Portability Flags

```
      410.bwaves: -DSPEC_CPU_LP64
      416.gamess: -DSPEC_CPU_LP64
        433.milc: -DSPEC_CPU_LP64
      434.zeusmp: -DSPEC_CPU_LP64
     435.gromacs: -DSPEC_CPU_LP64 -nofor_main
    436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
     437.leslie3d: -DSPEC_CPU_LP64
       444.namd: -DSPEC_CPU_LP64
       447.dealII: -DSPEC_CPU_LP64
      450.soplex: -DSPEC_CPU_LP64
      453.povray: -DSPEC_CPU_LP64
     454.calculix: -DSPEC_CPU_LP64 -nofor_main
    459.GemsFDTD: -DSPEC_CPU_LP64
       465.tonto: -DSPEC_CPU_LP64
        470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
     482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

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Tested by:	Supermicro	**Software Availability:**	Jun-2012

Peak Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32`
`-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias`
`-parallel`

C++ benchmarks:

444.namd: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias`
`-auto-ilp32`

447.dealII: `basepeak = yes`

450.soplex: `basepeak = yes`

453.povray: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

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Peak Optimization Flags (Continued)

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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