



# SPEC® CFP2006 Result

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## Hewlett-Packard Company

**SPECfp®2006 = 70.9**

ProLiant DL380p Gen8  
(2.30 GHz, Intel Xeon E5-2630)

**SPECfp\_base2006 = 67.4**

CPU2006 license: 3

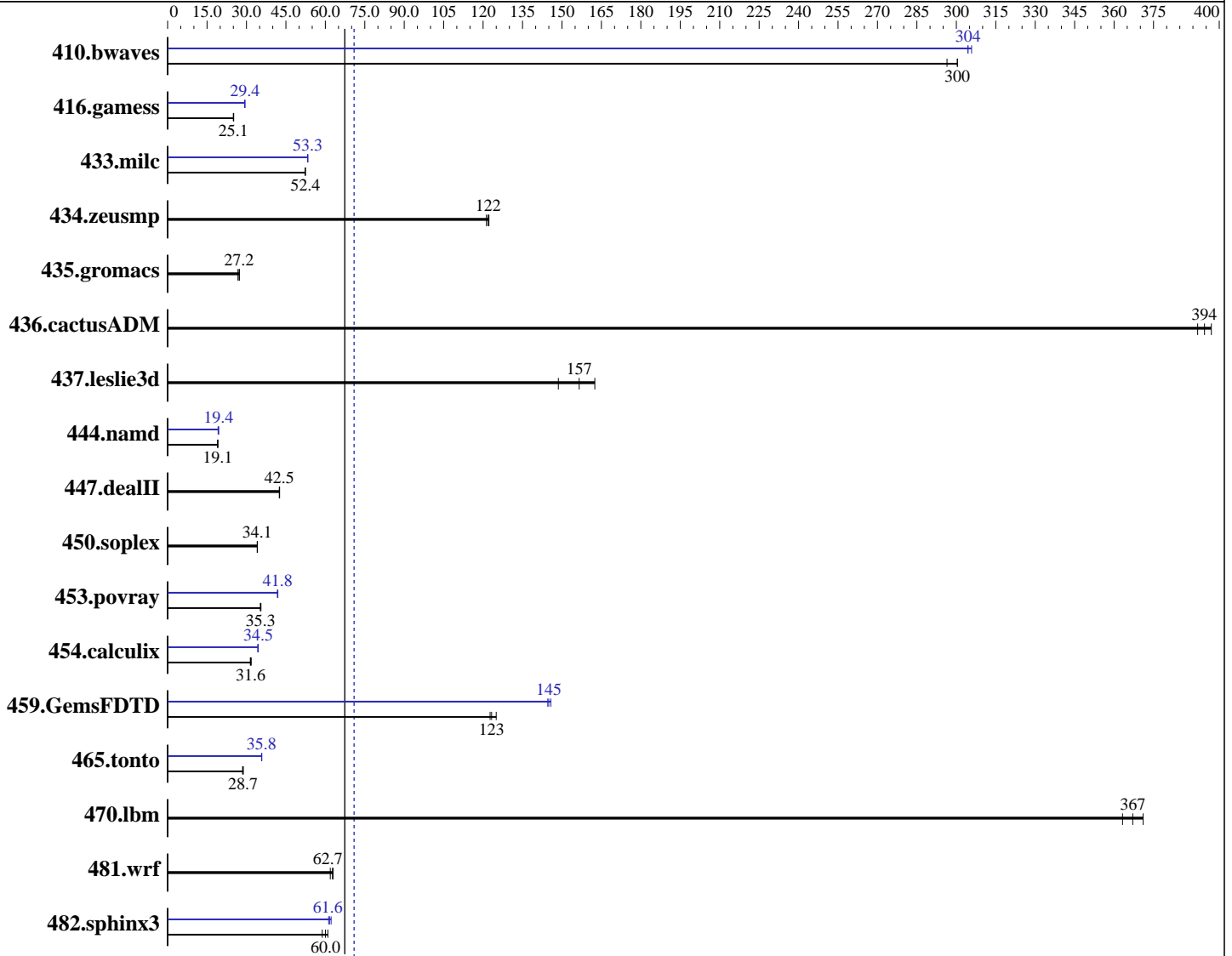
Test date: May-2012

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2012

Tested by: Hewlett-Packard Company

Software Availability: Mar-2012



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### Hardware

CPU Name: Intel Xeon E5-2630  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2, Kernel 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.2.273 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)

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L3 Cache: 15 MB I+D on chip per chip  
Other Cache: None  
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz and CL9)  
Disk Subsystem: 2 x 146 GB 15 K SAS, RAID 1  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: HP Array Configuration Utility, CLI version

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	45.8	296	<b>45.2</b>	<b>300</b>	45.2	300	44.4	306	<b>44.6</b>	<b>304</b>	44.6	304
416.gamess	<b>782</b>	<b>25.1</b>	783	25.0	779	25.1	666	29.4	668	29.3	<b>666</b>	<b>29.4</b>
433.milc	<b>175</b>	<b>52.4</b>	175	52.4	175	52.3	172	53.3	<b>172</b>	<b>53.3</b>	172	53.3
434.zeusmp	74.5	122	75.1	121	<b>74.7</b>	<b>122</b>	74.5	122	75.1	121	<b>74.7</b>	<b>122</b>
435.gromacs	267	26.8	<b>262</b>	<b>27.2</b>	262	27.3	267	26.8	<b>262</b>	<b>27.2</b>	262	27.3
436.cactusADM	30.1	397	30.5	392	<b>30.3</b>	<b>394</b>	30.1	397	30.5	392	<b>30.3</b>	<b>394</b>
437.leslie3d	<b>60.0</b>	<b>157</b>	57.8	162	63.2	149	<b>60.0</b>	<b>157</b>	57.8	162	63.2	149
444.namd	421	19.1	420	19.1	<b>421</b>	<b>19.1</b>	<b>414</b>	<b>19.4</b>	413	19.4	418	19.2
447.dealII	269	42.5	268	42.6	<b>269</b>	<b>42.5</b>	269	42.5	268	42.6	<b>269</b>	<b>42.5</b>
450.soplex	245	34.1	<b>244</b>	<b>34.1</b>	244	34.1	245	34.1	<b>244</b>	<b>34.1</b>	244	34.1
453.povray	<b>151</b>	<b>35.3</b>	150	35.5	151	35.3	127	41.8	<b>127</b>	<b>41.8</b>	127	41.9
454.calculix	259	31.8	262	31.5	<b>261</b>	<b>31.6</b>	<b>239</b>	<b>34.5</b>	241	34.3	239	34.5
459.GemsFDTD	84.9	125	<b>86.1</b>	<b>123</b>	86.5	123	72.8	146	73.4	145	<b>73.2</b>	<b>145</b>
465.tonto	<b>343</b>	<b>28.7</b>	345	28.5	342	28.8	275	35.8	275	35.7	<b>275</b>	<b>35.8</b>
470.lbm	<b>37.4</b>	<b>367</b>	37.0	371	37.8	363	<b>37.4</b>	<b>367</b>	37.0	371	37.8	363
481.wrf	181	61.9	<b>178</b>	<b>62.7</b>	178	62.9	181	61.9	<b>178</b>	<b>62.7</b>	178	62.9
482.sphinx3	331	58.8	<b>325</b>	<b>60.0</b>	320	60.9	<b>316</b>	<b>61.6</b>	318	61.3	313	62.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
Drive Write Cache set to Enabled in HP Array Configuration Utility, CLI version
Accelerator Ratio for Reads/Writes set to = 100% Read / 0% Write in HP Array Configuration Utility, CLI version
```



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### Platform Notes

Sysinfo program /cpu2006/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 # \$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on DL380G8-3 Tue Aug 21 11:21:47 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2630 0 @ 2.30GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 6
  siblings  : 12
  physical 0: cores 0 1 2 3 4 5
  physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      132119936 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux DL380G8-3 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Aug 17 13:37 last=5
```

```
SPEC is set to: /cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_rh62-lv_root
                ext4      50G   8.9G   38G  19% /
```

```
Additional information from dmidecode:
BIOS HP P70 08/06/2012
Memory:
16x HP Not Specified 8 GB 1600 MHz 2 rank
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64"
OMP_NUM_THREADS = "12"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

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## Base Optimization Flags (Continued)

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel`

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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