



# SPEC<sup>®</sup> CINT2006 Result

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## Fujitsu

**SPECint<sup>®</sup>\_rate2006 = 58.0**

PRIMERGY TX120 S3p, Intel Celeron G550, 2.60 GHz

**SPECint\_rate\_base2006 = 55.9**

CPU2006 license: 19

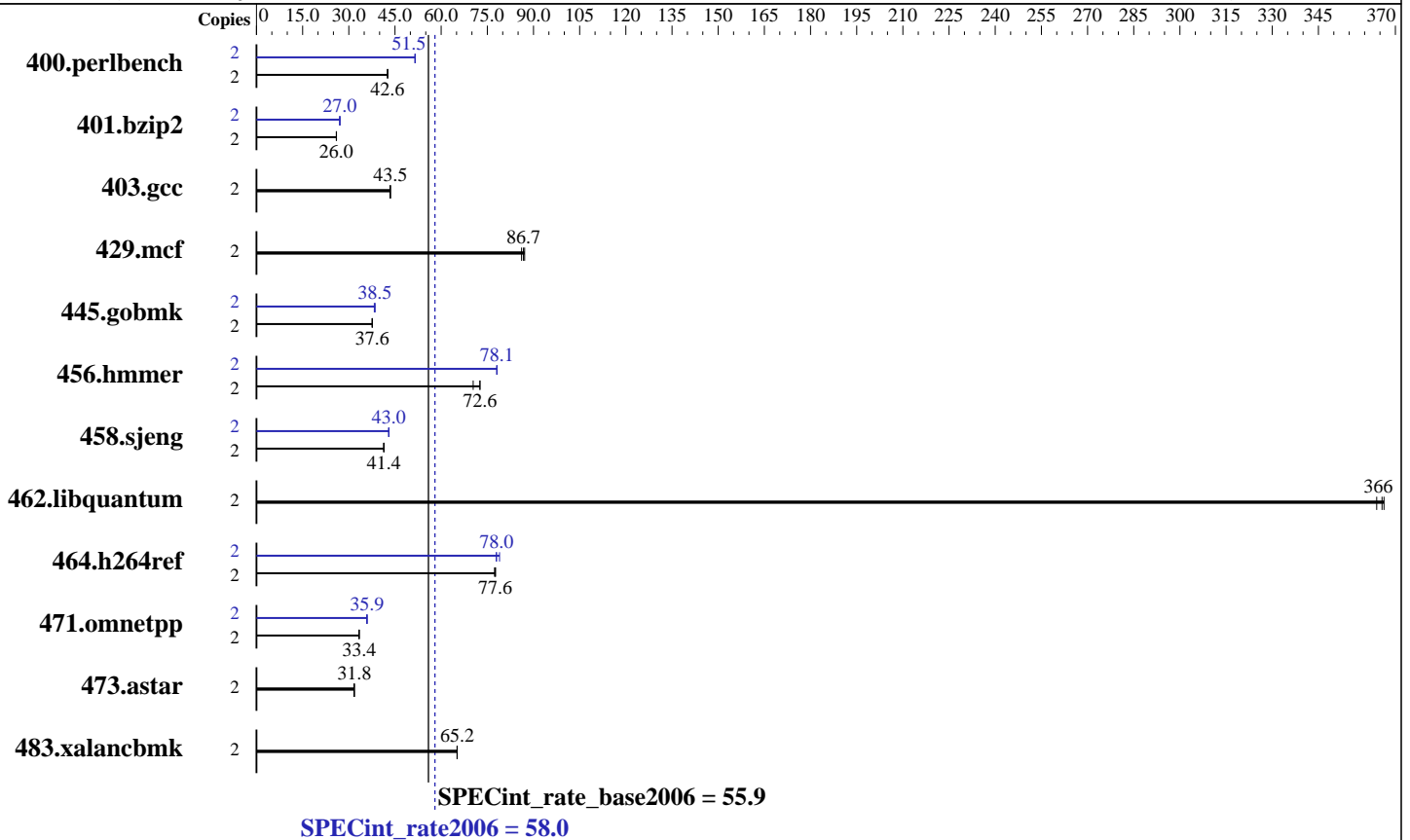
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jun-2012

Hardware Availability: Jun-2012

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Celeron G550  
 CPU Characteristics:  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 2 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC, running at 1067 MHz and CL7)  
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	459	42.6	<b>459</b>	<b>42.6</b>	459	42.6	2	379	51.6	379	51.5	<b>379</b>	<b>51.5</b>
401.bzip2	2	744	25.9	<b>743</b>	<b>26.0</b>	742	26.0	2	<b>714</b>	<b>27.0</b>	711	27.2	714	27.0
403.gcc	2	<b>370</b>	<b>43.5</b>	370	43.6	371	43.4	2	<b>370</b>	<b>43.5</b>	370	43.6	371	43.4
429.mcf	2	212	86.1	<b>210</b>	<b>86.7</b>	209	87.1	2	212	86.1	<b>210</b>	<b>86.7</b>	209	87.1
445.gobmk	2	557	37.6	<b>558</b>	<b>37.6</b>	558	37.6	2	545	38.5	<b>546</b>	<b>38.5</b>	548	38.3
456.hammer	2	265	70.4	<b>257</b>	<b>72.6</b>	257	72.6	2	<b>239</b>	<b>78.1</b>	239	78.2	239	78.1
458.sjeng	2	584	41.4	<b>585</b>	<b>41.4</b>	585	41.3	2	563	43.0	<b>563</b>	<b>43.0</b>	563	43.0
462.libquantum	2	<b>113</b>	<b>366</b>	114	364	113	366	2	<b>113</b>	<b>366</b>	114	364	113	366
464.h264ref	2	<b>571</b>	<b>77.6</b>	572	77.3	570	77.7	2	<b>568</b>	<b>78.0</b>	568	77.9	560	79.0
471.omnetpp	2	374	33.4	376	33.3	<b>375</b>	<b>33.4</b>	2	<b>348</b>	<b>35.9</b>	348	36.0	349	35.8
473.astar	2	<b>442</b>	<b>31.8</b>	443	31.7	441	31.8	2	<b>442</b>	<b>31.8</b>	443	31.7	441	31.8
483.xalancbmk	2	212	65.2	<b>212</b>	<b>65.2</b>	212	65.1	2	212	65.2	<b>212</b>	<b>65.2</b>	212	65.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

Binaries compiled on a system with 1x E3-1270v2 CPU + 32 GB memory using RHEL6.2  
For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
400.perlbench: icc -m64  
401.bzip2: icc -m64  
456.hmmer: icc -m64  
458.sjeng: icc -m64  
C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/opt/SmartHeap/lib -lsmarheap

473.astar: basepeak = yes

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Thu Jul 24 10:23:36 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 11 September 2012.