



# SPEC® CINT2006 Result

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## Fujitsu

**SPECint®2006 = 34.8**

PRIMERGY TX150 S8, Intel Pentium 1403, 2.60 GHz

**SPECint\_base2006 = 32.9**

CPU2006 license: 19

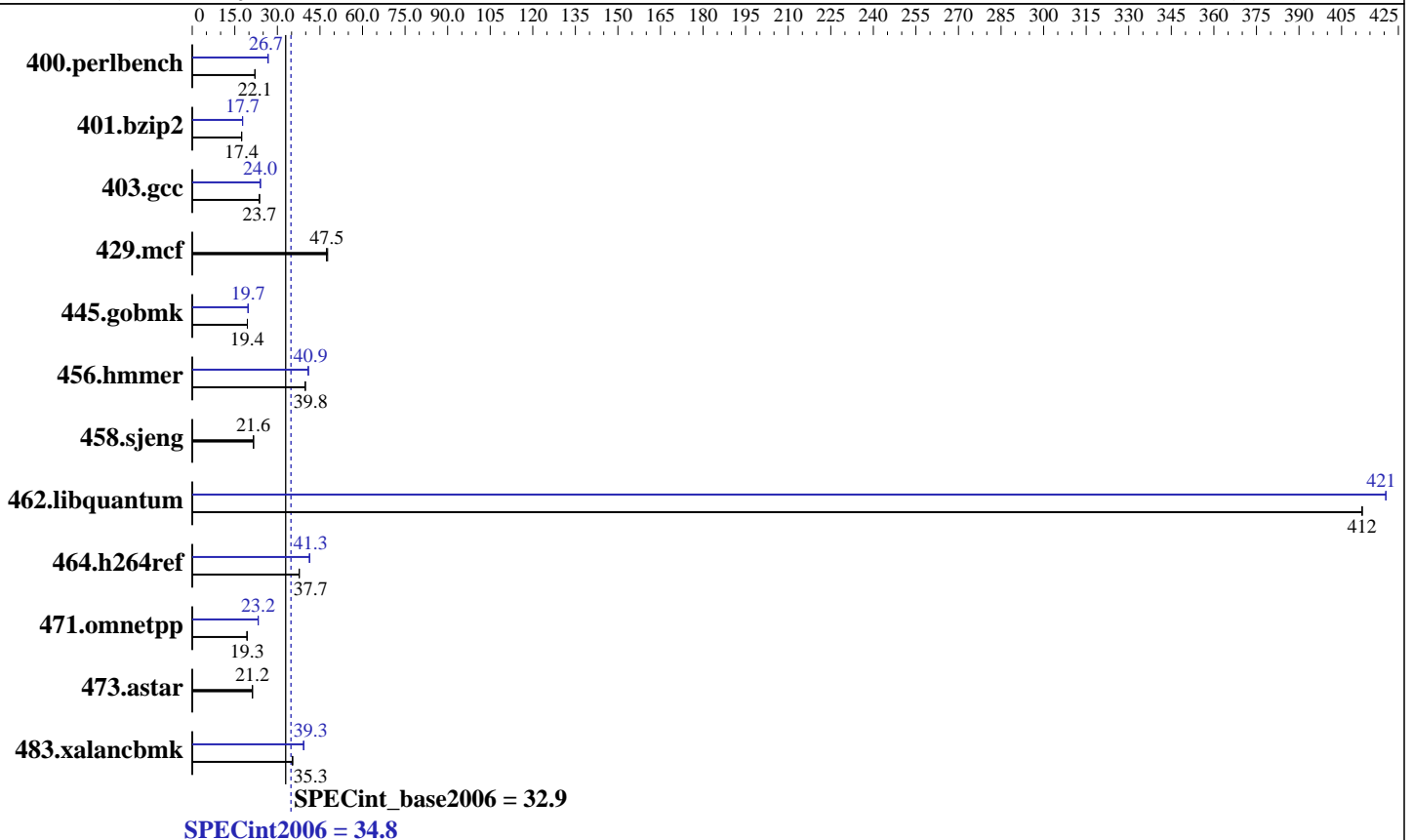
Test date: Jun-2012

Test sponsor: Fujitsu

Hardware Availability: Jul-2012

Tested by: Fujitsu

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Pentium 1403  
 CPU Characteristics:  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 5 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1067 MHz and CL7)  
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>442</u></b>	<b><u>22.1</u></b>	441	22.1	442	22.1	366	26.7	366	26.7	<b><u>366</u></b>	<b><u>26.7</u></b>
401.bzip2	<b><u>554</u></b>	<b><u>17.4</u></b>	555	17.4	554	17.4	<b><u>544</u></b>	<b><u>17.7</u></b>	544	17.7	544	17.8
403.gcc	340	23.7	340	23.6	<b><u>340</u></b>	<b><u>23.7</u></b>	336	24.0	335	24.0	<b><u>335</u></b>	<b><u>24.0</u></b>
429.mcf	193	47.3	<b><u>192</u></b>	<b><u>47.5</u></b>	191	47.7	193	47.3	<b><u>192</u></b>	<b><u>47.5</u></b>	191	47.7
445.gobmk	<b><u>540</u></b>	<b><u>19.4</u></b>	540	19.4	540	19.4	533	19.7	533	19.7	<b><u>533</u></b>	<b><u>19.7</u></b>
456.hammer	234	39.9	234	39.8	<b><u>234</u></b>	<b><u>39.8</u></b>	<b><u>228</u></b>	<b><u>40.9</u></b>	228	40.9	228	40.9
458.sjeng	561	21.6	<b><u>561</u></b>	<b><u>21.6</u></b>	561	21.6	561	21.6	<b><u>561</u></b>	<b><u>21.6</u></b>	561	21.6
462.libquantum	50.3	412	<b><u>50.3</u></b>	<b><u>412</u></b>	50.3	412	49.3	421	<b><u>49.3</u></b>	<b><u>421</u></b>	49.3	421
464.h264ref	587	37.7	588	37.7	<b><u>587</u></b>	<b><u>37.7</u></b>	534	41.4	536	41.3	<b><u>535</u></b>	<b><u>41.3</u></b>
471.omnetpp	322	19.4	<b><u>323</u></b>	<b><u>19.3</u></b>	324	19.3	269	23.2	<b><u>269</u></b>	<b><u>23.2</u></b>	268	23.3
473.astar	331	21.2	<b><u>331</u></b>	<b><u>21.2</u></b>	331	21.2	331	21.2	<b><u>331</u></b>	<b><u>21.2</u></b>	331	21.2
483.xalancbmk	<b><u>195</u></b>	<b><u>35.3</u></b>	196	35.3	195	35.4	176	39.3	<b><u>176</u></b>	<b><u>39.3</u></b>	176	39.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Platform Notes

BIOS configuration:  
Intel HT Technology = Disable  
Frequency Floor Override = Enable

## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"  
OMP\_NUM\_THREADS = "2"

Binaries compiled on a system with 1x E3-1270v2 CPU + 32 GB  
memory using RHEL6.2  
For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/opt/SmartHeap/lib64 -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

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## Peak Compiler Invocation (Continued)

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
 -opt-prefetch -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -ansi-alias

458.sjeng: basepeak = yes

462.libquantum: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch  
 -auto-p32

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## Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 11:04:28 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 29 August 2012.