



# SPEC® CFP2006 Result

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## IBM Corporation

**SPECfp®2006 = 65.1**

IBM System x3100 M4 (Intel Xeon E3-1265L v2)

**SPECfp\_base2006 = 62.9**

CPU2006 license: 11

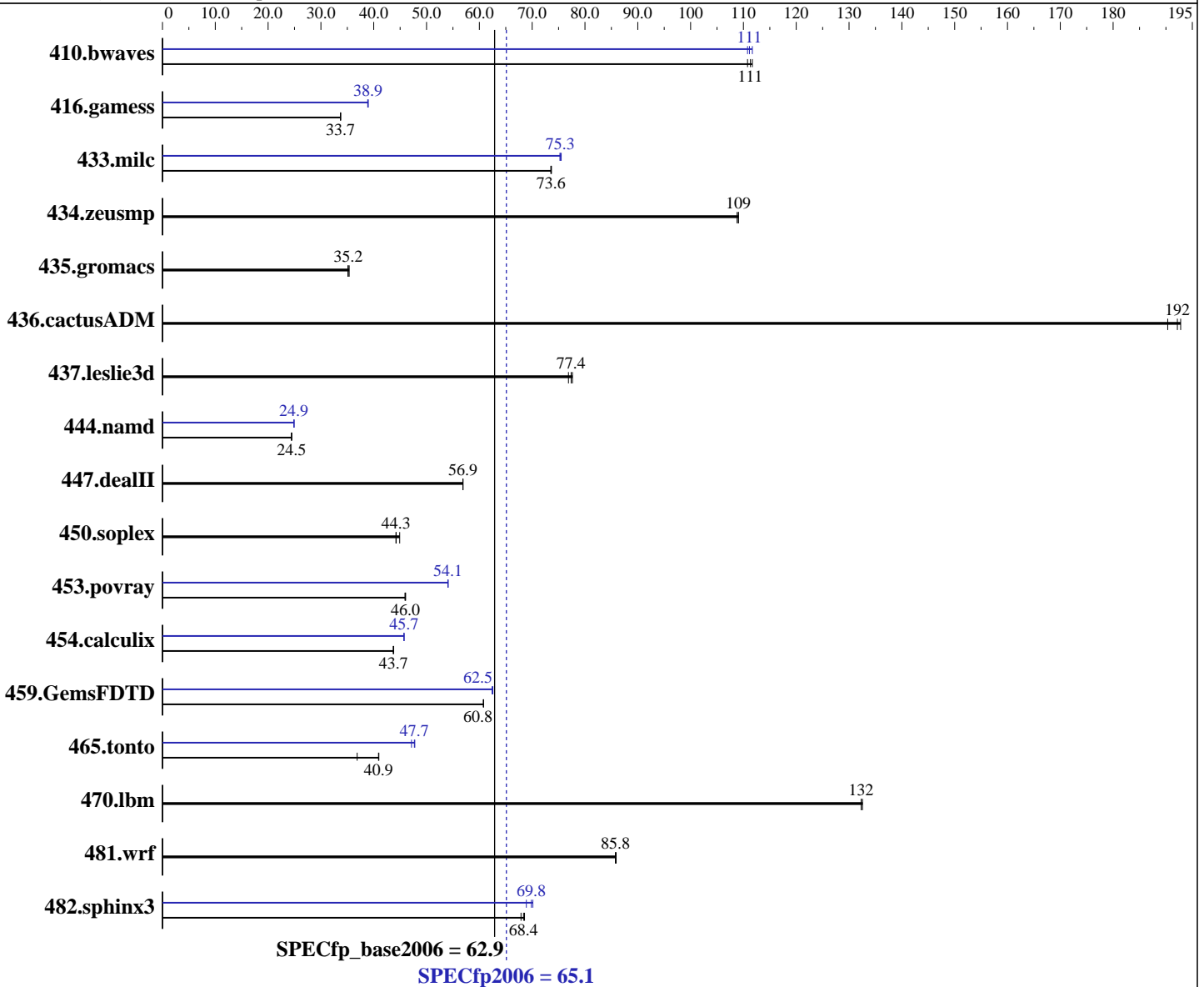
Test date: Jul-2012

Test sponsor: IBM Corporation

Hardware Availability: May-2012

Tested by: IBM Corporation

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E3-1265L v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

Continued on next page



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L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)  
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<b><u>122</u></b>	<b><u>111</u></b>	123	111	122	112	122	112	<b><u>122</u></b>	<b><u>111</u></b>	123	111
416.gamess	<b><u>581</u></b>	<b><u>33.7</u></b>	580	33.7	581	33.7	<b><u>503</u></b>	<b><u>38.9</u></b>	504	38.9	503	38.9
433.milc	125	73.6	125	73.7	<b><u>125</u></b>	<b><u>73.6</u></b>	<b><u>122</u></b>	<b><u>75.3</u></b>	122	75.5	122	75.3
434.zeusmp	83.6	109	<b><u>83.4</u></b>	<b><u>109</u></b>	83.4	109	83.6	109	<b><u>83.4</u></b>	<b><u>109</u></b>	83.4	109
435.gromacs	<b><u>203</u></b>	<b><u>35.2</u></b>	203	35.1	202	35.4	<b><u>203</u></b>	<b><u>35.2</u></b>	203	35.1	202	35.4
436.cactusADM	62.0	193	<b><u>62.2</u></b>	<b><u>192</u></b>	62.8	190	62.0	193	<b><u>62.2</u></b>	<b><u>192</u></b>	62.8	190
437.leslie3d	<b><u>121</u></b>	<b><u>77.4</u></b>	121	77.6	122	76.9	<b><u>121</u></b>	<b><u>77.4</u></b>	121	77.6	122	76.9
444.namd	329	24.4	<b><u>328</u></b>	<b><u>24.5</u></b>	328	24.5	322	24.9	<b><u>322</u></b>	<b><u>24.9</u></b>	322	24.9
447.dealII	201	56.9	<b><u>201</u></b>	<b><u>56.9</u></b>	201	56.9	201	56.9	<b><u>201</u></b>	<b><u>56.9</u></b>	201	56.9
450.soplex	186	44.9	189	44.2	<b><u>188</u></b>	<b><u>44.3</u></b>	186	44.9	189	44.2	<b><u>188</u></b>	<b><u>44.3</u></b>
453.povray	116	45.9	<b><u>116</u></b>	<b><u>46.0</u></b>	116	46.0	<b><u>98.4</u></b>	<b><u>54.1</u></b>	98.5	54.0	98.4	54.1
454.calculix	<b><u>189</u></b>	<b><u>43.7</u></b>	188	43.8	189	43.7	180	45.8	180	45.7	<b><u>180</u></b>	<b><u>45.7</u></b>
459.GemsFDTD	175	60.7	174	60.8	<b><u>174</u></b>	<b><u>60.8</u></b>	170	62.4	<b><u>170</u></b>	<b><u>62.5</u></b>	170	62.6
465.tonto	<b><u>241</u></b>	<b><u>40.9</u></b>	267	36.9	240	41.0	206	47.8	209	47.2	<b><u>206</u></b>	<b><u>47.7</u></b>
470.lbm	104	132	<b><u>104</u></b>	<b><u>132</u></b>	104	133	104	132	<b><u>104</u></b>	<b><u>132</u></b>	104	133
481.wrf	<b><u>130</u></b>	<b><u>85.8</u></b>	130	85.9	130	85.8	<b><u>130</u></b>	<b><u>85.8</u></b>	130	85.9	130	85.8
482.sphinx3	284	68.6	<b><u>285</u></b>	<b><u>68.4</u></b>	287	67.9	<b><u>279</u></b>	<b><u>69.8</u></b>	283	68.9	278	70.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
 Turbo Mode enabled in BIOS  
 C-State enabled in BIOS  
 Sysinfo program /root/SPECcpul.2/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on localhost.localdomain Wed Jul 11 09:08:43 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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CPU2006 license: 11

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Hardware Availability: May-2012

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## Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1265L V2 @ 2.50GHz
 1 "physical id"s (chips)
 8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 8
  physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal:      16322724 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux localhost.localdomain 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13
EST 2011 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 9 14:49

SPEC is set to: /root/SPECcpul.2
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
                ext4      50G   45G   2.7G  95% /

Additional information from dmidecode:
Memory:
  2x Micron 18JSF1G72AZ-1G6D1 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)

```

## General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/SPECcpul.2/libs/32:/root/SPECcpul.2/libs/64"
OMP_NUM_THREADS = "4"

```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

Continued on next page



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## General Notes (Continued)

memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Continued on next page



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## Base Optimization Flags (Continued)

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

Continued on next page



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## Peak Optimization Flags (Continued)

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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